

CONNETTORI PWA CPU

CONNECTORS PWA CPU

J 01 (TEST CONNECTOR)		
PIH	SIGNAL	ADD.
1	MEMR * 0	50-18
2	HOLD * 1	52-1
3	RESET * 00	51-17
4	HLDA * 1	52-1
5	AB14 * 1	52-1
6	IHTA * 0	52-5
7	AB13 * 1	52-1
8	EXREA * 1	52-1
9	AB12 * 1	52-1
10	ENBUS * 1	52-12
11	AB15 * 1	52-1
12	EXCLØ * 1	51-13
13	EIHT * 1	51-31
14	MEMW * 0	50-18
15	STRB * 0	51-17
16	ABCLE * 0	51-15
17	BUSEH * 0	52-5
18	IØW * 0	52-5
19	AB10 * 1	52-1
20	AB11 * 1	52-1
21	AB9 * 1	52-1
22	AB8 * 1	52-1
23	IØR * 0	52-5
24	DS00Ø * 0	52-2
25	DB6 * 1	52-9
26	DB7 * 1	52-9
27	DB5 * 1	52-9
28	DB4 * 1	52-9
29	DB3 * 1	52-9
30	DB2 * 1	52-9
31	DB1 * 1	52-9
32	DB0 * 1	52-9
33	AB4 * 1	52-1
34	AB7 * 1	52-1
35	AB6 * 1	52-1
36	AB5 * 1	52-1
37	AB3 * 1	52-1
38	AB2 * 1	52-1
39	AB1 * 1	52-1
40	AB0 * 1	52-1

J 02 (KEYBOARD)		
PIH	SIGNAL	ADD.
1	-12V	
2	KB7 * 0	53-21
3	KB6 * 0	53-21
4	GROUND	
5	GROUND	
6	REPEA * 0	54-56
7	CTR * 1	53-16
8	STRØB * 0	54-55
9	KB3 * 0	53-21
10	KB4 * 0	53-21
11	-15V	
12	+15V	
13	KB5 * 0	53-21
14	KB1 * 0	53-21
15	KB2 * 0	53-21
16	KB0 * 0	53-21

J 03 (OPERATOR PANEL)		
PIH	SIGNAL	ADD.
1	+5V	
2	DS05Ø * 0	52-2
3	GROUND	
4	FI2TT * 1	51-17
5	120 * 0	51-22
6	STØP * 10	53-27
7	TEST * 10	53-32
8	ST1 * 10	51-10
9	DS06Ø * 0	52-2
10	START * 10	53-24
11	DISCØ * 10	53-30
12	CALL * 1	53-6
13	STABY * 1	53-4
14	DSRL * 1	53-11
15	LØCAL * 1	53-18
16	BUSY * 1	53-8
17	ERROR * 1	53-14
18	GROUND	

J 04 (SYNCHRONOUS MODEM INTERFACE)		
PIH	SIGNAL	ADD.
1	-12V	
2	LRELI * 1	
3	SSTBY * 110	54-34
4	+5V	
5	LDR * 00	54-1
6	LRTS * 1	54-21
7	LDTR * 1	54-35
8	+12V	
9	LTRXC * 1	54-37
10	GROUND	
11	LTD * 0	54-11
12	LDARA * 1	54-30
13	LCTS * 10	54-19
14	LDSR * 1	54-22
15	LTXC * 10	54-42
16	LRXC * 10	54-24

J 05 (ASYNCHRONOUS MODEM INTERFACE)		
PIH	SIGNAL	ADD.
1	-12V	
2	LRD * 00	54-1
3	LCALI * 1	54-45
4	LSTD * 0	54-15
5	LSCTS * 10	54-13
6	LSRTS * 1	54-17
7	LDTR * 1	54-35
8	LRTS * 1	54-21
9	LDSR * 1	54-22
10	LTD * 0	54-11
11	GROUND	
12	LSRD * 00	54-8
13	+12V	
14	DASTC * 1	54-53
15	LCTS * 10	54-19
16	LRELI * 1	54-36

J 06		
PIH	SIGNAL	ADD.
1	SOHAL * 0	53-3
2	PR4 * 1	51-29
3	AB11 * 0	52-3
4	FIVIS * 1	53-28
5	IØWR * 0	53-33
6	PRO * 0	51-25
7	PR3 * 0	51-25
8	AB13 * 1	52-1
9	AB12 * 1	52-1
10	AB14 * 1	52-1
11	PR7 * 1	51-38
12	PAPEH * 1	53-16
13	SØL * 00	53-17
14	EØL * 00	53-10
15	AB15 * 1	52-1
16	PR1 * 1	51-25
17	100KH * 0	51-9
18	PR6 * 1	51-36

J 07		
PIH	SIGNAL	ADD.
1	-12V	
2	RESET * 0	
3	-15V	
4	DBR7 * 1	52-13
5	DBR3 * 1	52-10
6	DBR1 * 1	52-10
7	DBR0 * 1	52-10
8	DBR2 * 1	52-10
9	+12V	
10	+12V	
11	DBR6 * 1	52-13
12	DBR5 * 1	52-13
13	DS06I * 0	52-6
14	ØRDS * 0	52-15
15	DBR4 * 1	52-13
16	DS05I * 0	52-6
17	MR1 * 1	51-24
18	-5V	

J 19 (POWER SUPPLY)		
PIH	SIGNAL	ADD.
1	+5V	
2	GROUND	

USO INTERNO. TUTTA LA DISTRIBUZIONE A TERZI È VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

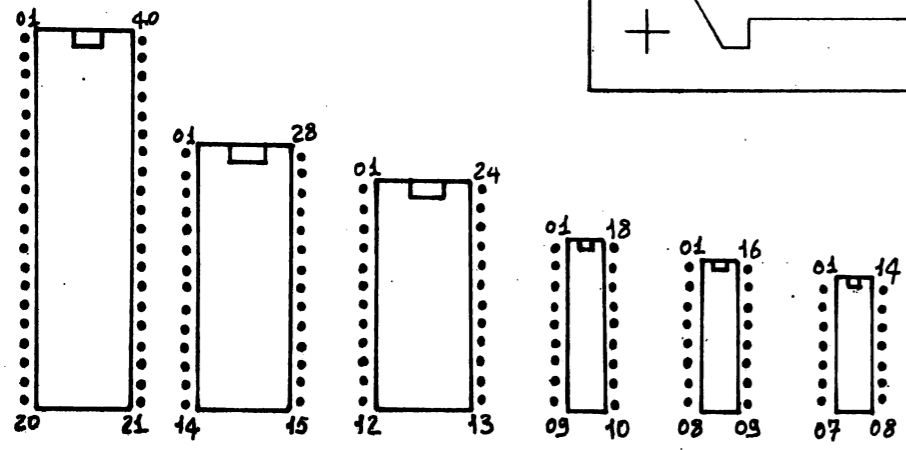
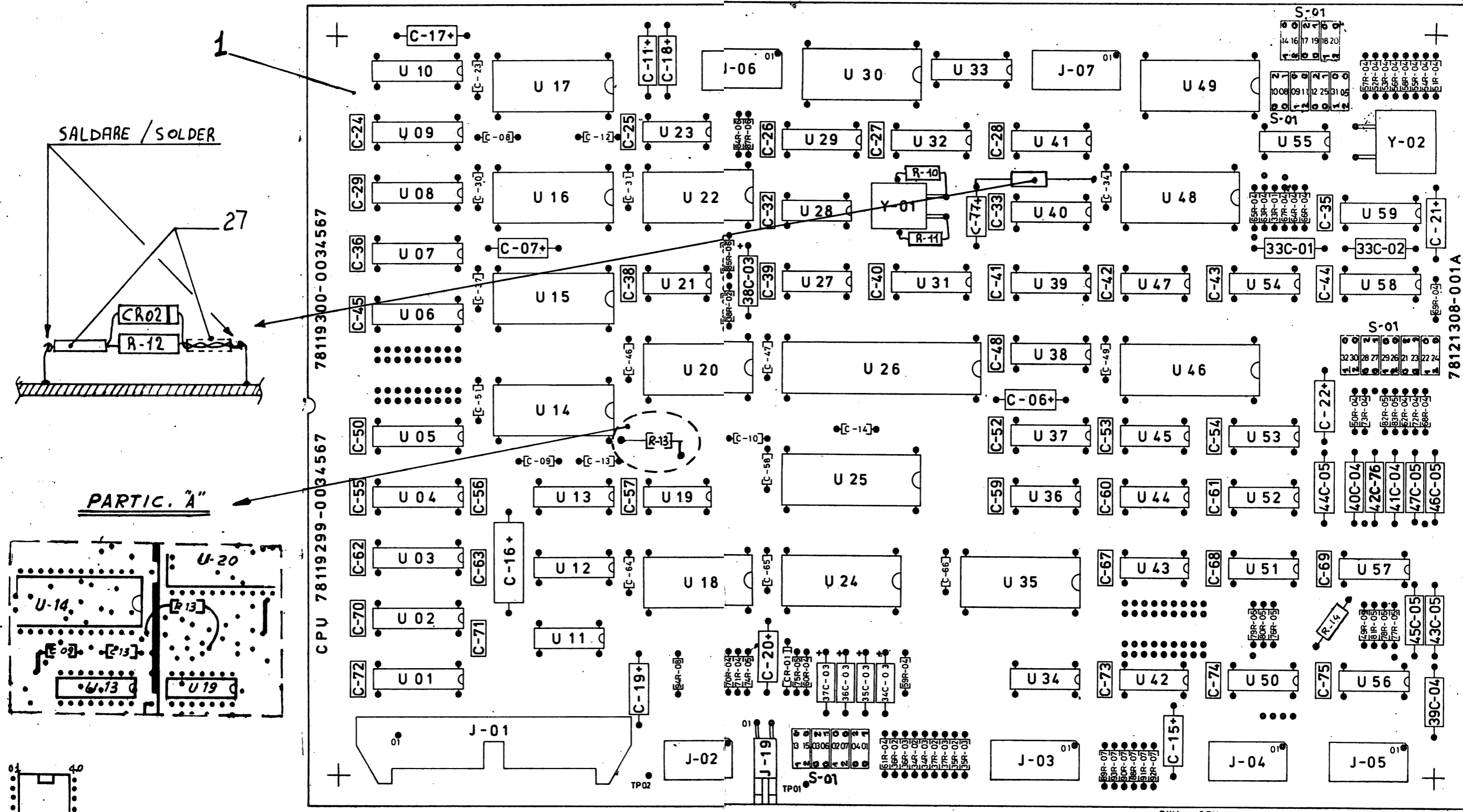
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Honeywell

F.T.O. DISEGNO
B 78119299

PAG. REV
5 FA

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.



005 004

PWA-CPU00
Honeywell

F.T.O	DISEGNO	PAG.	REV
B	78119300	4	VA

PWA CPU

CONNETTORI PWA CPU

CONNECTORS PWA CPU

J 01 (TEST CONNECTOR)		
PIN	SIGNAL	ADD.
1	MEMR	50-18
2	HOLD	52-1
3	RESET	51-17
4	HLDA	52-1
5	AB14	52-1
6	IHTA	52-5
7	AB13	52-1
8	EXREA	52-1
9	AB12	52-1
10	ENBUS	52-12
11	AB15	52-1
12	EXCLD	51-13
13	EIHT	51-31
14	MENW	50-18
15	STRB	51-17
16	ABCLE	51-15
17	BUSEH	52-5
18	IOW	52-5
19	AB10	52-1
20	AB11	52-1
21	AB9	52-1
22	ABB	52-1
23	IOR	52-5
24	DS000	52-2
25	DB6	52-9
26	DB7	52-9
27	DB5	52-9
28	DB4	52-9
29	DB3	52-9
30	DB2	52-9
31	DB1	52-9
32	DB0	52-9
33	AB4	52-1
34	AB7	52-1
35	AB6	52-1
36	AB5	52-1
37	AB3	52-1
38	AB1	52-1
39	AB2	52-1
40	AB0	52-1

J 02 (KEYBOARD)		
PIN	SIGNAL	ADD.
1	-12V	53-21
2	KB7	53-21
3	KB6	53-21
4	GROUND	
5	GROUND	54-56
6	REPEA	53-16
7	CTR	54-55
8	STR0B	53-21
9	KB3	53-21
10	KB4	53-21
11	-15V	53-21
12	+15V	
13	KB5	53-21
14	KB1	53-21
15	KB2	53-21
16	KB0	53-21

J 03 (OPERATOR PANEL)		
PIN	SIGNAL	ADD.
1	+5V	52-2
2	DS050	52-2
3	GROUND	
4	FI2TT	51-17
5	120	51-22
6	ST0P	53-27
7	TEST	53-32
8	ST1	51-10
9	DS060	52-2
10	START	53-24
11	DISC0	53-30
12	CALL	53-6
13	STABY	53-4
14	DSRL	53-11
15	L0CAL	53-18
16	BUSY	53-8
17	ERROR	53-14
18	GROUND	

J 04 (SYNCHRONOUS MODEM INTERFACE)		
PIN	SIGNAL	ADD.
1	-12V	54-34
2	LRELI	54-1
3	SSTBY	54-1
4	+5V	54-1
5	LDR	54-1
6	LRTS	54-21
7	LDTR	54-35
8	+12V	54-35
9	LTRXC	54-37
10	GROUND	
11	LTD	54-11
12	LDARA	54-30
13	LCTS	54-19
14	LDSR	54-22
15	LTXC	54-42
16	LRXC	54-24

J 05 (ASYNCHRONOUS MODEM INTERFACE)		
PIN	SIGNAL	ADD.
1	-12V	54-1
2	LRD	54-45
3	LCALI	54-15
4	LSTD	54-15
5	LSCTS	54-15
6	LSRTS	54-17
7	LDTR	54-35
8	LRTS	54-21
9	LDSR	54-22
10	LTD	54-11
11	GROUND	
12	LSRD	54-8
13	+12V	
14	DASTC	54-53
15	LCTS	54-19
16	LRELI	54-36

J 06		
PIN	SIGNAL	ADD.
1	50HAL	53-3
2	PR4	51-29
3	AB11	52-3
4	FIVIS	53-28
5	IOWR	53-33
6	PRO	51-25
7	PR3	61-25
8	AB13	52-1
9	AB12	52-1
10	AB14	52-1
11	PR7	51-38
12	PAPEH	53-16
13	S0L	53-17
14	EOL	53-10
15	AB15	52-1
16	PR1	51-26
17	100KH	51-9
18	PR6	51-36

J 19 (POWER SUPPLY)		
PIN	SIGNAL	ADD.
1	+5V	
2	GROUND	

J 07		
PIN	SIGNAL	ADD.
1	-12V	
2	RESET	52-13
3	-15V	52-10
4	DBR7	52-10
5	DBR3	52-10
6	DBR1	52-10
7	DBR0	52-10
8	DBR2	52-10
9	+12V	
10	+12V	
11	DBR6	52-13
12	DBR5	52-13
13	DS061	52-6
14	DRDS	52-15
15	DBR4	52-13
16	DS051	52-6
17	MR1	51-24
18	-5V	

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F.T.O. DISEGNO B 78119300

PAG. REV 5 JA

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CONNETTORI PWA DRIVE
CONNECTORS PWA DRIVE

J08		
PIN	SIGNAL	ADD.
1	SIGNAL	0 45-19
2	PR4	* 1 45-6
3	RB4	* 1 46-51
4	FVTS	* 1 44-15
5	TWR	* 0 46-52
6	PRO	* 0 44-10
7	PR3	* 0 46-3
8	RB13	* 1 46-3
9	RB14	* 1 44-4
10	PR7	* 1 44-24
11	PAPER	* 1 44-11
12	SOL	* 0 47-11
13	MBLD	* 0 47-46
14	RB15	* 1 46-41
15	PR1	* 0 46-28
16	PR4	* 0 46-6
17	100KH	* 0 46-6
18	PR6	* 1 46-5

J09		
PIN	SIGNAL	ADD.
1	-12V	* 0
2	RESET	* 0
3	DBR3	* 1 44-21
4	DBR7	* 1 44-29
5	DBR5	* 1 49-29
6	DBR1	* 1 49-29
7	DBR2	* 1 49-29
8	DBR5	* 1 44-21
9	+12	* 1 44-21
10	+12	* 1 44-21
11	DBR6	* 0 44-39
12	DBR5	* 1 44-39
13	D506I	* 0 44-40
14	ØRDS	* 1 44-21
15	DBR4	* 0 44-41
16	DS05I	* 1 46-11
17	MR1	* 0 46-11
18	-5V	* 1 46-11

J10 (VPU-SFU)		
PIN	SIGNAL	ADD.
1	DBR1	* 1 46-2
2	DBR3	* 0 46-27
3	PRO	* 0 46-27
4	DBR0	* 1 46-2
5	DBR6	* 1 46-2
6	DBR2	* 1 46-2
7	DBR4	* 1 46-2
8	DBR5	* 1 46-2
9	+5V	* 1 46-2
10	+19V	* 1 46-2
11	D506I	* 0 44-39
12	DBR7	* 1 46-2
13	IMPPA	* 01
14	RESET	* 0 46-11
15	GROUND	* 0 46-11
16	DSE8Ø	* 0 46-3

J11 (SWITCHES)		
PIN	SIGNAL	ADD.
1	+5V	1 47-27
2	PREND	* 0 47-11
3	SØLØD	* 0
4	EØLØD	* 0
5	IMBUZ	* 0 45-20
6	+8.5	* 0
7	GROUND	* 0
8	GROUND	* 0
9	SPECRA	* 0 47-35
10	MASEN	* 0
11	GROUND	* 0
12	GROUND	* 0 47-14

J12 (power supply)		
PIN	SIGNAL	ADD.
1	+8.5V	
2	+8.5V	
3	+19V	
4	+5V	
5	+5V	
6	GROUND	
7	GROUND	
8	GROUND	

J13 (HEAD CONNECTOR)		
PIN	SIGNAL	ADD.
1	+1ØV	
2	+19V	
3	+19V	
4	ØPRIB	* 0 47-23
5	+19V	
6	+19V	
7	+19V	
8	+19V	
9	ØL11	* 00 47-35
10	ØL12	* 00 47-35
11	ØL13	* 00 47-35
12	GROUND	
13	ØL14	* 00 47-35
14	ØL15	* 00 47-35
15	ØL16	* 00 47-35
16	ØL17	* 00 47-35

J14 (motors)		
PIN	SIGNAL	ADD.
1	-19V	* 1 47-31
2	BACT	* 1 47-37
3	BREAT	* 1 47-20
4	COMØ2	* 1 47-20
5	COMØ1	* 1 47-20
6	COMØ4	* 0 47-24
7	COMØ3	* 1 47-24
8	POCA1	* 1 47-28
9	POCA2	* 1 47-28
10	POCA1	* 1 47-28
11	POCA2	* 1 47-28
12	POPAR	* 1 47-19
13	POPAR	* 1 47-19
14	POPAR	* 1 47-19
15	ØPR2	* 1 47-15
16	ØPR4	* 1 47-15
17	ØPR1	* 1 47-15
18	ØPR3	* 1 47-15

J15 (AFF)		
PIN	SIGNAL	ADD.
1	AFFAR	* 0 47-41
2	10KHZ	* 0 46-7
3	D505I	* 0 44-41
4	PR1	* 00 46-28
5	+8.5V	
6	+8.5V	
7	+5V	
8	+5V	
9	+5V	
10	+5V	
11	S1234	* 1 44-41
12	+19V	
13	+19V	
14	+19V	
15	+19V	
16	+19V	

J16 (AFF)		
PIN	SIGNAL	ADD.
1	DBRØ	* 1 46-2
2	DSC8Ø	* 1 46-2
3	DBR5	* 0 46-2
4	DSØØØ	* 1 46-2
5	DBR2	* 1 46-2
6	-5V	
7	+12V	
8	MR1	* 1 46-12
9	DBR1	* 1 46-2
10	GROUND	
11	GROUND	
12	DBR7	* 1 46-2
13	DBR6	* 1 46-2
14	DBR4	* 1 46-2
15	DBR3	* 1 46-2
16		

INATTIVO
DO NOT USE FOR NEW DESIGN
INATTIVO
NON USARE PER NEU DESIGN

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INTERFACE CONNECTORS - K. DRIVE

Honeywell

F.TO DISEGNO
B 78117135

PAG. REV
12 ZC

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETÀ DELLA HONEYWELL
INFORMAZIONE SUI SISTEMI ITALIANI, ESSO È DA CONSIDERARE COME DOCUMENTO DI
APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.
QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETÀ DELLA HONEYWELL
INFORMAZIONE SUI SISTEMI ITALIANI, ESSO È DA CONSIDERARE COME DOCUMENTO DI
APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

POSIZIONARE GLI STRAPPINGS S-01/S-04 COME IN TABELLA SUL FOGLIO 13

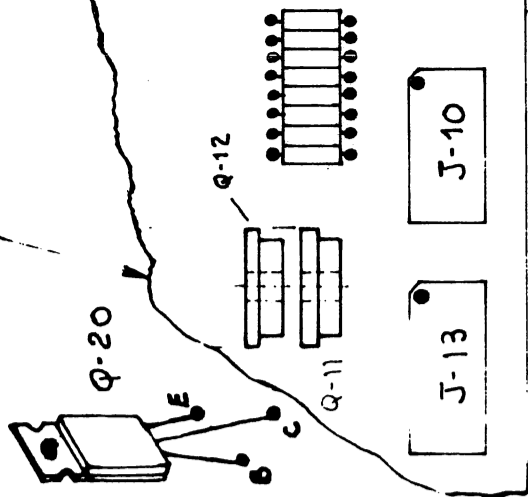
SWITCH THE STRAPPINGS S-01/S-04 AS INDICATED IN TABLE ON SHEET 13

(Q-05 Q-20 Q-24-Q-28) 92 (Q-22 Q-23) 93

INATTIVO
DO NOT USE FOR NEW DESIGN

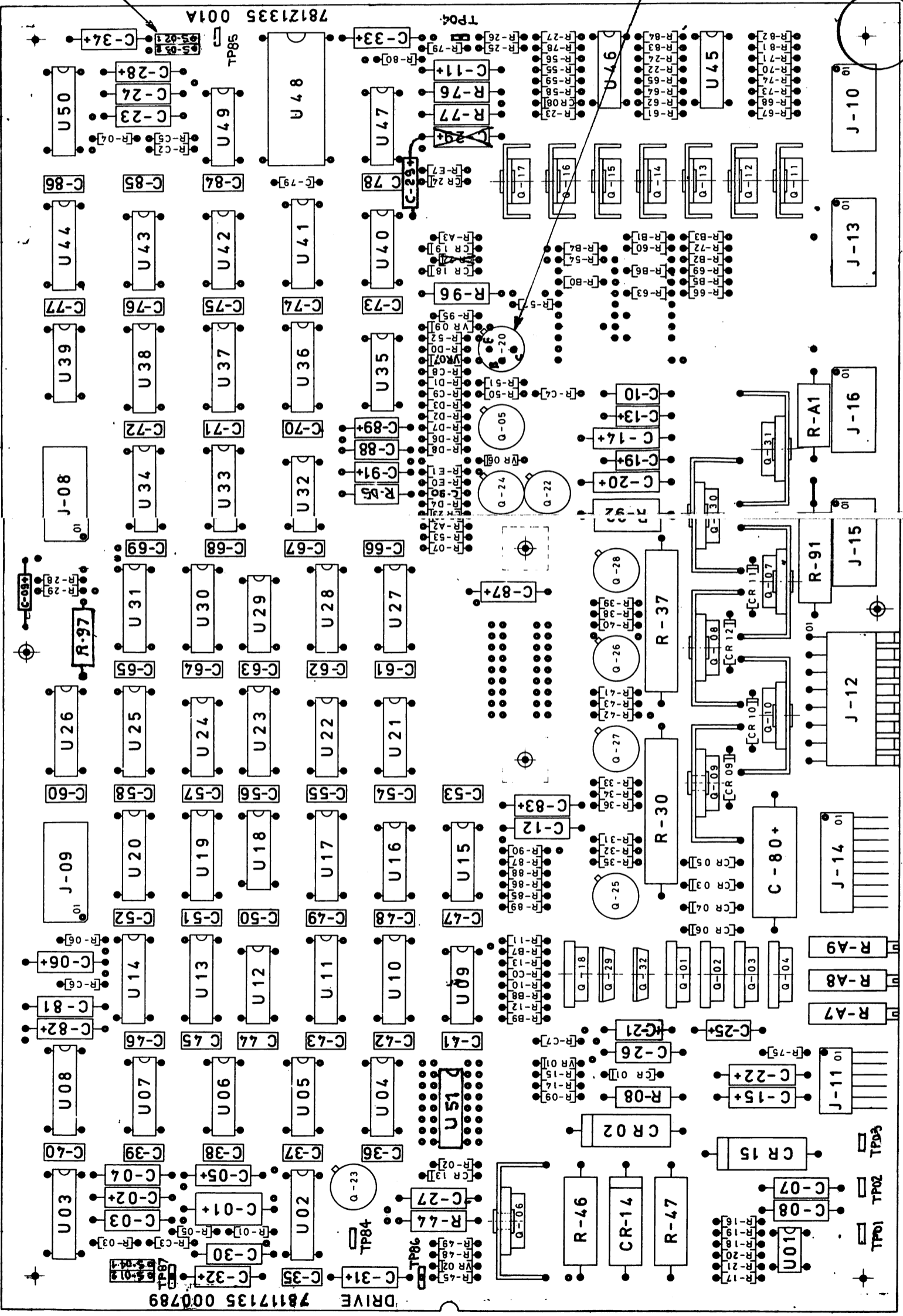
MONTARE IL TRANSISTOR PER L'ALTERNATIVA INDICATA IN PARTICOLARE ITEM 124

MOUNT THE TRANSISTOR IN ALTERNATIVE AS INDICATED IN DETAIL ITEM 124



N°4 ZONE (LATO SALDATURA) ESSENTI DATRACCE DI STAGNO DOVUTE ALLA SALDATURA COMPONENTI.

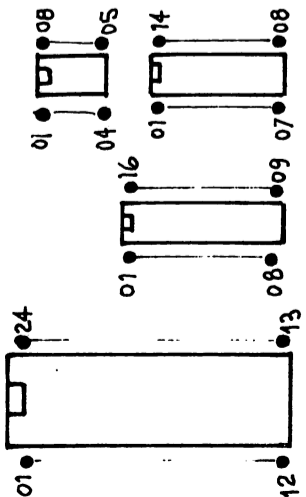
N°4 AREAS (SOLDERING SIDE) WITHOUT TIN RESIDUE, DUE TO COMPONENT SOLDERING

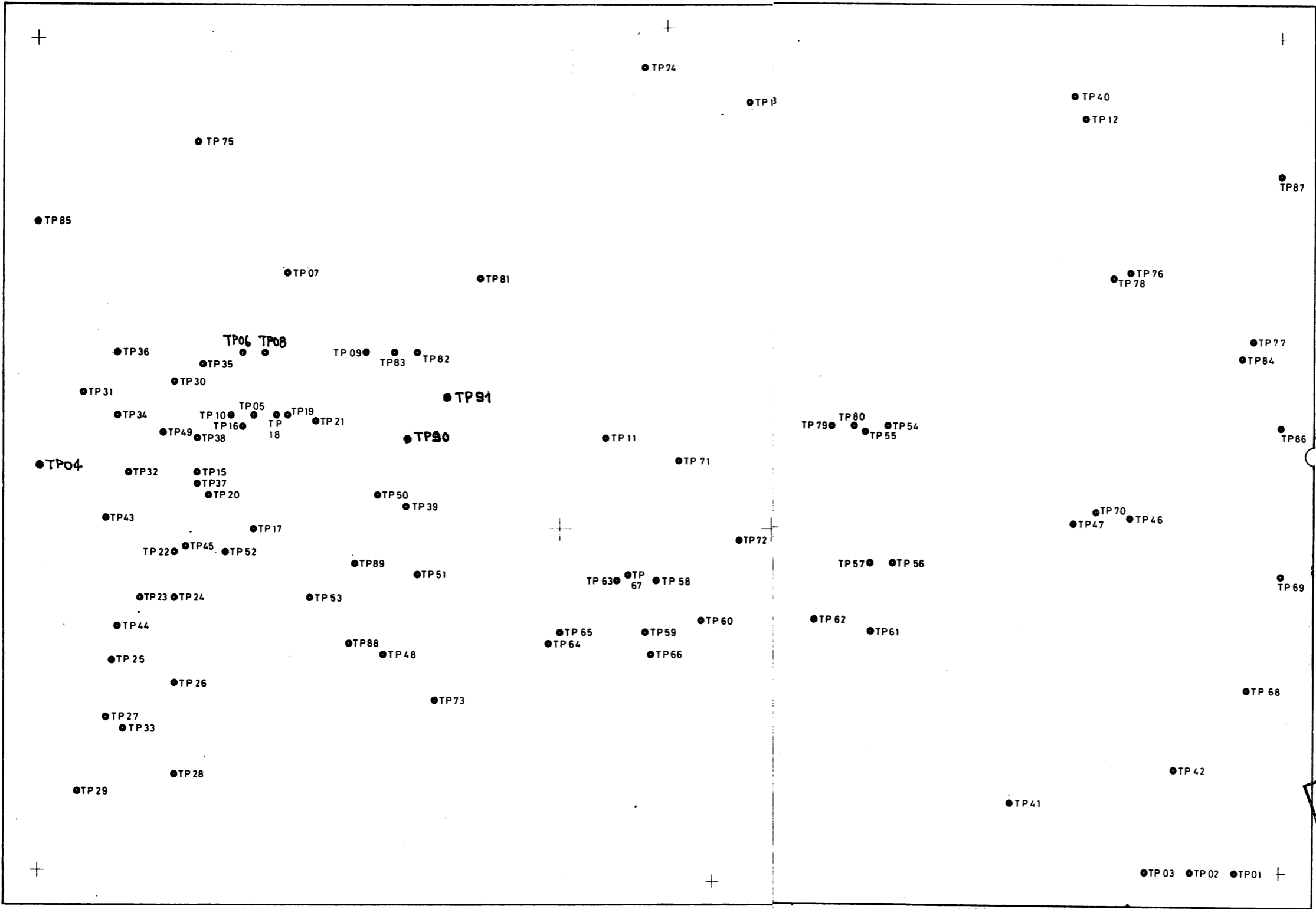


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PWA DRIVE

Honeywell

R-30 R-37 R-91 R-92 R-46 R-47
R-A1 R-E2 R-E3 CR02 CR14 CR15





INACTIVE
 DO NOT USE FOR NEW DESIGN
INATTIVO
 NON USARE PER NUOVI PROGETTI

PWA DRIVE

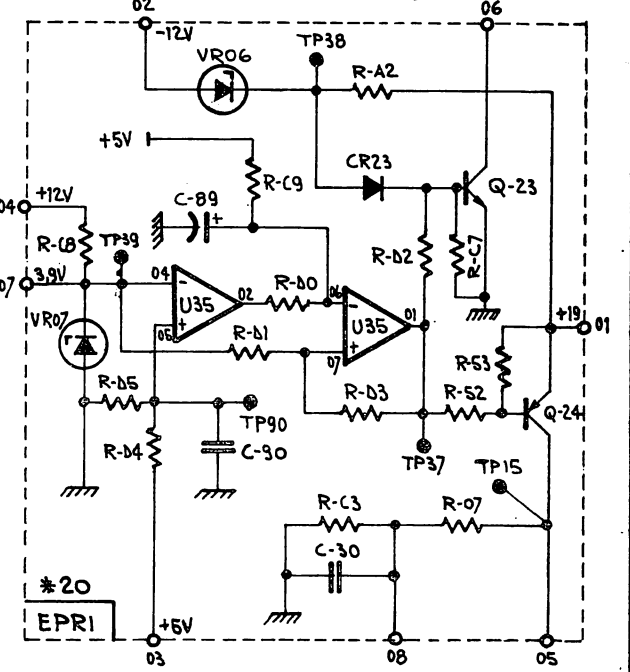
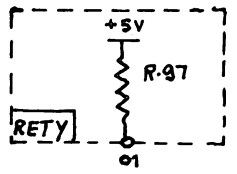
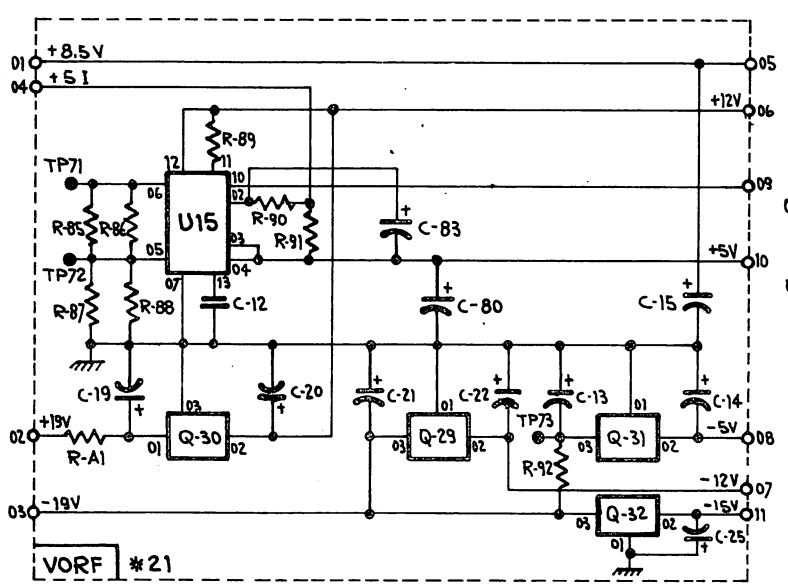
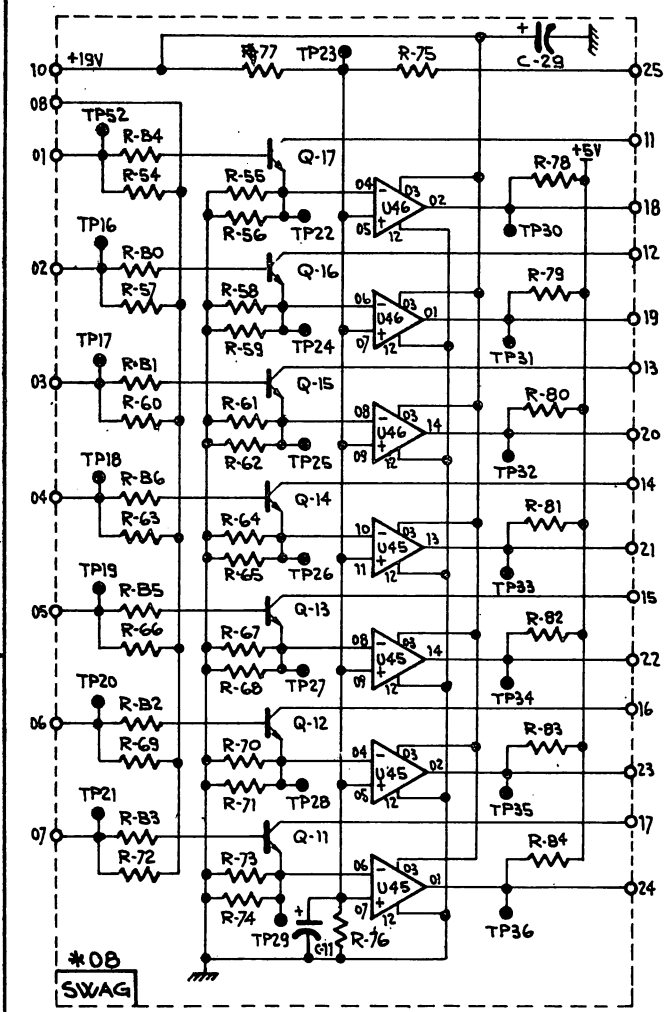
Honeywell

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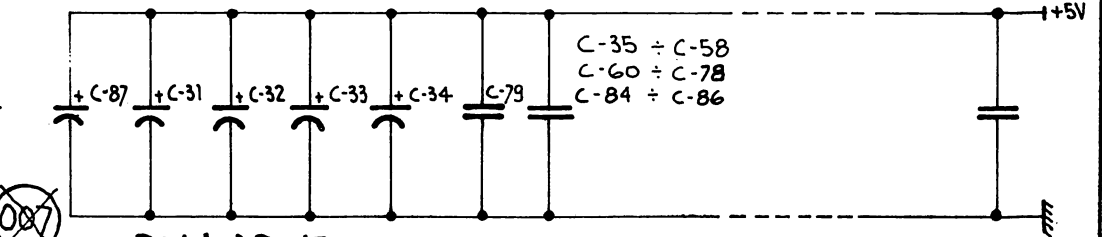
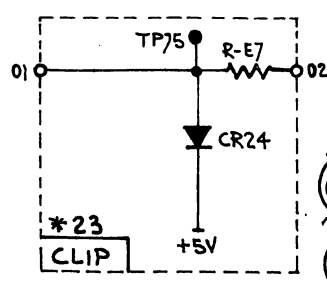
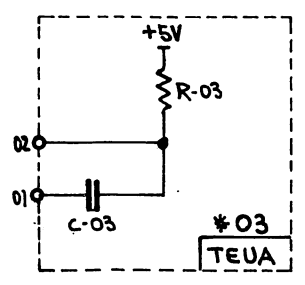
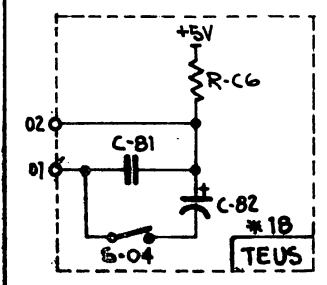
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15	ZC

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APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.



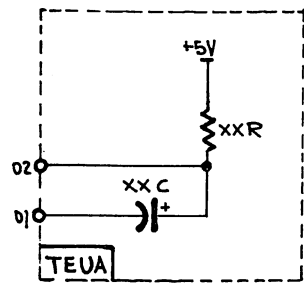
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DO NOT USE FOR NEW DESIGN
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NON USARE PER NUOVI PROGETTI



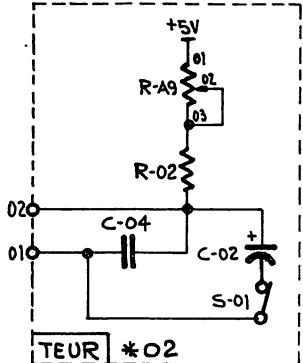
PWA DRIVE
Honeywell

F.T.O	DISEGNO	PAG.	REV
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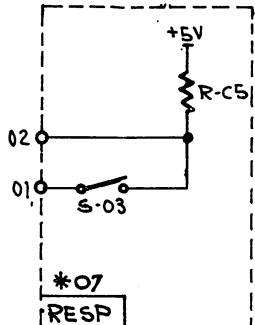
QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE "BERITTA" DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.



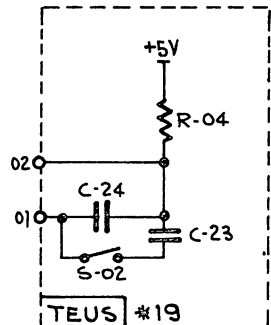
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 #04 C-28 R-C2



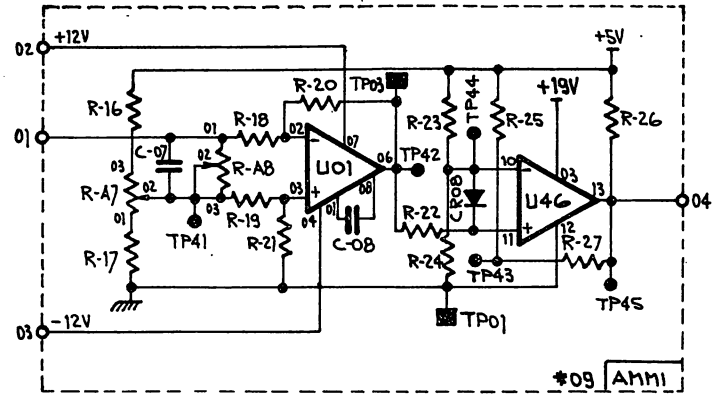
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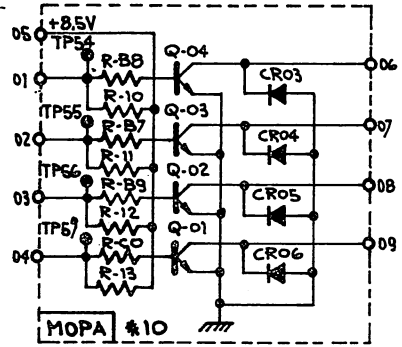
*07
 RESP



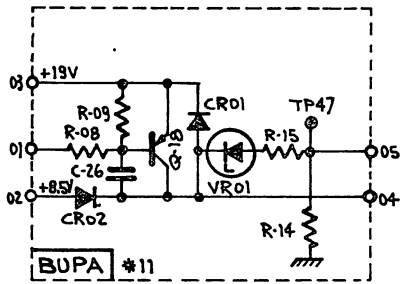
TEUS #19



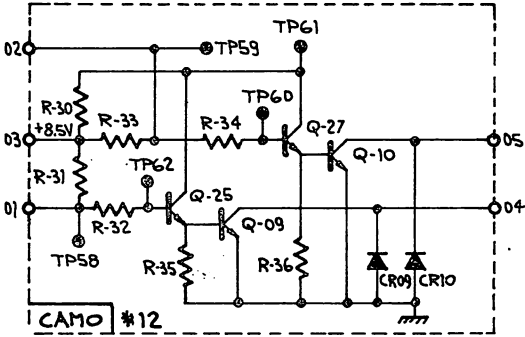
#09 AMMI



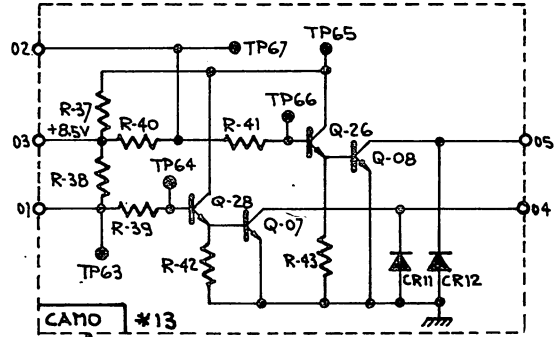
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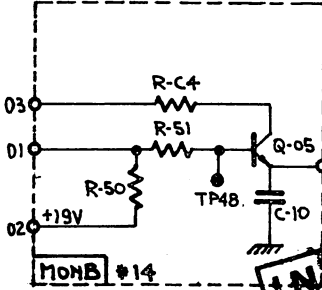
BUPA #11



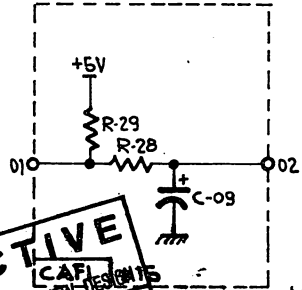
CAMO #12



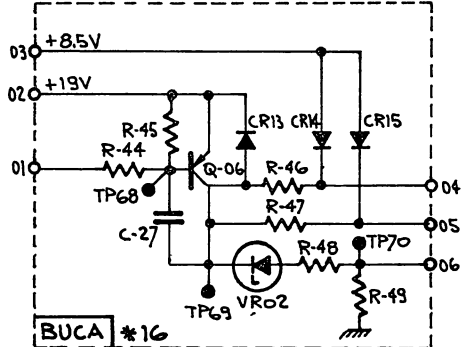
CAMO #13



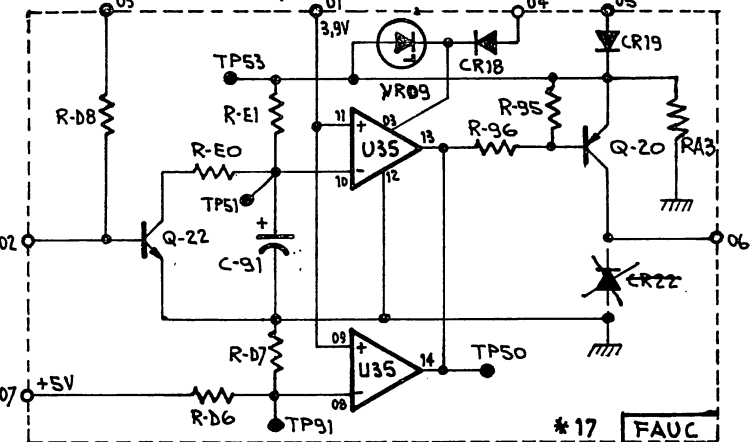
MONB #14



CAFA



BUCA #16



#17 FAUC

INACTIVE
 DO NOT USE FOR NEW DESIGNS
INATTIVO
 NON USARE PER NUOVI PROGETTI

~~007~~
~~006~~

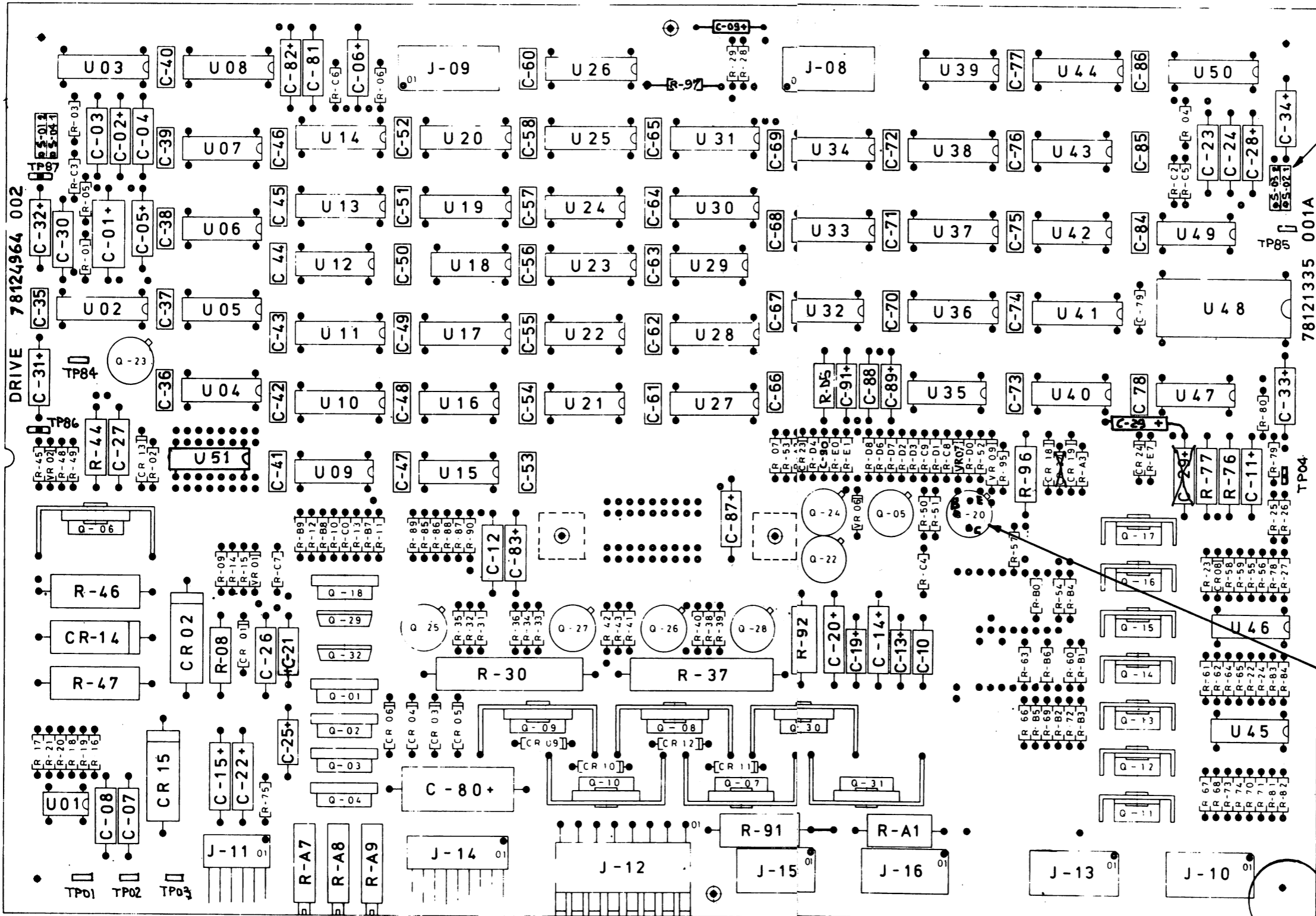
PWA DRIVE

Honeywell

F.T.O. DISEGNO
 B 78117135

PAG. REV
 17/F ZC

INFORMATION SYSTEMS ITALIA. ESCLUSIVO PER IL MERCATO ITALIANO. TUTTI I DIRITTI RISERVATI. APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

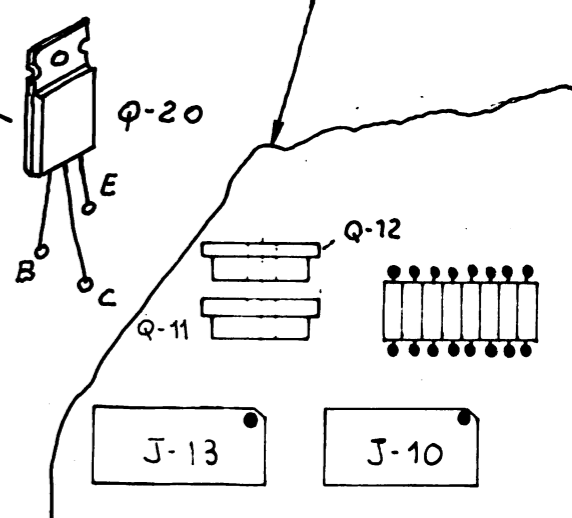


POSIZIONARE GLI STRAPPING S-01 S-04 COME IN TABELLA SUL FOGLIO 1
 SWITCH THE STRAPPINGS S-01/S-04 AS INDICATED IN TABLE ON SHEET 1

Q-05 Q-20 Q-24-Q-28) 92
 (Q-22 Q-23) 93

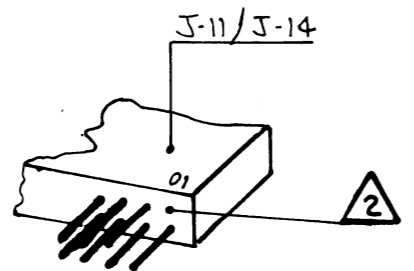
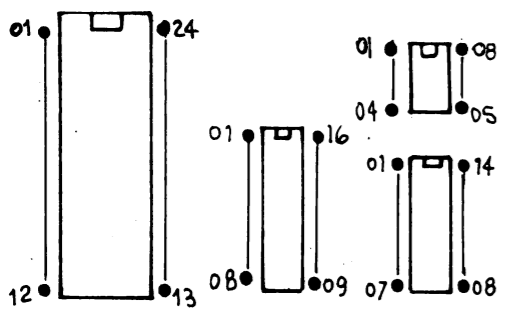
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 DO NOT USE FOR NEW DESIGN
INATTIVO
 NON USARE PER NUOVI PROGETTI
 ALTERNATIVE COME INDICATO IN PARTICOLARE ITEM 124

MOUNT THE TRANSISTOR IN ALTERNATIVE AS INDICATED IN DETAIL ITEM 124

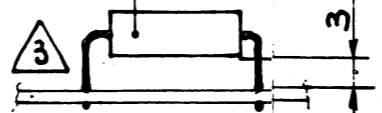


N°4 ZONE (LATO SALDATURA) ESENTI DATRACCE DI STAGNO DOVUTE ALLA SALDATURA COMPONENTI.

N°4 AREAS (SOLDERING SIDE) WITHOUT TIN RESIDUE, DUE TO COMPONENT SOLDERING



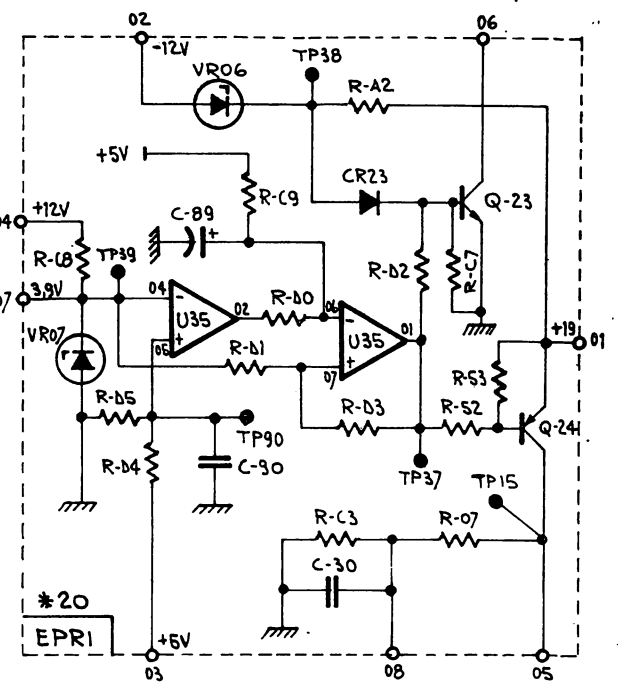
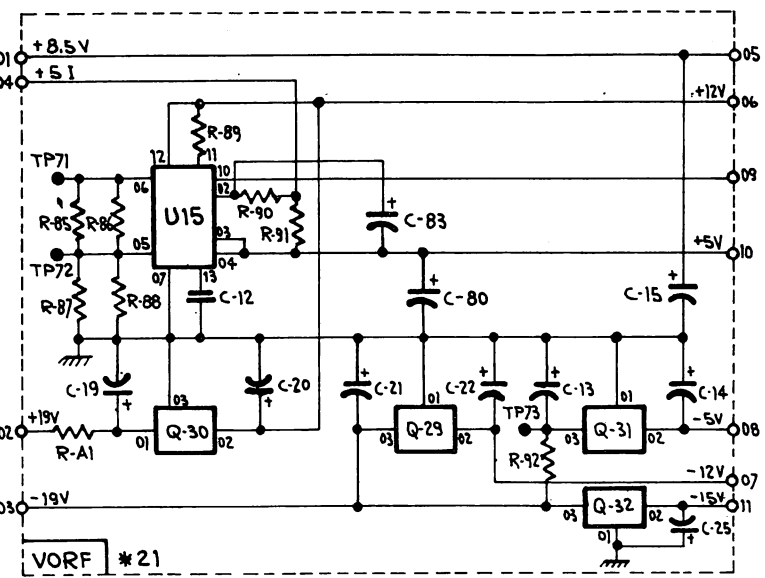
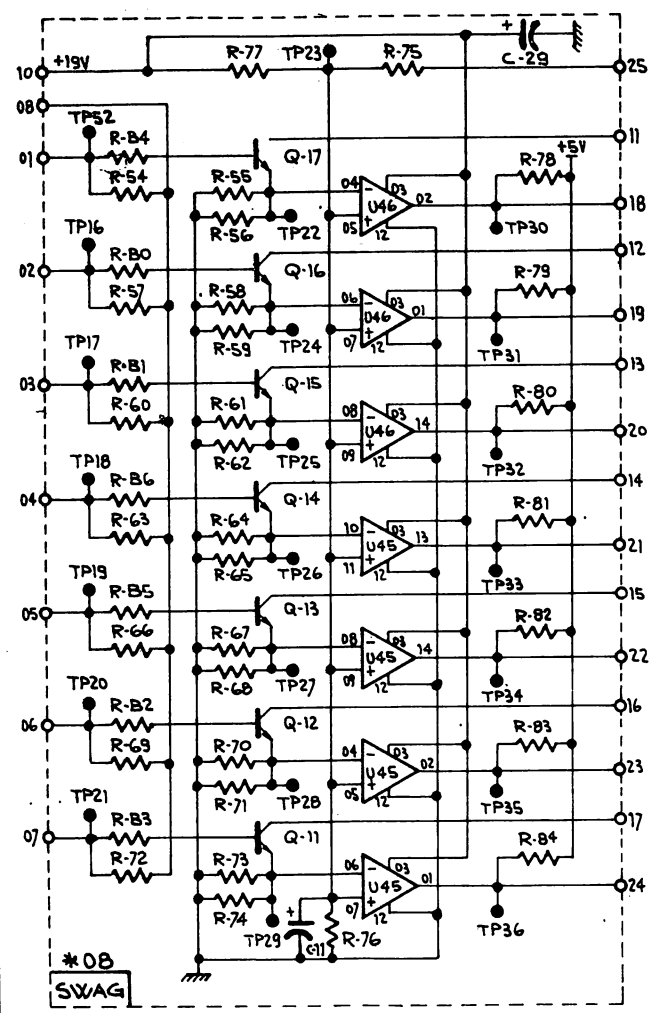
R-30 R-37 R-91 R-92 R-46 R-47
 R-A1 R-E2 R-E3 CR02 C-14 CR15



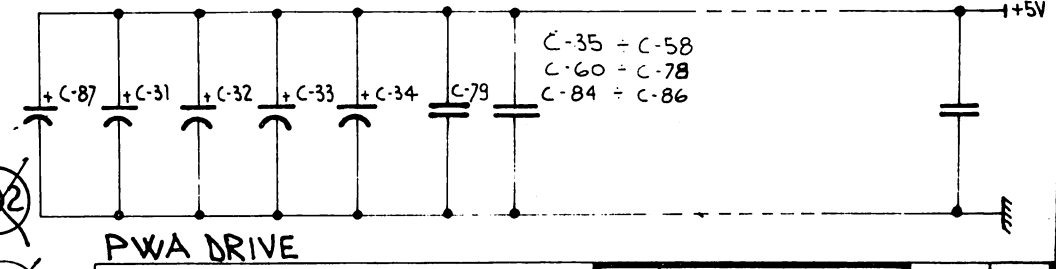
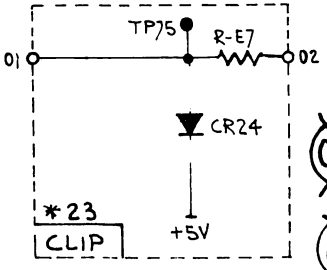
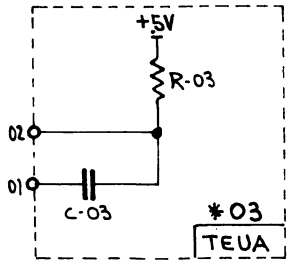
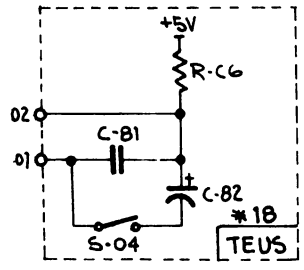
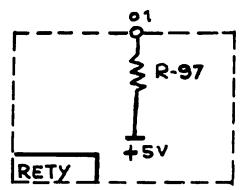
~~001~~ ~~002~~
 PWA DRIVE

Honeywell

QUESTO DOCUMENTO CONTIENE INFORMAZIONI SENSIBILI. IL SUO USO, INTERNO O ESTERNO, SENZA L'APPROVAZIONE SCRITTA È VIETATA.



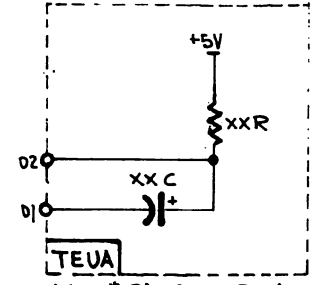
INACTIVE
DO NOT USE FOR NEW DESIGN
INATTIVO
NON USARE PER NUOVI PROGETTI



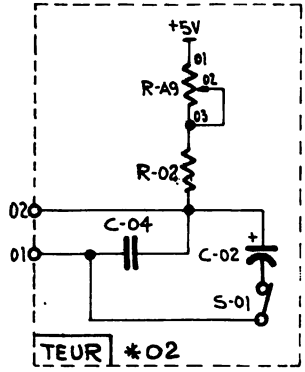
PWA DRIVE
Honeywell

F.T.O	DISEGNO	PAG.	REV
B	78124964	3	BC

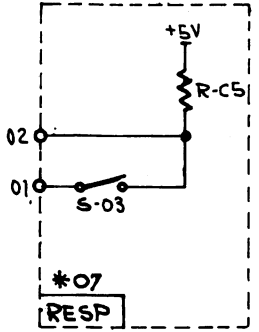
QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.



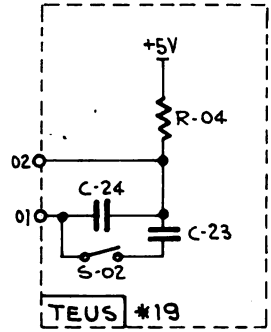
TEUA
 XX = *01 C-01 R-01
 *05 C-05 R-05
 *06 C-06 R-06
 *04 C-28 R-C2



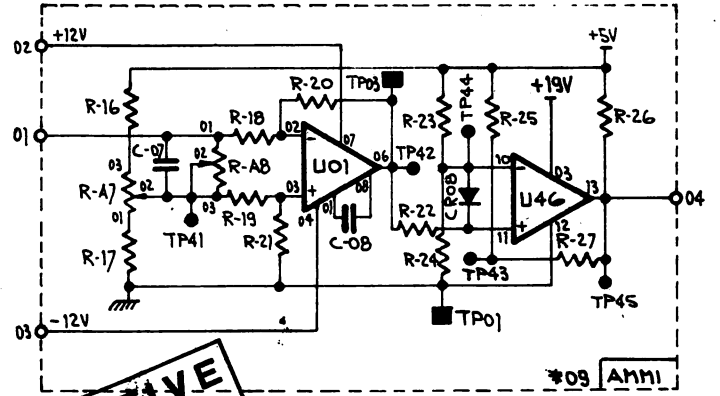
TEUR *02



*07
 RESP

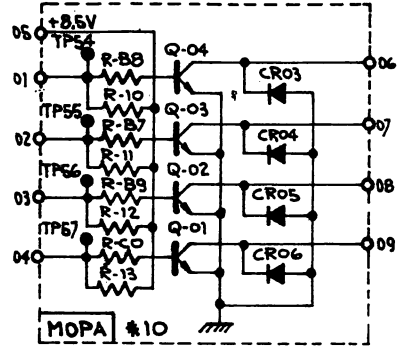


TEUS *19

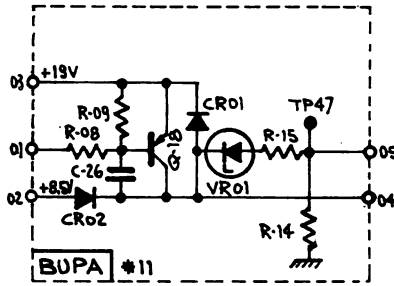


*09 ANMI

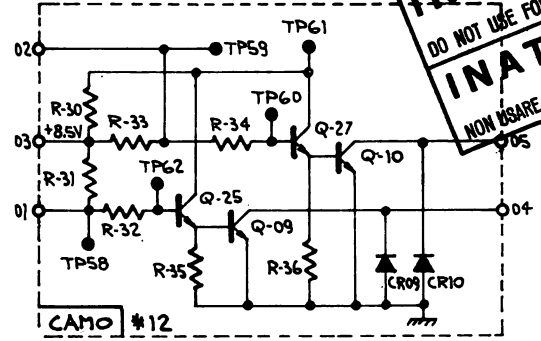
INACTIVE
 DO NOT USE FOR NEW DESIGN
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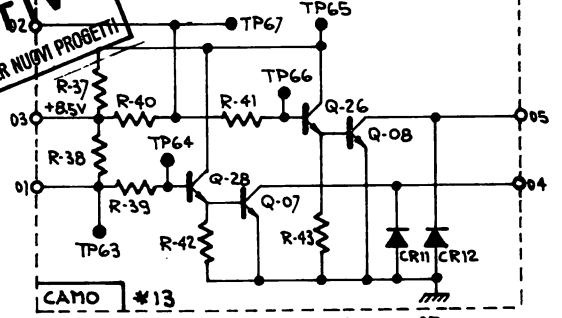
MOPA *10



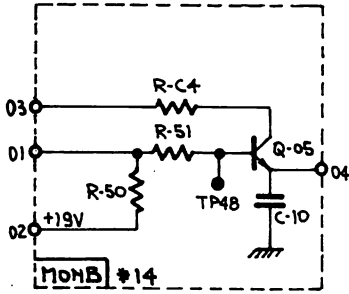
BUPA *11



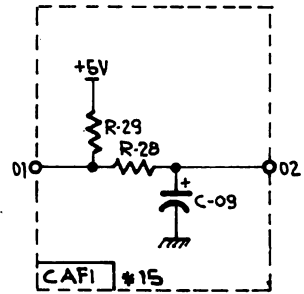
CAMO *12



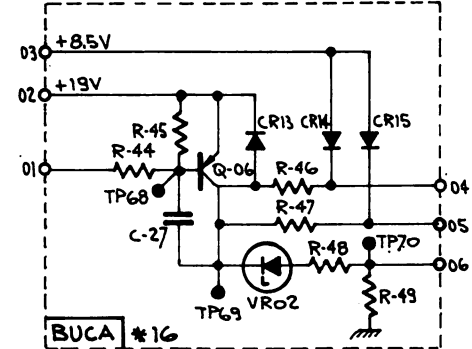
CAMO *13



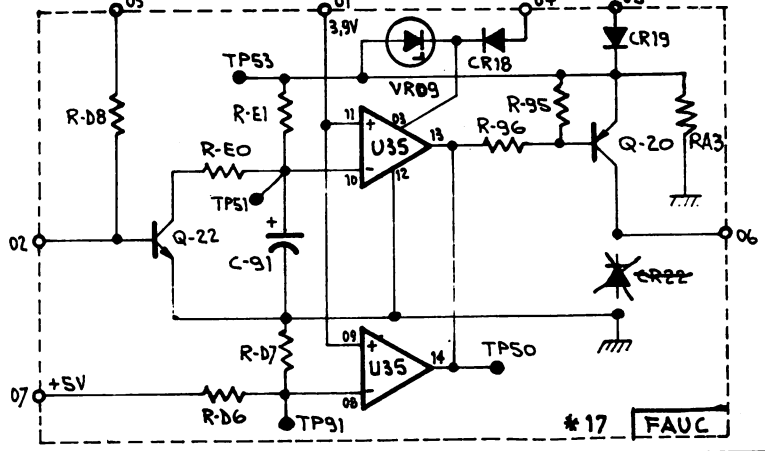
MONB *14



CAFI *15



BUCA *16



*17 FAUC

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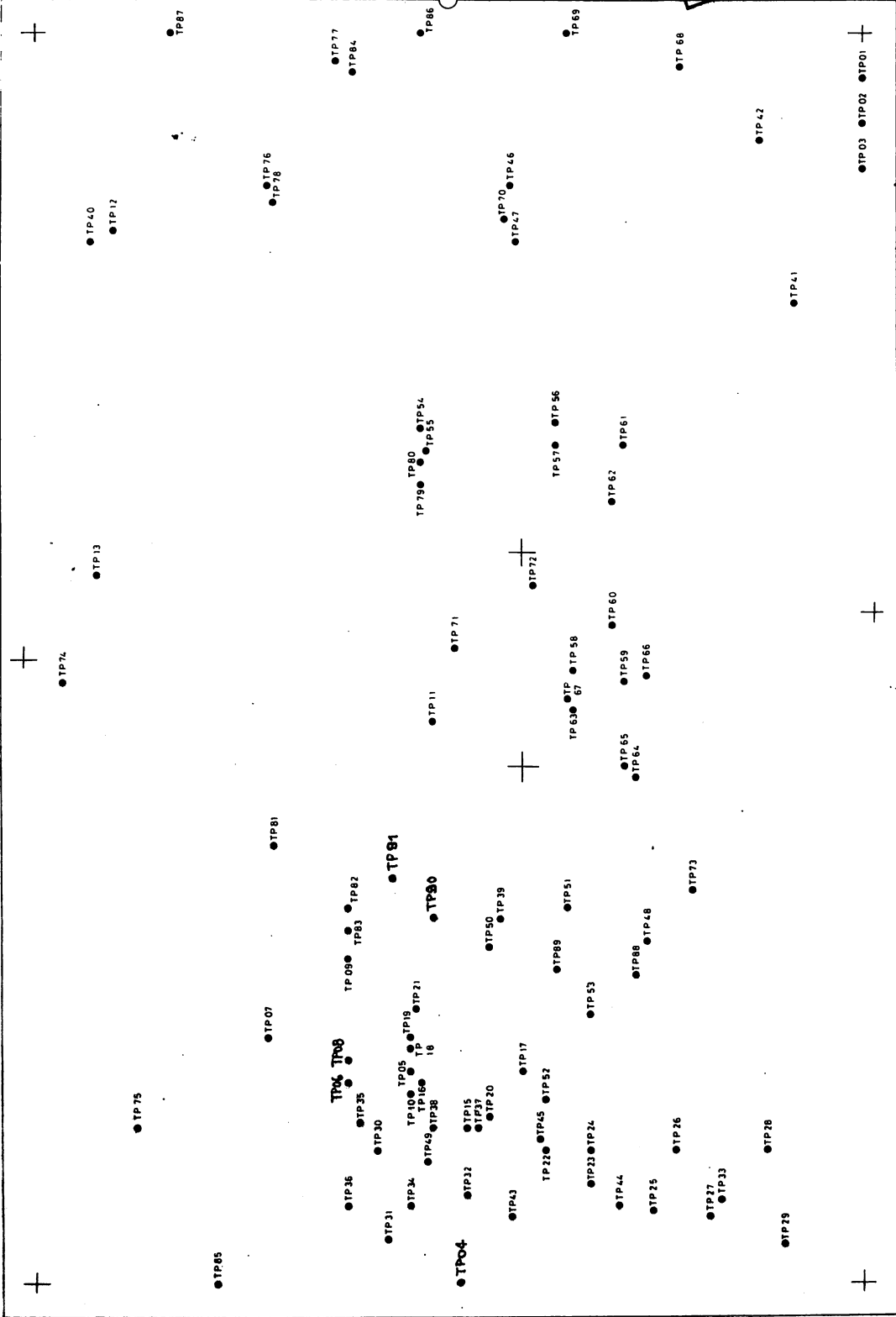
PWA DRIVE

Honeywell

F.T.O. DISEGNO
 B 78124964

PAG. REV
 4 BC

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI ALTRA DISTRIBUZIONE, A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.



LATO SALD. PWA DRIVE

INATTIVO
 NON USARE PER NEI DESCRIZIONI
 IL LEGGERE

PWA DRIVE

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PAG. REV

5 BC



CONNECTORI PWA DRIVE
CONNECTORS PWA DRIVE

J08		
PIN	SIGNAL	ADD.
1	S0NAL	*
2	PR4	* 1
3	RB4	* 0
4	FB4	* 1
5	FW4	* 0
6	PRO	* 0
7	PRS	* 0
8	RB13	* 12
9	RB14	* 12
10	PR7	* 1
11	PAPEN	* 1
12	ISOL	* 00
13	HBLP	* 0
14	RB15	* 12
15	PR1	* 0
16	HOOKH	* 0
17	PR6	* 1
18		

J09		
PIN	SIGNAL	ADD.
1	-12V	*
2	RESET	* 0
3	-15V	* 00
4	DBR3	* 1
5	DBR7	* 1
6	DBR1	* 1
7	DBR2	* 1
8	DBR0	* 1
9	+12	* 1
10	+12	* 1
11	DBR6	* 1
12	DBR5	* 0
13	DS061	* 0
14	DRDS	* 1
15	DBR4	* 1
16	DS051	* 0
17	MR1	* 1
18	-5V	* 1

J10 (VPU-SFU)		
PIN	SIGNAL	ADD.
1	DBR1	* 1
2	DBR3	* 1
3	PRO	* 00
4	DBR0	* 1
5	DBR6	* 1
6	DBR2	* 1
7	DBR5	* 1
8	+5V	* 1
9	+19V	* 1
10	DS061	* 0
11	DBR7	* 01
12	IMPPA	* 01
13	RESET	* 0
14	GROUND	* 0
15	DS058	* 0
16	DS058	* 0

J11 (SWITCHES)		
PIN	SIGNAL	ADD.
1	+5V	
2	PREND	* 1
3	S0L00	* 0
4	S0L00	* 0
5	EMBUZ	* 0
6	+8.5	
7	GROUND	
8	GROUND	
9	SPECA	* 0
10	MRSEN	* 0
11	GROUND	
12	GROUND	

J12 (POWER SUPPLY)		
PIN	SIGNAL	ADD.
1	+8.5V	
2	+8.5V	
3	+19V	
4	+5V	
5	+5V	
6	GROUND	
7	GROUND	
8	GROUND	

J13 (HEAD CONNECTOR)		
PIN	SIGNAL	ADD.
1	+19V	
2	+19V	
3	+19V	
4	CPRI8	* 0
5	+19V	
6	+19V	
7	+19V	
8	+19V	
9	CPIL1	* 00
10	CPIL2	* 00
11	CPIL3	* 00
12	GROUND	
13	CPIL4	* 00
14	CPIL5	* 00
15	CPIL6	* 00
16	CPIL7	* 00

J14 (NOTES)		
PIN	SIGNAL	ADD.
1	-19V	
2	BRCIT	* 1
3	BRENT	* 1
4	COM02	* 0
5	COM01	* 0
6	COM04	* 1
7	COM03	* 1
8	POCAR1	* 1
9	POCAR1	* 1
10	POCAR2	* 1
11	POCAR2	* 1
12	POCAR2	* 1
13	POPAR	* 1
14	POPAR	* 1
15	COPR2	* 1
16	COPR1	* 1
17	COPR1	* 1
18	COPR3	* 1

J15 (AFF)		
PIN	SIGNAL	ADD.
1	AFAR	* 0
2	10KHZ	* 0
3	DS051	* 00
4	PR1	* 00
5	+8.5V	
6	+5V	
7	+5V	
8	S1234	* 1
9	+19V	
10	+19V	

J16 (AFF)		
PIN	SIGNAL	ADD.
1	DBR0	* 1
2	DSC8	* 1
3	DBR5	* 1
4	DSDB	* 1
5	DBR2	* 1
6	-5V	
7	+12V	
8	MR1	* 1
9	DBR1	* 1
10	GROUND	
11	GROUND	
12	DBR7	* 1
13	DBR6	* 1
14	DBR4	* 1
15	DBR3	* 1
16		

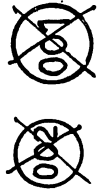
INACTIVE
 DO NOT USE FOR NEW DESIGN
 INACTIVE
 NON-DESIGN REVERSE SIDE

INTERFACE CONNECTORS - K. DRIVE

Honeywell

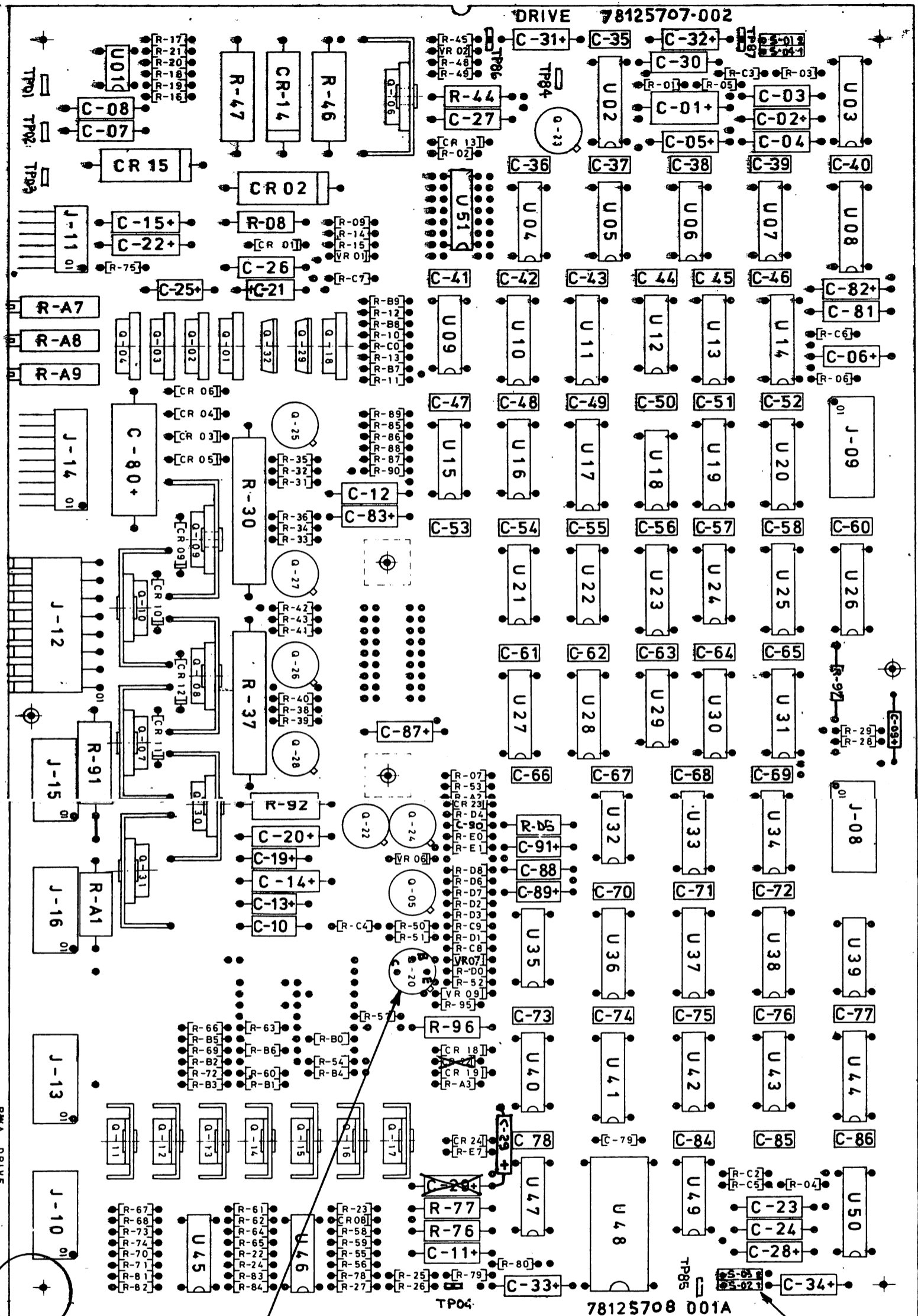
F.T.O. DISEGNO
B 78124964

PAG. REV.
6/F BC



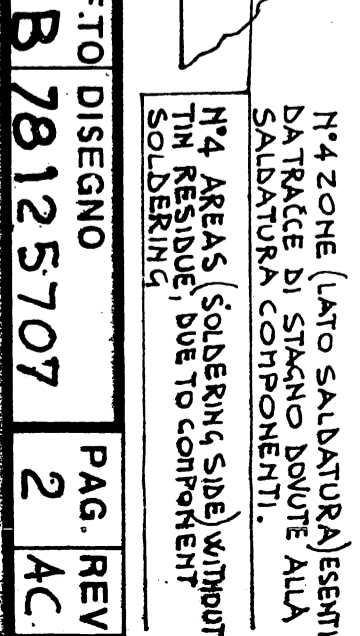
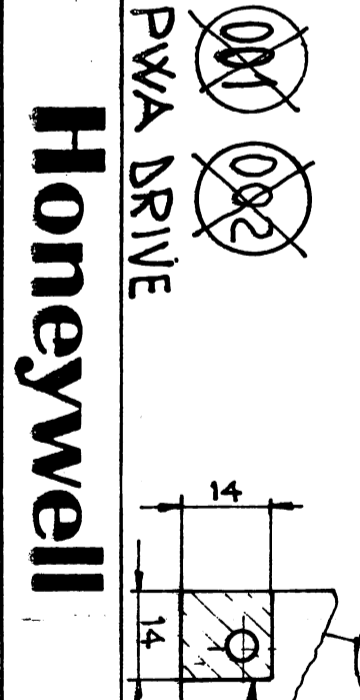
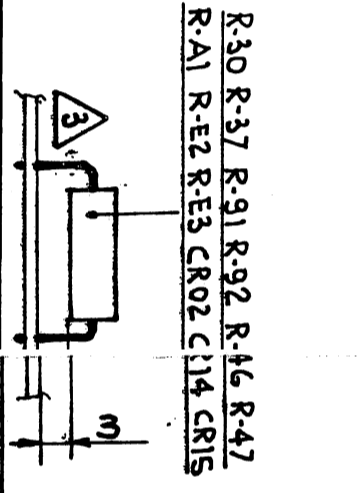
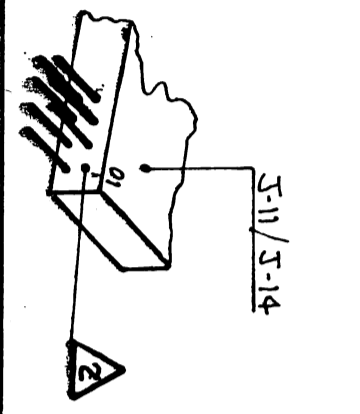
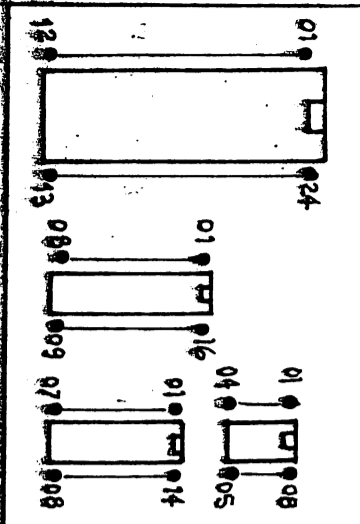
THIS DOCUMENT AND THE INFORMATION CONTAINED THEREIN IS CONFIDENTIAL AND PROPRIETARY TO AND NOT TO BE DISCLOSED TO ANY OTHER PARTY WITHOUT THE EXPRESS WRITTEN CONSENT OF HONEYWELL. THE INFORMATION CONTAINED HEREIN IS THE PROPERTY OF HONEYWELL AND IS NOT TO BE DISCLOSED TO ANY OTHER PARTY WITHOUT THE EXPRESS WRITTEN CONSENT OF HONEYWELL.

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78125708 001A

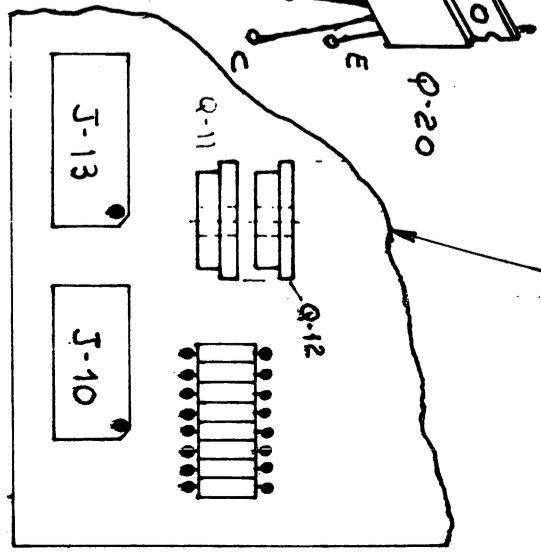


R-30 R-37 R-91 R-92 R-46 R-47
R-A1 R-E2 R-E3 CR02 C14 CR15

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~~002~~
PWA DRIVE

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F.T.O. DISEGNO
B 78125707
PAG. REV
2 AC



MONTE IL TRANSISTOR IN ALTERNATIVE AS INDICATED IN DETAIL ITEM 124

NON TARE USARE PER NONI PROGETTI
ALTERNATIVE COME INDICATO IN PARTICOLARE ITEM 124
INATTIVO
DO NOT USE FOR NEW DESIGN
93

POSIZIONARE GLI STRAPPING S-01/S-04 COME IN TABELLA SUL FOGLIO 1
SWITCH THE STRAPPING S-01/S-04 AS INDICATED IN TABLE ON SHEET 1
92

B78125707

B78125707

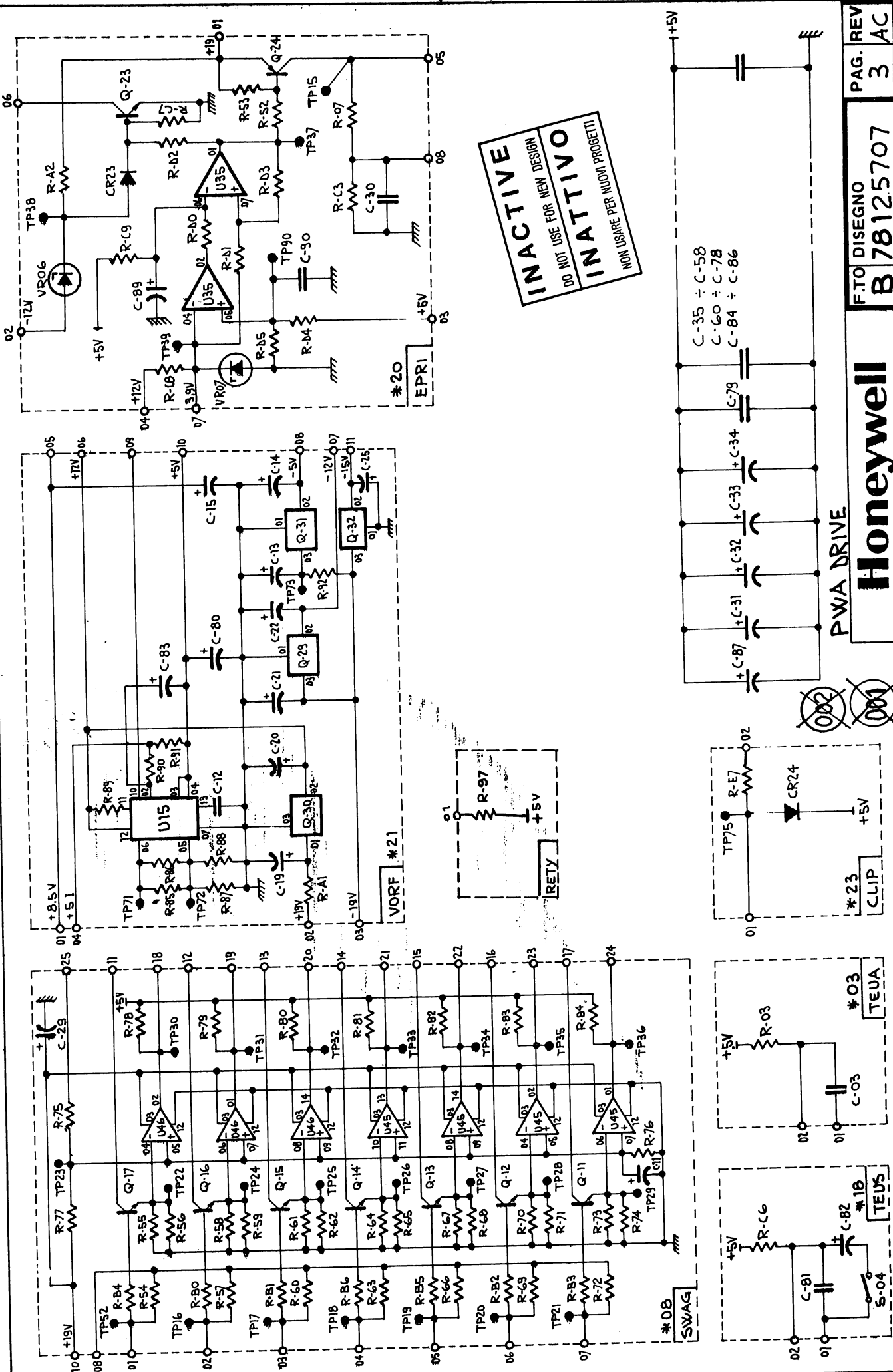
F.TO DISEGNO B 78125707

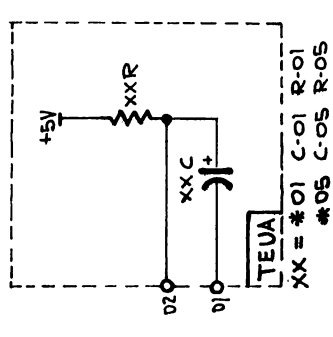
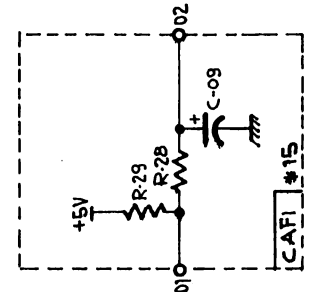
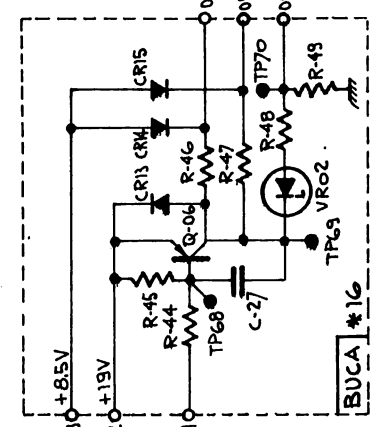
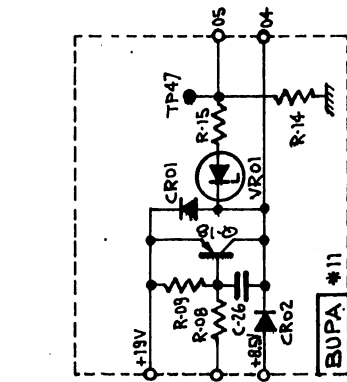
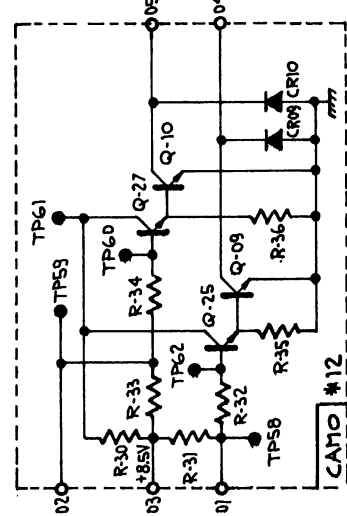
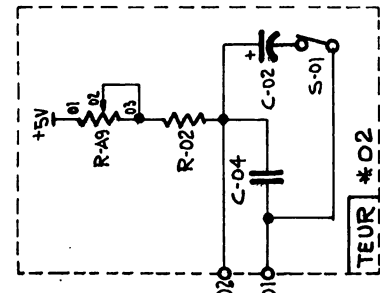
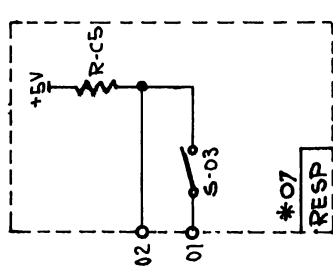
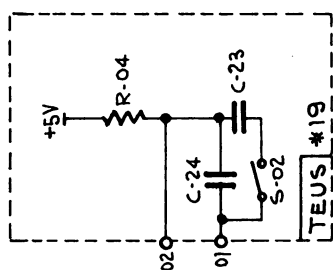
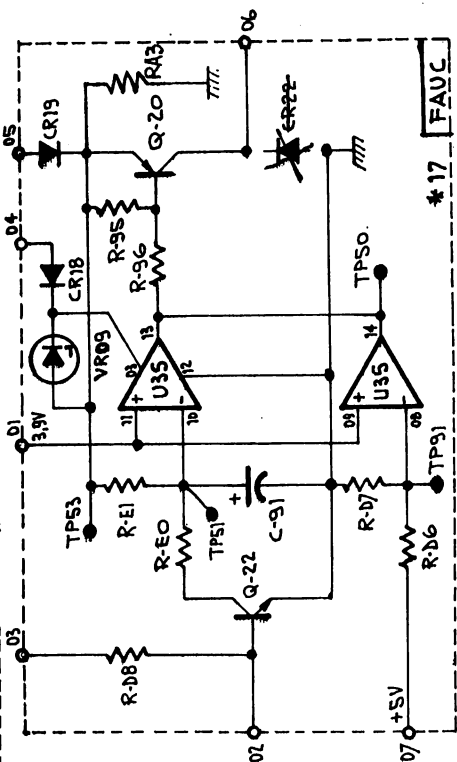
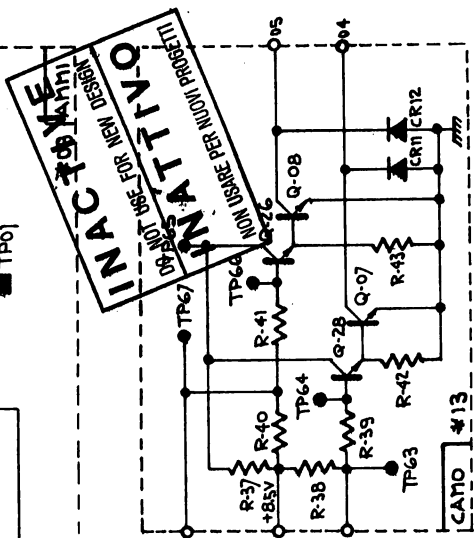
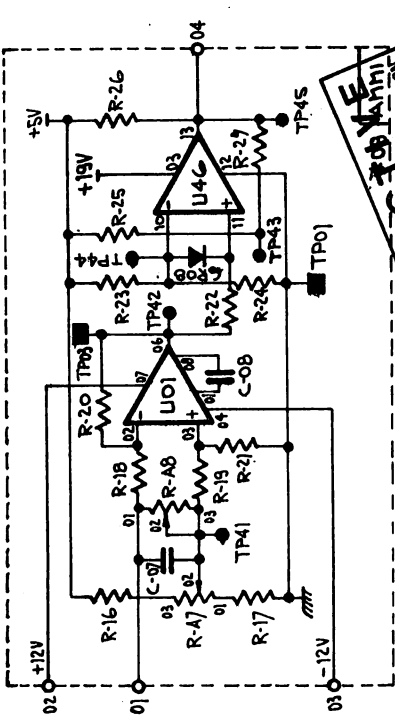
PWA DRIVE

Honeywell

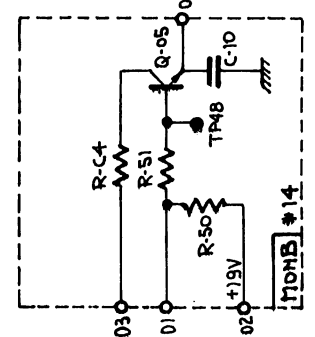
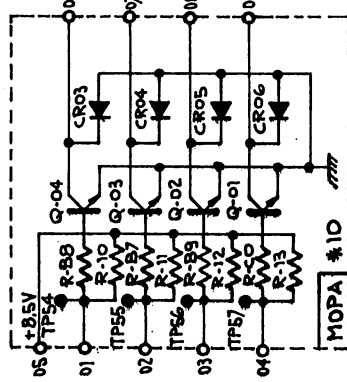
PAG. REV 3 AC

INACTIVE
DO NOT USE FOR NEW DESIGN
INATTIVO
NON USARE PER NUOVI PROGETTI

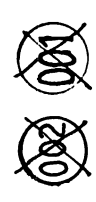




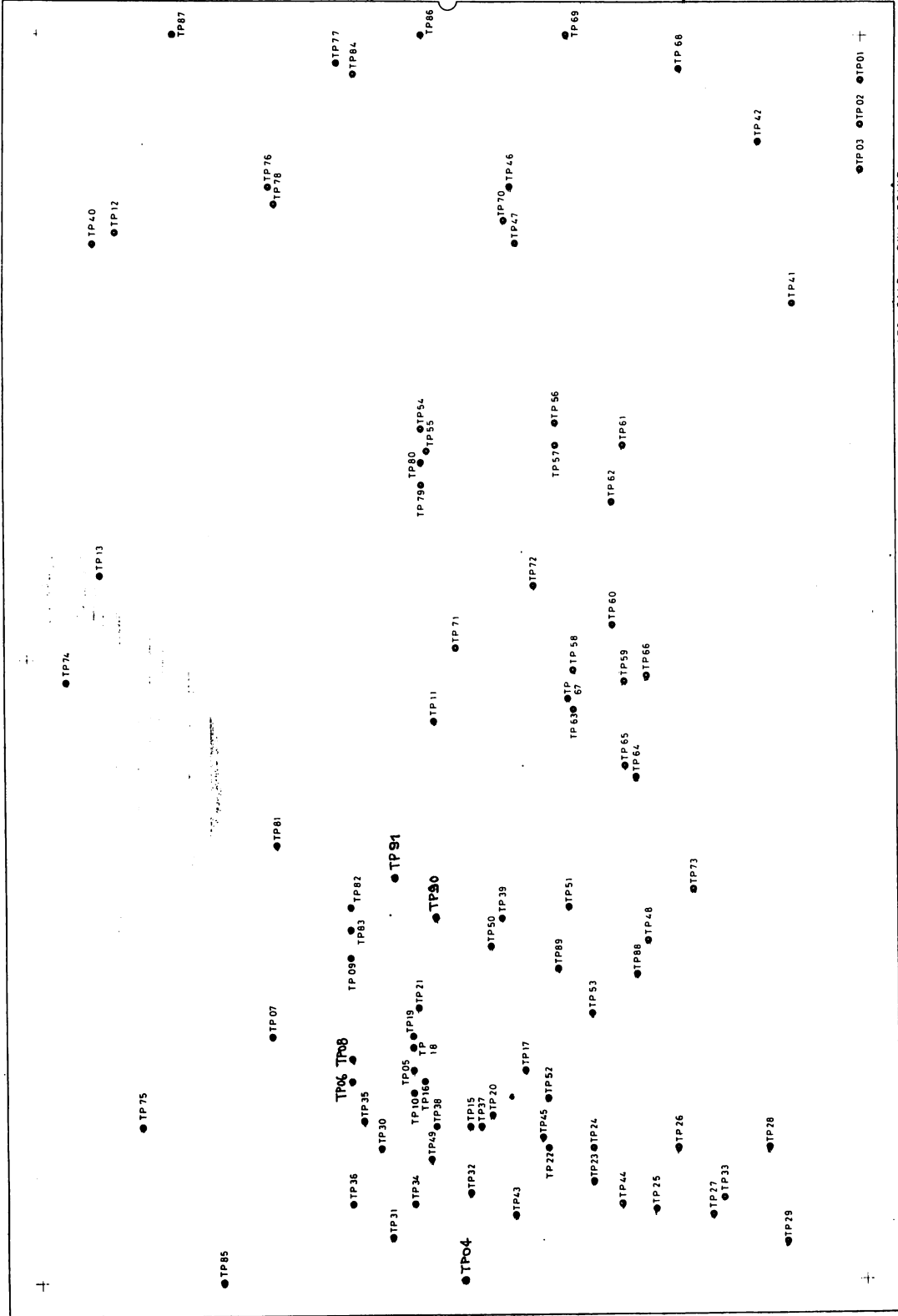
XX = #01 C-01 R-05
 #05 C-05 R-06
 #06 C-06 R-06
 #04 C-28 R-C2



ATTENTION
 NON USARE PER NULLI PROGETTI
 IL TIPO DI COMPONENTI
 INDICATO



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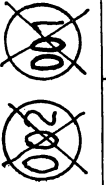


LATO SALD. PWA DRIVE

INATTIVO
 DO NOT USE FOR NEW DESIGN
 NON USARE PER NUOVI PROGETTI

PWA DRIVE

Honeywell



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CONNETTORI PWA DRIVE

CONNECTORS PWA DRIVE

J08		
PIN	SIGNAL	ADD.
1	SIGNAL * 1	
2	PR4 * 1	
3	AB11 * 03	
4	FIVIS * 1	
5	IOWR * 0	
6	PRO * 0	
7	PR3 * 0	
8	AB13 * 12	
9	AB12 * 12	
10	AB14 * 12	
11	PR7 * 1	
12	PAPEN * 1	
13	SOL * 00	
14	HOLD * 0	
15	AB15 * 12	
16	PR1 * 0	
17	100KH * 0	
18	PR6 * 1	

J09		
PIN	SIGNAL	ADD.
1	-12V	
2	RESET * 0	
3	-15V	
4	DBR7 * 1	
5	DBR3 * 1	
6	DBR1 * 1	
7	DBR0 * 1	
8	DBR2 * 1	
9	+12	
10	+12	
11	DBR6 * 1	
12	DBR5 * 1	
13	DS06I * 0	
14	DRDS * 0	
15	DBR4 * 1	
16	DS05I * 0	
17	MR1 * 1	
18	-5V	

J10 (VFU-SFU)		
PIN	SIGNAL	ADD.
1	DBR1 * 1	
2	DBR3 * 1	
3	PRO * 00	
4	DBR0 * 1	
5	DBR6 * 1	
6	DBR2 * 1	
7	DBR4 * 1	
8	DBR5 * 1	
9	+5V	
10	+19V	
11	DS06I * 0	
12	DBR7 * 1	
13	IMPPA * 01	
14	RESET * 0	
15	GROUND	
16	DSE80 * 0	

J11 (SWITCHES)		
PIN	SIGNAL	ADD.
1	+5V	
2	PAEND * 1	
3	SOL00 * 0	
4	SOL00 * 0	
5	EOL00 * 0	
6	IMBUZ * 0	
7	+2,5	
8	GROUND	
9	GROUND	
10	SPECA * 0	
11	MASEN * 0	
12	GROUND	

J12 (POWER SUPPLY)		
PIN	SIGNAL	ADD.
1	+8,5V	
2	+8,5V	
3	+19V	
4	+5I	
5	+5V	
6	GROUND	
7	GROUND	
8	GROUND	

J13 (HEAD CONNECTOR)		
PIN	SIGNAL	ADD.
1	+19V	
2	+19V	
3	+19V	
4	CORIB * 0	
5	+19V	
6	+19V	
7	+19V	
8	+19V	
9	C0IL1 * 00	
10	C0IL2 * 00	
11	C0IL3 * 00	
12	GROUND	
13	C0IL4 * 00	
14	C0IL5 * 00	
15	C0IL6 * 00	
16	C0IL7 * 00	

J14 (NOTORS)		
PIN	SIGNAL	ADD.
1	-19V	
2	BACIT * 1	
3	BREAT * 1	
4	COM2 * 0	
5	COM02 * 0	
6	COM01 * 1	
7	COM04 * 0	
8	COM03 * 1	
9	POCA1 * 1	
10	POCA1 * 1	
11	POCA2 * 1	
12	POCA2 * 1	
13	POPAR * 1	
14	POPAR * 1	
15	COPA2 * 1	
16	COPA4 * 1	
17	COPA1 * 1	
18	COPA3 * 1	

J15 (AFF)		
PIN	SIGNAL	ADD.
1		
2		
3	AFFAR * 0	
4	10KHZ * 0	
5	DS05I * 0	
6	PR1 * 00	
7	+8,5V	
8	+8,5V	
9	+5V	
10	+5V	
11		
12		
13	S1234 * 0	
14		
15	+19V	
16	+19V	

J16 (AFF)		
PIN	SIGNAL	ADD.
1	DBR0 * 1	
2	DSC80 * 0	
3	DBR5 * 1	
4	DSDB0 * 0	
5	DBR2 * 1	
6	-5V	
7	+12V	
8	MR1 * 1	
9	DBR1 * 1	
10	GROUND	
11	GROUND	
12	DBR7 * 1	
13	DBR6 * 1	
14	DBR4 * 1	
15	DBR3 * 1	
16		

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INTERFACE CONNECTORS - K. DRIVE

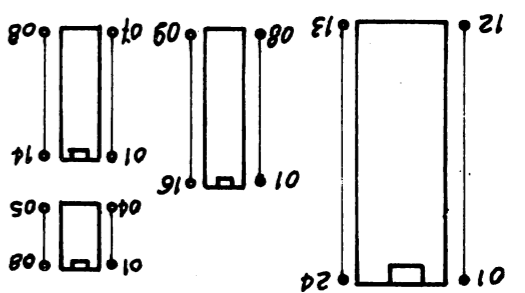
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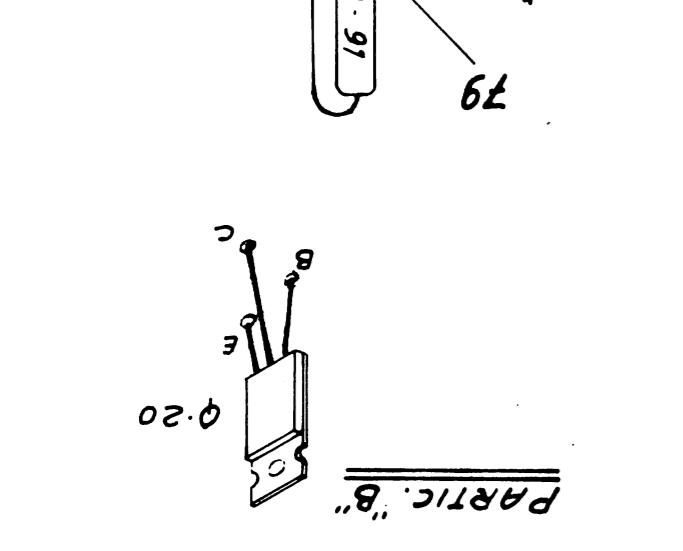
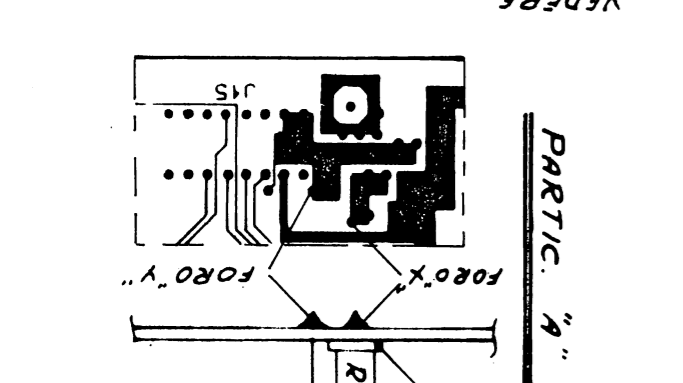
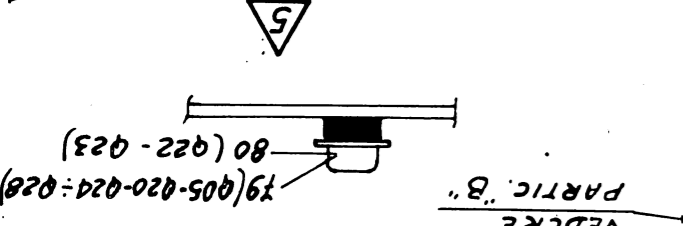
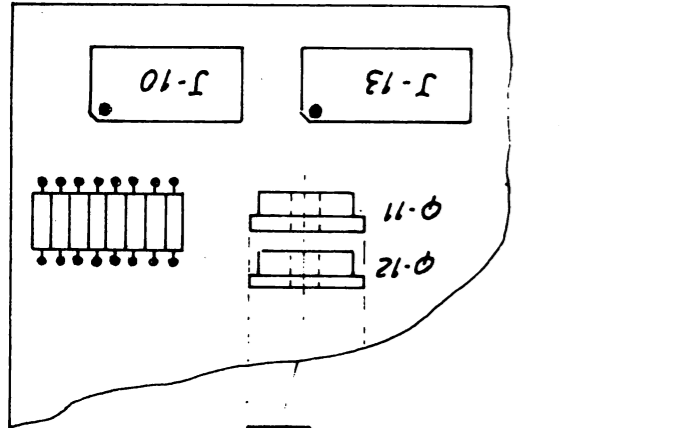
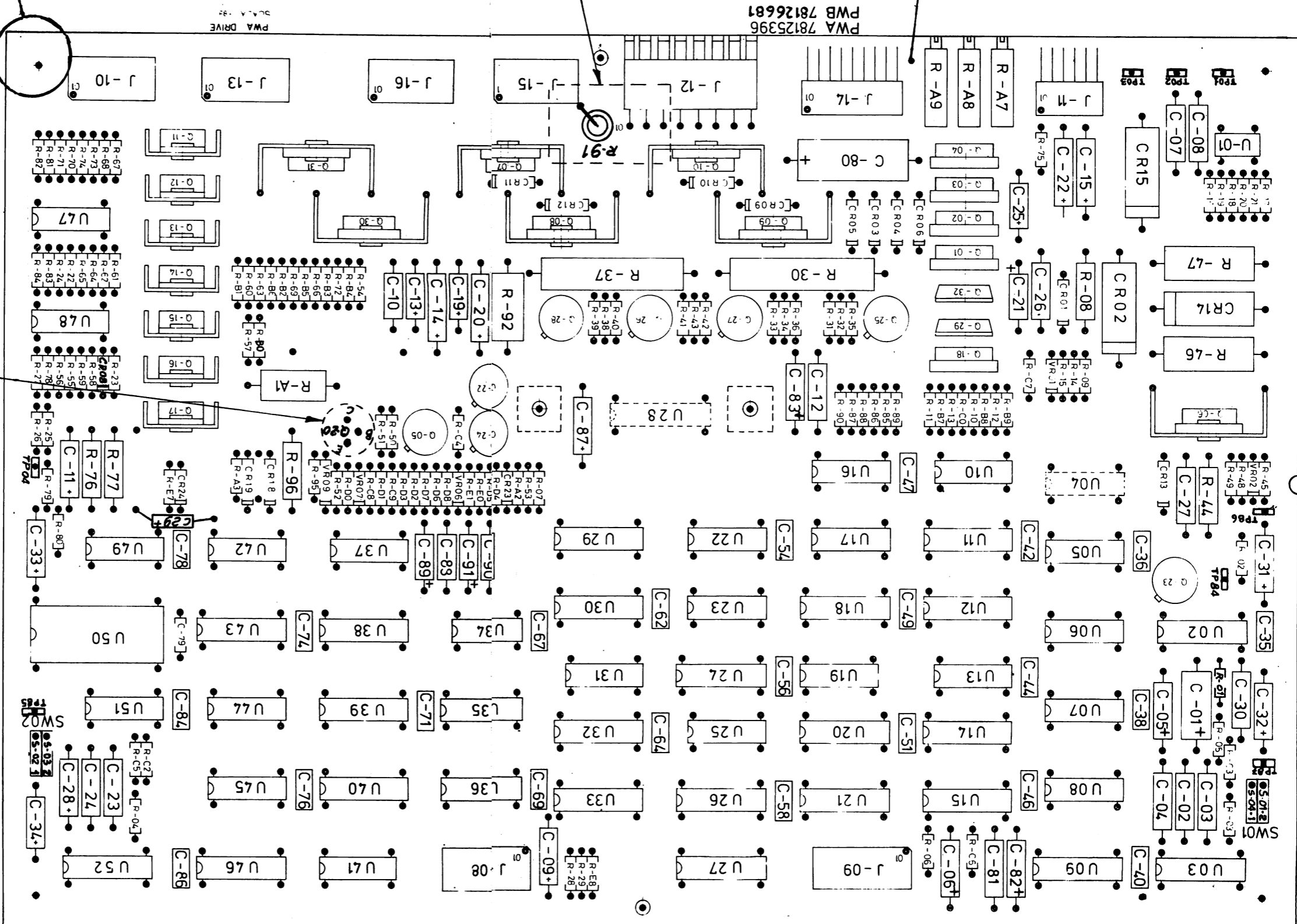
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R30-R37-R92-R16-R47
R A1 - R E3 - R E3 - C R12 - C R14 - C R15

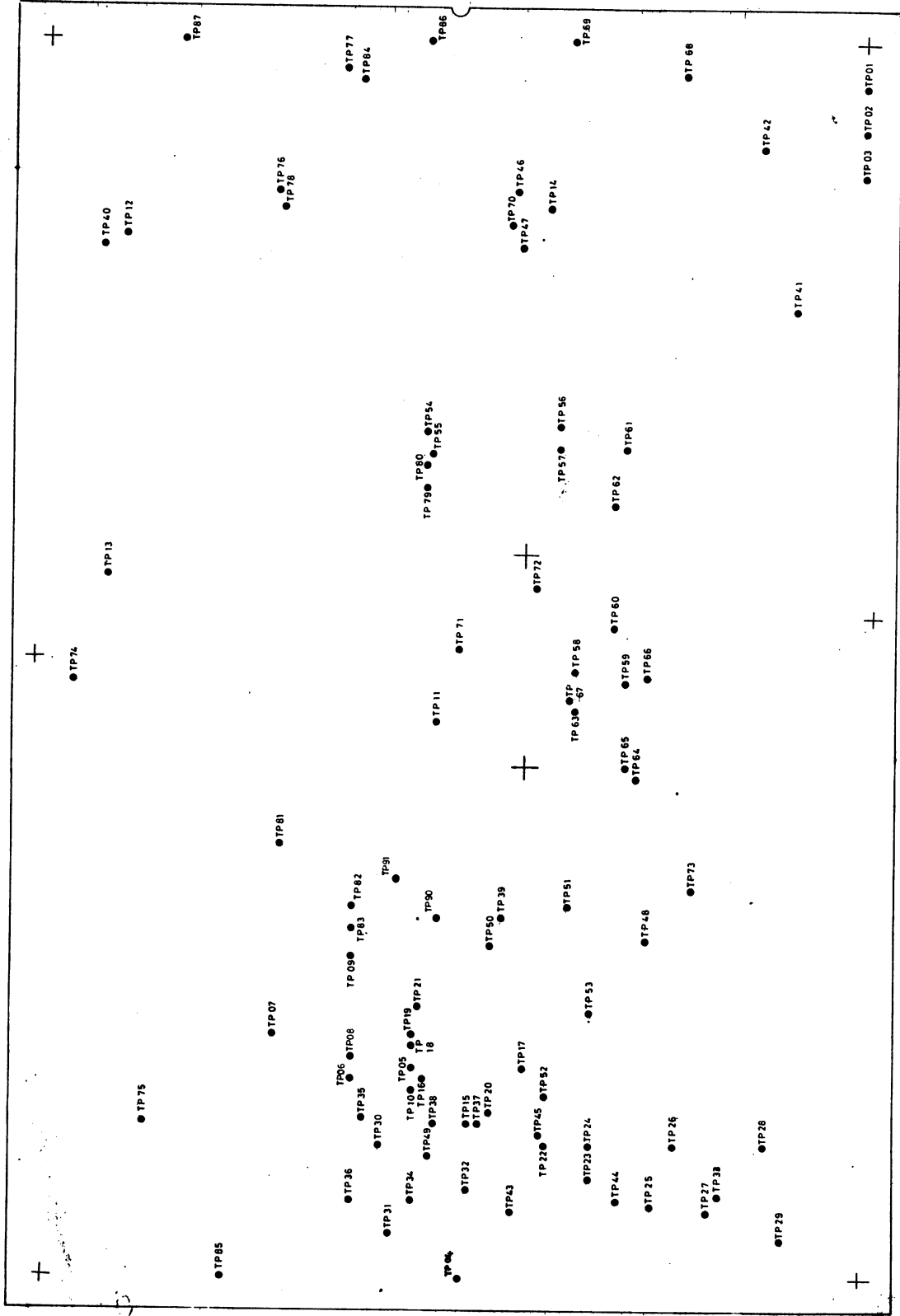
VEDERE PART. "A"

H4 ZONE (LATO SALDATURA) ESSENTI DA TRACCE DI STAGNO DOVUTE ALLA SALDATURA COMPONENTI
NO4 AREAS (SOLDERING SIDE) WITHOUT TIN RESIDUE, DUE TO COMPONENT SOLDERING



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LATO SALD. PWA DRIVE
SCALA 3,00 1

PWA - DRIVE

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3 FA

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CONNETTORI PWA DRIVE

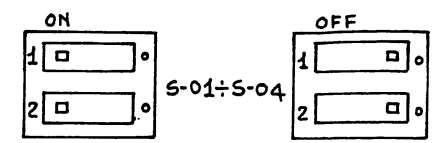
CONNECTORS PWA DRIVE

J08		
PIN	SIGNAL	L.B.D.
1	S0NAL	* 0
2	PR4	* 1
3	AB11	* 0
4	FIVIS	* 1
5	I0WR	* 0
6	PRO	* 0
7	PR3	* 0
8	AB13	* 1
9	AB12	* 1
10	AB14	* 1
11	PR7	* 1
12	PAPEN	* 1
13	S0L	* 00
14	H0LD	* 0
15	AB15	* 1
16	PR1	* 0
17	100KH	* 0
18	PR6	* 1

J09		
PIN	SIGNAL	L.B.D.
1	-12V	
2	RESET	* 0
3	-15V	
4	DBR7	* 1
5	DBR3	* 1
6	DBR1	* 1
7	DBR0	* 1
8	DBR2	* 1
9	+12	
10	+12	
11	DBR6	* 1
12	DBR5	* 1
13	DS06I	* 0
14	0RDS	* 0
15	DBR4	* 1
16	DS05I	* 0
17	MR1	* 1
18	-5V	

J10 (VFU-SFU)		
PIN	SIGNAL	L.B.D.
1	DBR1	* 1
2	DBR3	* 1
3	PRO	* 00
4	DBR0	* 1
5	DBR6	* 1
6	DBR2	* 1
7	DBR4	* 1
8	DBR5	* 1
9	+5V	
10	+19V	
11	DS06I	* 0
12	DBR7	* 1
13	IMPPA	* 01
14	AESET	* 0
15	GROUND	
16	DSE30	* 0

J11 (SWITCHES)		
PIN	SIGNAL	L.B.D.
1	+5V	
2	PREND	* 1
3	S0L00	* 0
4	E0L00	* 0
5	IMBUZ	* 0
6	+8.5	
7	GROUND	
8	GROUND	
9	GROUND	
10	SPECA	* 0
11	MASEN	* 0
12	GROUND	



J12 (POWER SUPPLY)		
PIN	SIGNAL	L.B.D.
1	+8.5V	
2	+8.5V	
3	+19V	
4	+5I	
5	+5V	
6	GROUND	
7	GROUND	
8	GROUND	

J13 (HEAD CONNECTOR)		
PIN	SIGNAL	L.B.D.
1	+19V	
2	+19V	
3	+19V	
4	C0RIB	* 0
5	+19V	
6	+19V	
7	+19V	
8	+19V	
9	C0IL1	* 00
10	C0IL2	* 00
11	C0IL3	* 00
12	GROUND	
13	C0IL4	* 00
14	C0IL5	* 00
15	C0IL6	* 00
16	C0IL7	* 00

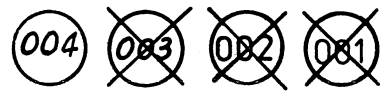
J14 (NOTORS)		
PIN	SIGNAL	L.B.D.
1	-19V	
2	BACIT	* 1
3	BREAT	* 1
4	COM02	* 0
5	COM01	* 1
6	COM04	* 0
7	COM03	* 1
8	POCA1	* 1
9	POCA1	* 1
10	POCA1	* 1
11	POCA2	* 1
12	POCA2	* 1
13	POPAR	* 1
14	POPAR	* 1
15	COPR2	* 1
16	COPR4	* 1
17	COPA1	* 1
18	COPA3	* 1

J15 (AFF)		
PIN	SIGNAL	L.B.D.
1		
2		
3	AFFAR	* 0
4	10KHZ	* 0
5	DS05I	* 0
6	PR1	* 00
7	+8.5V	
8	+8.5V	
9	+5V	
10	+5V	
11		
12		
13	S1234	* 1
14		
15	+19V	
16	+19V	

J16 (AFF)		
PIN	SIGNAL	L.B.D.
1	DBR0	* 1
2	DSC30	* 0
3	DBR5	* 1
4	DSDB0	* 0
5	DBR2	* 1
6	-5V	
7	+12V	
8	MR1	* 1
9	DBR1	* 1
10	GROUND	
11	GROUND	
12	DBR7	* 1
13	DBR6	* 1
14	DBR4	* 1
15	DBR3	* 1
16		

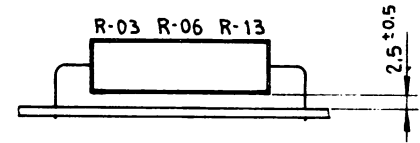
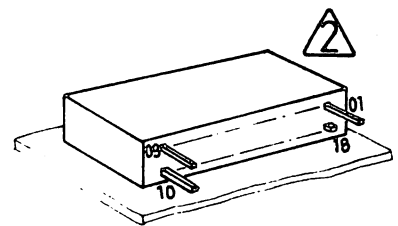
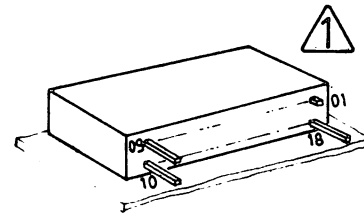
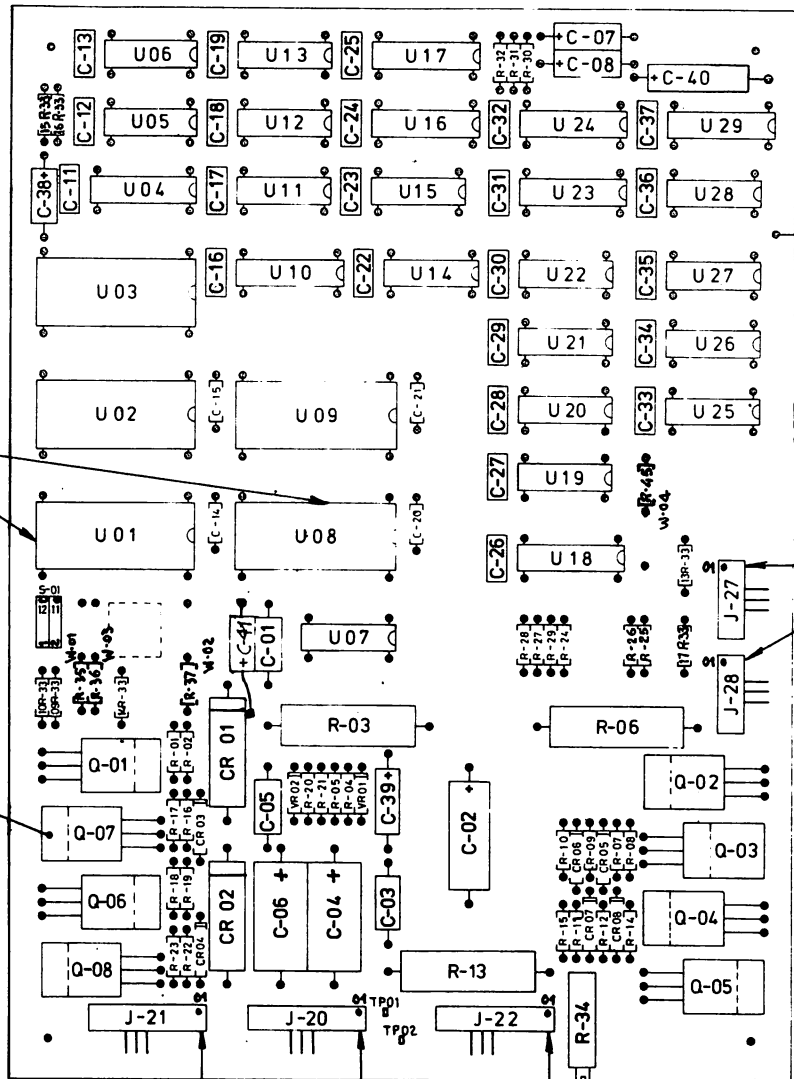
TABELLA POSIZIONAMENTO STRAPPINGS		
SWITCH	Rosy: 26.0-26.1-26.12 26R-260EM 28	SARA: 19-20 POLY: 21
	LINA: 22-25-26-26LX-27 40-41-42-43-44-46	ROSY: 21-24.0-24.1-25 72-74-82
		LINA: 20-21
	S-01	MUST BE OFF
S-04	MUST BE OFF	MUST BE ON
S-02	MUST BE OFF	MUST BE ON
S-03	MUST BE ON	MUST BE ON

INTERFACE CONNECTORS - K. DRIVE



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W01÷W04 - RESISTORS FOR BRIDGE SIMULATION.

W01÷W04. RESISTENZE PER SIMULAZIONE PONTICELLI.

Δ3. TRANSISTOR MUST BE ASSEMBLED SO THAT THEIR SIDES IN PLASTIC MATERIAL IN TOUCH WITH THE PWB.

Δ3.1 TRANSISTOR DEVONO ESSERE MONTATI CON IL LATO IN PLASTICA A CONTATTO CON IL PWB.

Δ2. CUT THE PIN # 18

Δ2. TAGLIARE IL PIN 18

Δ1. CUT THE PIN # 1.

Δ1. TAGLIARE IL PIN N° 1.

NOTES:

NOTE:

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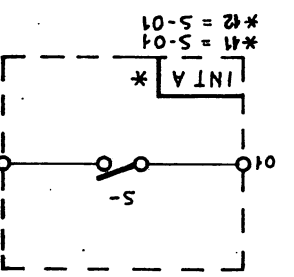
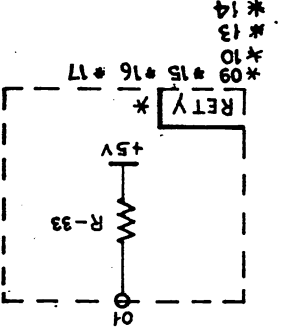
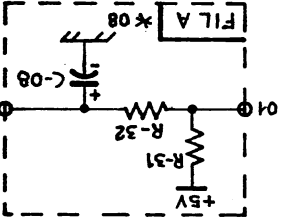
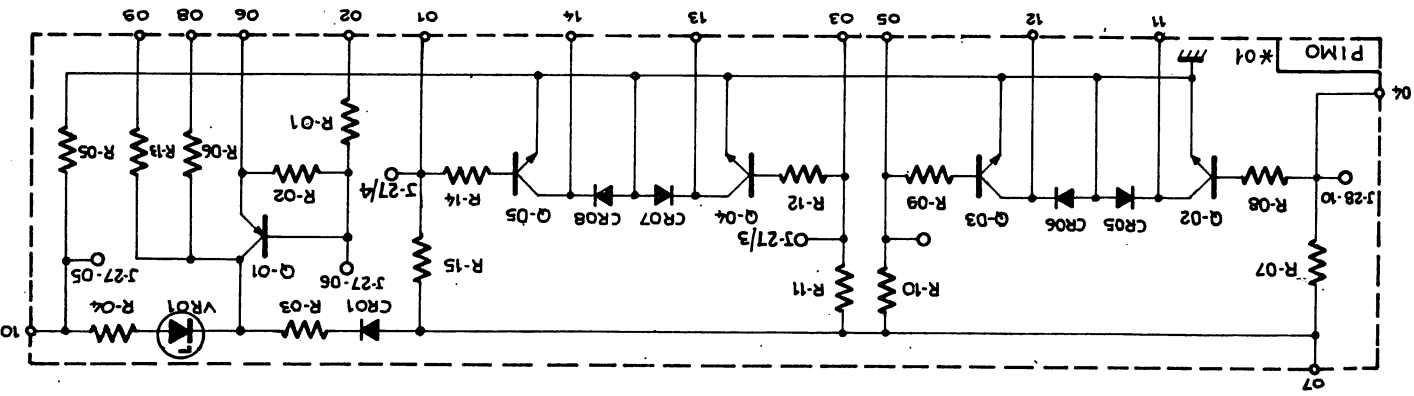
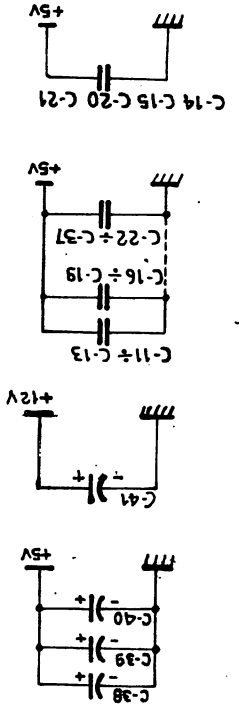
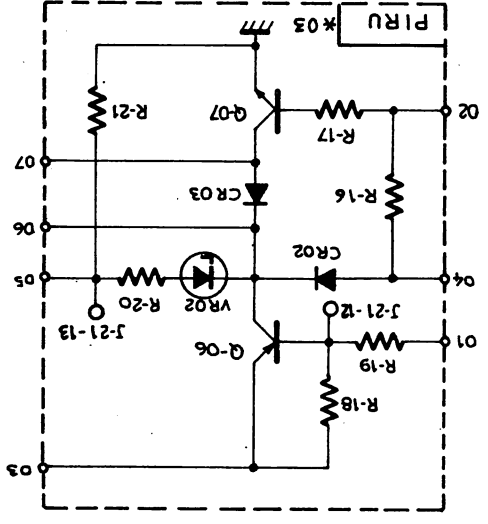
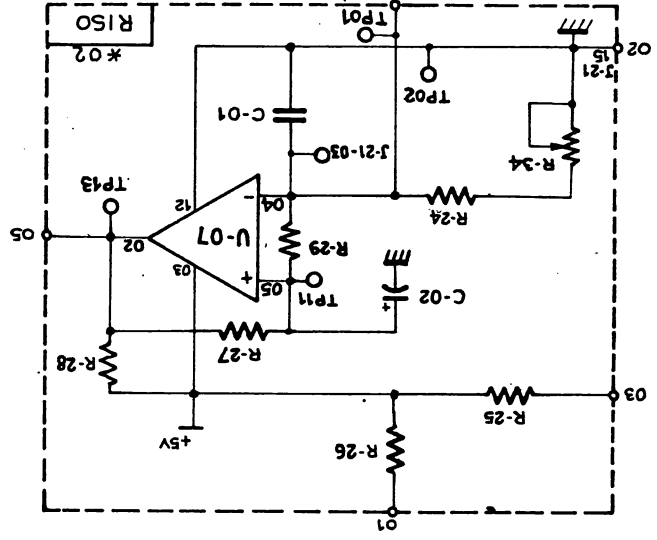
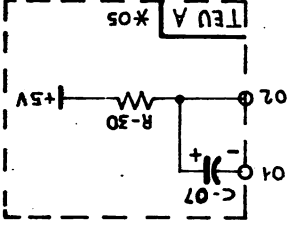
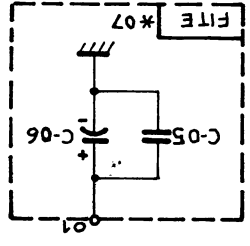
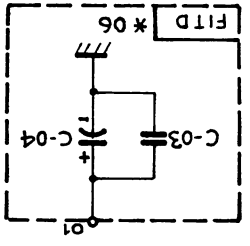
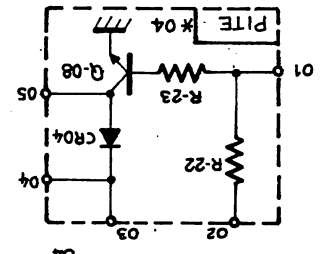
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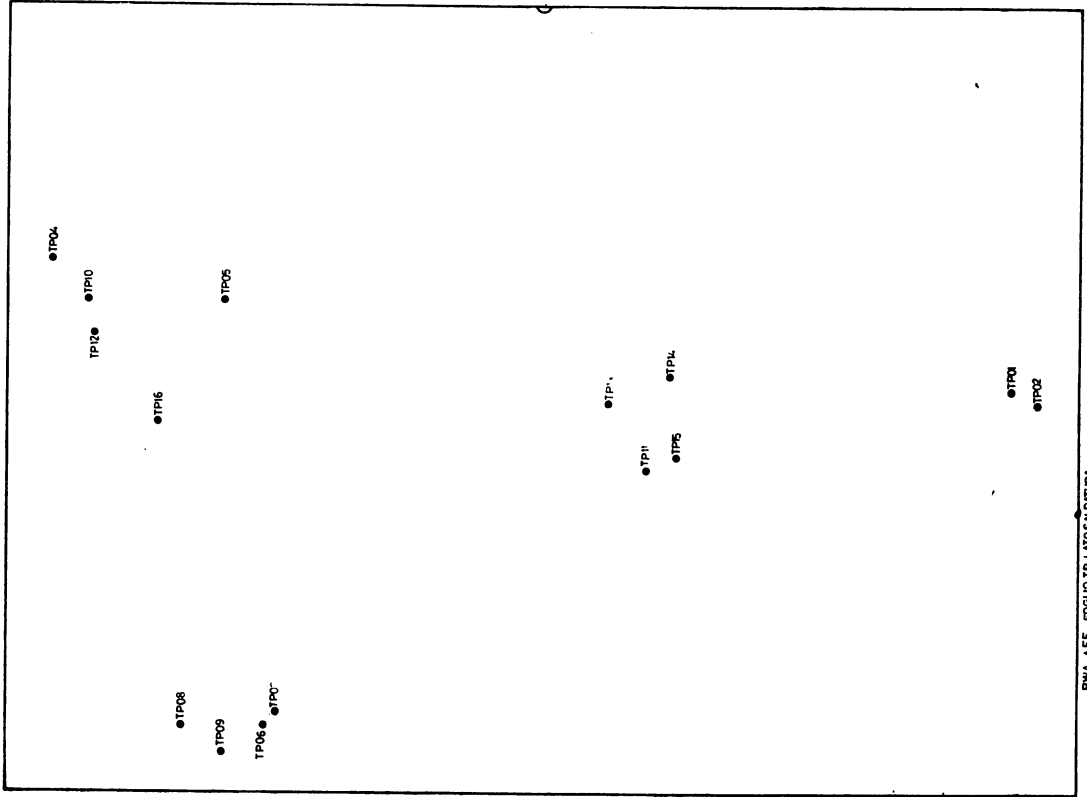
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PWA AFF EDIGLIO IP LATO SINISTRA

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J-20 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	DBR0 *1	DATA BUS
3	DSCB0 *0	SELECTION SIGH.
4	DBR5 *1	DATA BUS
5	DSD80 *0	SELECTION SIGH.
6	DBR2 *1	DATA BUS
7	-5V	
8	+12V	
9	MR1 *11	RESET
10	DBR1 *1	DATA BUS
11	ZER00 *1	
12	ZER00 *1	
13	DBR7 *1	
14	DBR6 *1	
15	DBR4 *1	DATA BUS
16	DBR3 *1	
17		
18		

J-21 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	PASE *1	
3		
4	AFFAR *0	
5	10KHZ *0	CLOCK
6	DS05I *1	SELECTION SIGH
7	PR1 *00	AFF INT.
8	+8.5V	
9	+8.5V	
10	+5V	
11	+5V	
12		
13	ELSER *1	ELECT. MAGH. SENSOR
14	S1234 *1	ENABLE MEM. ADDR.
15		
16	+19V	
17	+19V	
18		

J-22 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2	MEMW *02	
3	AB11 *1	ADDRESS BUS
4	AB8 *1	
5	AB7 *1	
6	AB5 *1	
7	AB2 *1	
8	AB0 *1	
9		
10	MEMR *02	MEMORY READ
11	AB12 *1	ADDRESS BUS
12	AB10 *1	
13	AB9 *1	
14	AB4 *1	
15	AB6 *1	
16	AB3 *1	
17	AB1 *1	
18	AB13 *1	

J-27 (SENSOR)		
PIN	SIGNAL	FUNCTION
1		
2	ZER00 *0	
3	STRA4 *0	LOADING
4	STRA3 *0	PDSIT. SELECTION
5	M0SER *1	ENANCH MOTOR
6		
7	AHF0 *0	FOTO DIODE ANODE
8	ZER00 *0	
9	C0F0 *1	FOTOTR. COLLECT
10	EMF0 *1	FOTOTR. EMITTER
11	ZER00 *1	
12	PULAF *00	AFF PUSH BUTTON

J-28 (MECHAN.)		
PIN	SIGNAL	FUNCTION
1		
2	FASE1 *1	MOTOR PHASE 1
3	FASE2 *1	MOTOR PHASE 2
4	ELT1 *1	STOPPER ELECTR
5	FASE3 *1	MOTOR PHASE 3
6	FASE4 *1	MOTOR PHASE 4
7	ELRU1 *1	PINCH ROLLER 1
8	ELRU2 *0	PINCH ROLLER 2
9	ELT2 *0	STOPPER ELECTR
10	IMSE1 *0	INPUT PHASE 1
11	C0SE1 *1	ENANCH MOTOR 1
12	C0SE2 *1	ENANCH MOTOR 2

PWA AFF



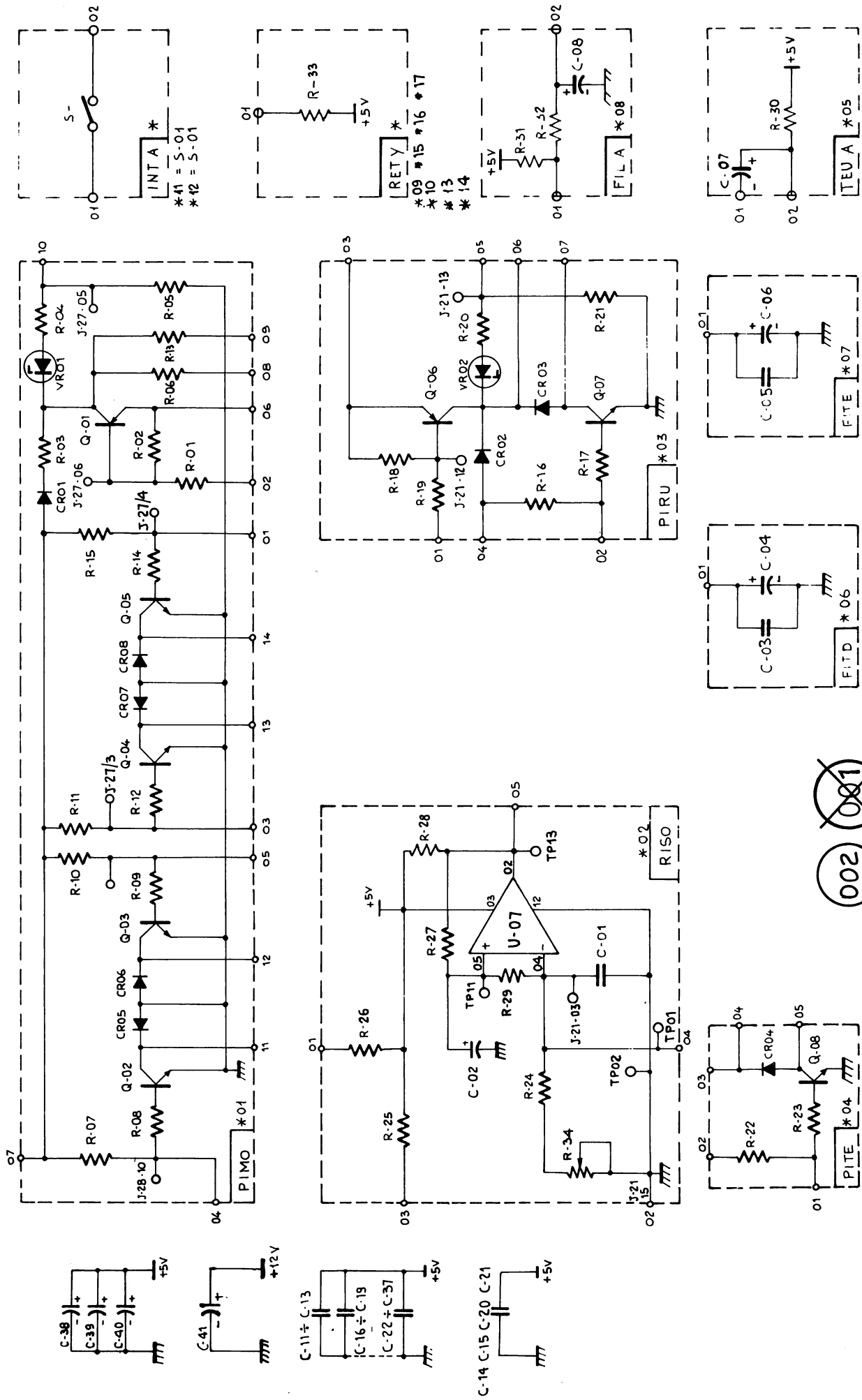
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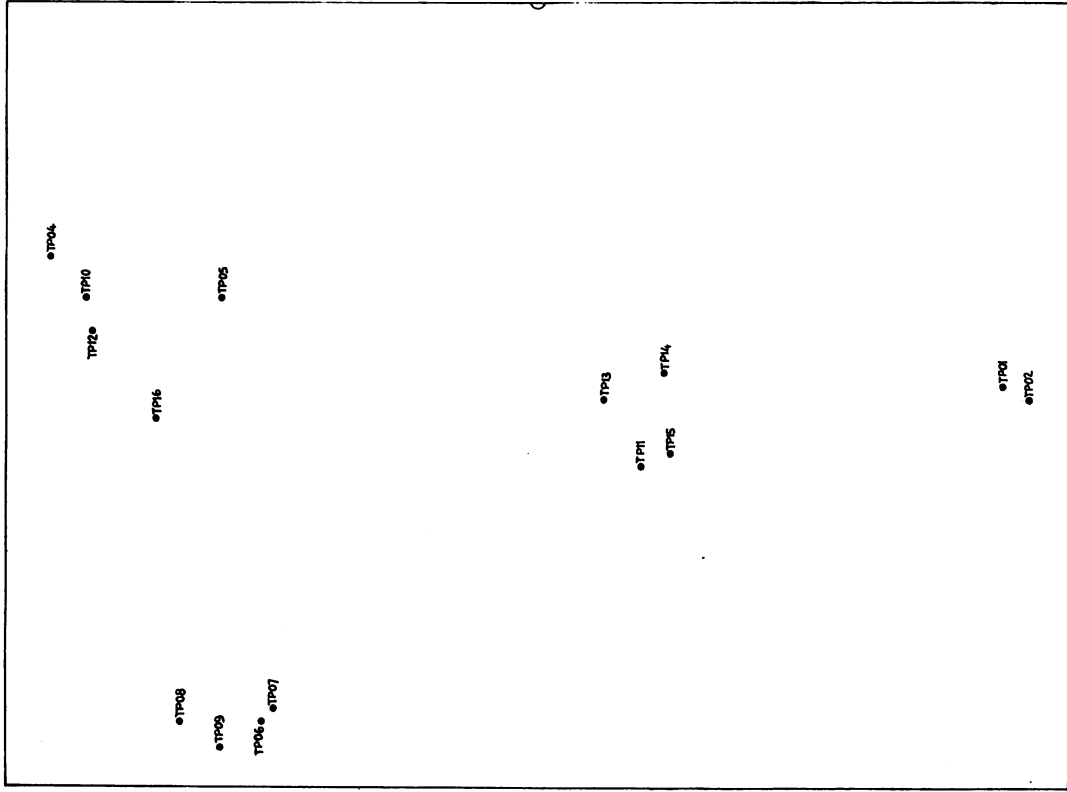


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F.T.O. DISEGNO B 78122655 PAG. REV 3 DA

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PWA AFF-E



ETP04

TP02 ETP0

ETP16

ETP05

ETP08

ETP09

TP06 ETP07

ETP03

ETP11

ETP05

ETP04

ETP01

ETP02

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F.T.O. DISEGNO B 78122655

PAG. REV 4 DA

J-20 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	DBR0 *1	DATA BUS
3	DSCB0 *0	SELECTION SIGH.
4	DBR5 *1	DATA BUS
5	DSDB0 *0	SELECTION SIGH.
6	DBR2 *1	DATA BUS
7	-5V	
8	+12V	
9	MR1 *11	RESET
10	DBR1 *1	DATA BUS
11	ZER0 *1	
12	ZER0 *1	
13	DBR7 *1	
14	DBR6 *1	
15	DBR4 *1	DATA BUS
16	DBR3 *1	
17		
18		

J-21 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	PASE *1	
3		
4	AFFAR *0	
5	10KHZ *0	CLOCK
6	DS05I *1	SELECTION SIGH
7	PR1 *00	AFF INT.
8	+8.5V	
9	+8.5V	
10	+5V	
11	+5V	
12		
13	ELSER *1	ELECT. MAGH. SENSOR
14	S1234 *1	ENABLE MEM. ADDR.
15		
16	+19V	
17	+19V	
18		

J-22 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2	MEMW *02	
3	AB11 *1	ADDRESS BUS
4	AB8 *1	
5	AB7 *1	
6	AB5 *1	
7	AB2 *1	
8	AB0 *1	
9		
10	MEMR *02	MEMORY READ
11	AB12 *1	ADDRESS BUS
12	AB10 *1	
13	AB9 *1	
14	AB4 *1	
15	AB6 *1	
16	AB3 *1	
17	AB1 *1	
18	AB13 *1	

J-27 (SENSOR)		
PIN	SIGNAL	FUNCTION
1		
2	ZER0 *0	
3	STRA4 *0	LOADING
4	STRA3 *0	
5	M0SER *1	ENANCH MOTOR
6		
7	ANF0 *0	FOTO DIODE ANODE
8	ZER0 *0	
9	C0F0 *1	FOTOTR. COLLECT
10	EMF0 *1	FOTOTR. EMITTER
11	ZER0 *1	
12	PULAF *00	AFF PUSH BUTTON

J-28 (MECHAN.)		
PIN	SIGNAL	FUNCTION
1		
2	FASE1 *1	MOTOR PHASE 1
3	FASE2 *1	MOTOR PHASE 2
4	ELT1 *1	STOPPER ELECTR
5	FASE3 *1	MOTOR PHASE 3
6	FASE4 *1	MOTOR PHASE 4
7	ELRU1 *1	PINCH ROLLER 1
8	ELRU2 *0	PINCH ROLLER 2
9	ELTE2 *0	STOPPER ELECTR
10	IMSE1 *0	IMPURT PHASE 1
11	C0SE1 *1	ENANCH MOTOR 1
12	C0SE2 *1	ENANCH MOTOR 2

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI PROPRIETA' CONFIDENZIALE. SE NE CONSIGLIA IL SEGRETO. L'APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

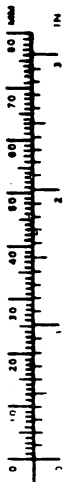
PWA AFF-E

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B	78122655	5/F	DA

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		A M G	



J-20 (DRIVE)		
PIN	SIGNAL	FUNCTION
1	DBR0 *1	DATA BUS
3	DSCBØ*0	SELECTION SIGH.
4	DBR5 *1	DATA BUS
5	DSDBØ*	SELECTION SIGH.
6	DBR2 *1	DATA BUS
7	-5V	
8	+12V	
9	MR1 *11	RESET
10	DBR1 *1	DATA BUS
11	ZERØ *1	
12	ZERØ *1	
13	DBR7 *1	DATA BUS
14	DBR6 *1	
15	DBR4 *1	
16	DBR3 *1	
17		
18		

J-21 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	PASE *1	PAPER SENSOR
3		
4	AFFAR *0	AFF FAUT SIGH.
5	10KHZ *0	CLOCK
6	DSØ5I *1	SELECTION SIGH.
7	PR1 *00	AFF INT.
8	+8.5V	
9	+8.5V	
10	+5V	
11	+5V	
12		
13	ELSER *1	ELECT. MAGM.SENSOR
14	S1234 *1	ENABLE MEM. ADDR.
15		
16	+19V	
17	+19V	
18		

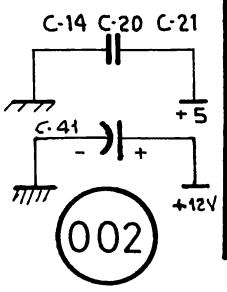
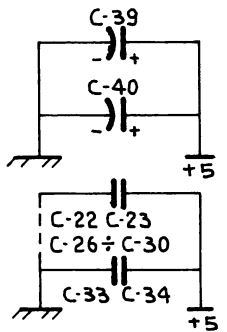
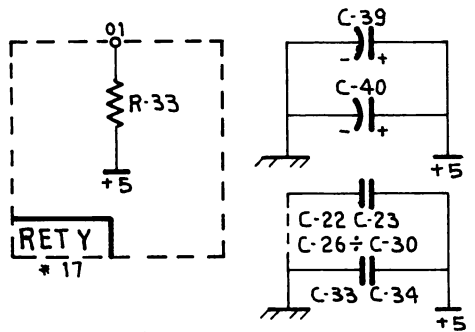
SEMICONDUCTORI SEMICONDUCTORS		
SIMBOLO SYMBOL		RIF.
U-21 U-25 U-15		5
U-1Ø		4
U-14		13
U-20 U-19		3
U-26		14
U-22		15
U-Ø9		16

CONDENSATORI CAPACITORS		
SIMBOLO SYMBOL		RIF.
C-22 C-23 C-26÷C-30		9
C-33 C-34		9
C-14 C-20 C-21		8
C-39 C-40		7
C-41		19

RESISTORI RESISTORS		
SIMBOLO SYMBOL		RIF.
R-33		17
R-45 R-35 R-36 R-37		18

VARI OTHERS	
SIMBOLO SYMBOL	RIF.
J-20 ÷ J-22	10

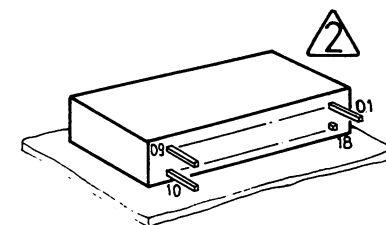
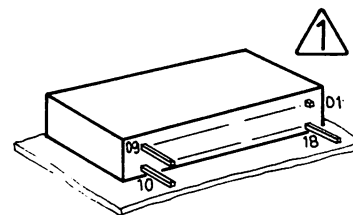
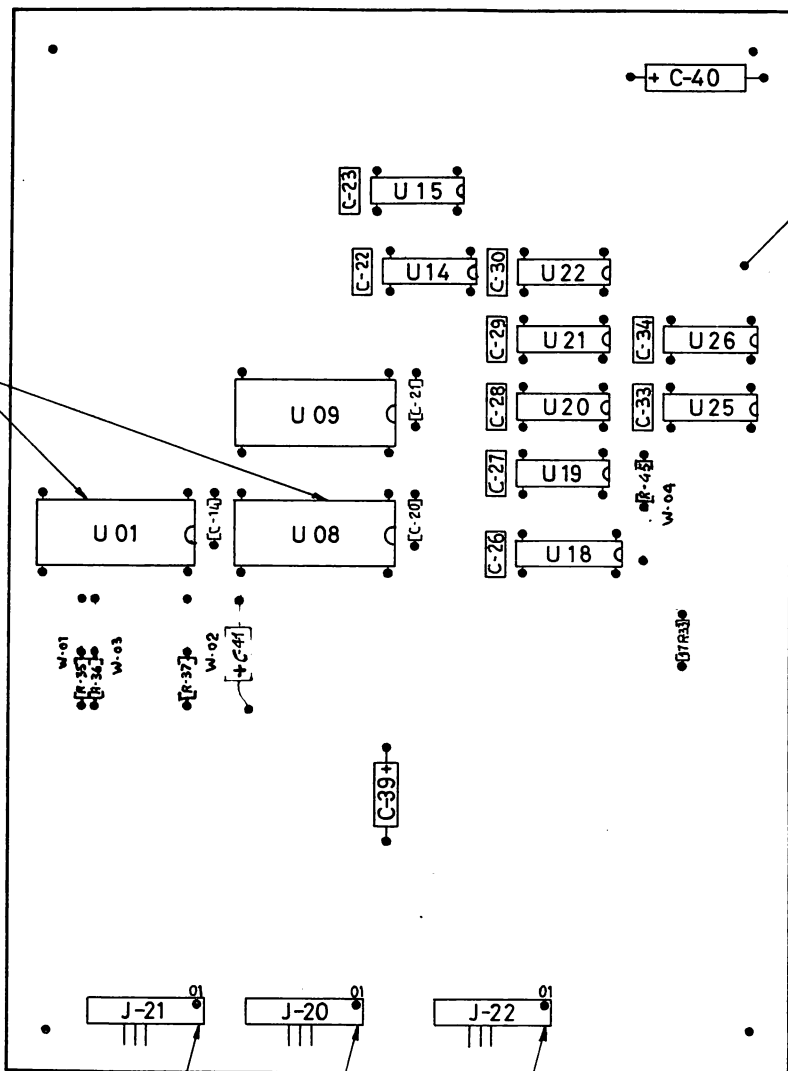
J-22 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2	MEMW *Ø2	MEMORY WRITE
3	AB11 *1	ADDRESS BUS
4	AB8 *1	
5	AB7 *1	
6	AB5 *1	
7	AB2 *1	
8	ABØ *1	
9		
10	MEMR *Ø2	MEMORY READ
11	AB12 *1	ADDRESS BUS
12	AB1Ø *1	
13	AB9 *1	
14	AB4 *1	
15	AB6 *1	
16	AB3 *1	
17	AB1 *1	
18	AB13 *1	



FOR DOCUMENT STATUS SEE REVISION STATUS SHEET		PER IL LIVELLO DI MODIFICA VEDI FOGLIO REVISIONE	
SALVO INDICAZIONE CONTRARIA DIMENSIONI: MILLIMETRI / INCH TOLLERANZE DIMENS. E DI FORMA VEDI: QUOTE IN PROIEZIONE		Honeywell Honeywell Information Systems Italia LOC. PREGNANA MILANESE, ITALIA	
DESCRIZIONE PWA AFF-D		F.TØ DISEGNO B 78119100	
SCALA COD.		DISEGNATO 78-GIU-7 E. GUALAND APPROVATO	
		PAG. 3 REV. BA	

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETÀ DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO È DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E QUALSIASI DISTRIBUZIONE, REPRODUZIONE O ALTRIO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

RIF. SPEC. N°



W01-W04-RESISTORS FOR BRIDGE SIMULATION.

W01-W04-RESISTENZE PER SIMULAZIONE PONTICELLI.

Δ2. CUT THE PIN # 18

Δ2. TAGLIARE IL PIN N° 18

Δ1. CUT THE PIN # 1

Δ1. TAGLIARE IL PIN N° 1

NOTES:

NOTE:

002

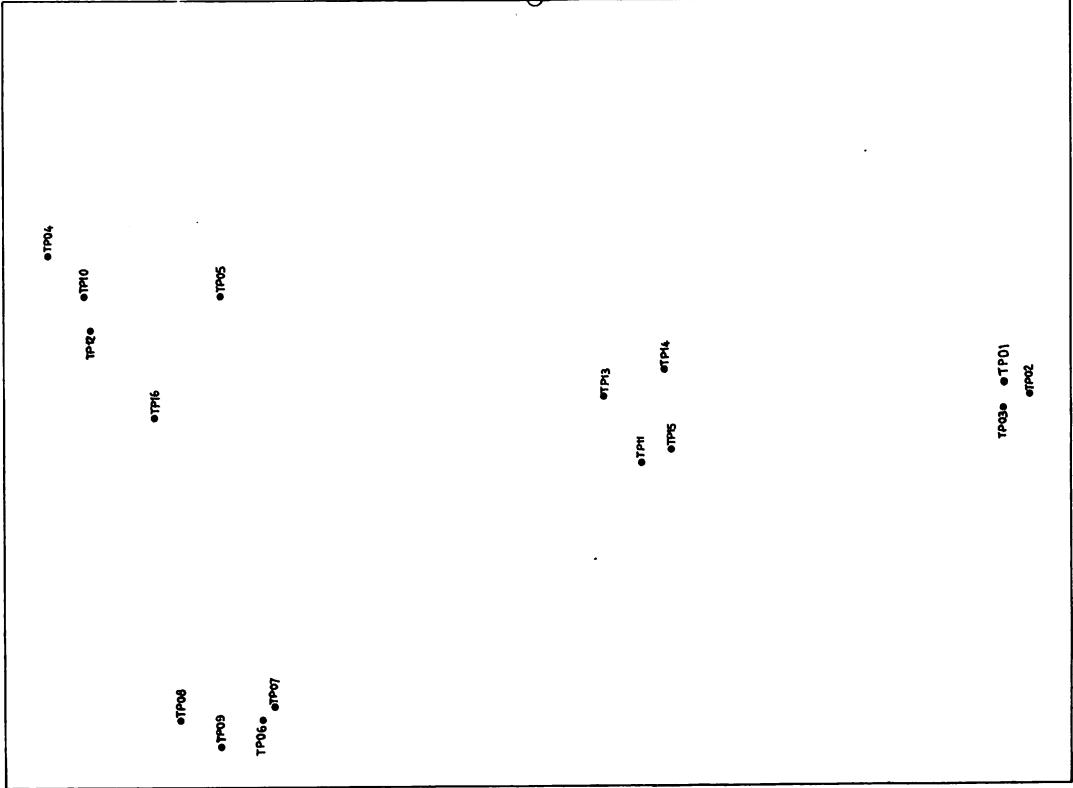
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F.TO DISEGNO
B 78119100

PAG. REV
4 DA

B 78119100



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PWA AFFD

Honeywell

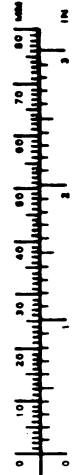
F.TO DISEGNO B 78119100

PAG. REV 5/F BA

PWA AFF - FOGLIO 12 LATO SALINARA

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

REVISIONI			
REV.	NUMERO C.O.	DATA	FIRMA
		A M G	



J-20 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	DBR0 *1	DATA BUS
3	DSCBØ *0	SELECTION SIGH.
4	DBR5 *1	DATA BUS
5	DSDBØ *	SELECTION SIGH.
6	DBR2 *1	DATA BUS
7	-5V	
8	+12V	
9	MR1 *11	RESET
10	DBR1 *1	DATA BUS
11	ZERØ *1	
12	ZERØ *1	
13	DBR7 *1	DATA BUS
14	DBR6 *1	
15	DBR4 *1	
16	DBR3 *1	
17		
18		

J-21 (DRIVE)		
PIN	SIGNAL	FUNCTION
1		
2	PASE *1	PAPER SENSOR
3		
4	AFFAR *0	AFF FAUT SIGH.
5	10KHZ *0	CLOCK
6	DSØSI *1	SELECTION SIGH.
7	PR1 *00	AFF INT.
8	+8.5V	
9	+8.5V	
10	+5V	
11	+5V	
12		
13	ELSER *1	ELECT. MAGN. SENSOR
14	S1234 *1	ENABLE MEM. ADDR.
15		
16	+19V	
17	+19V	
18		

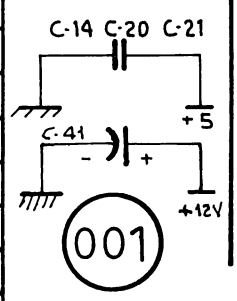
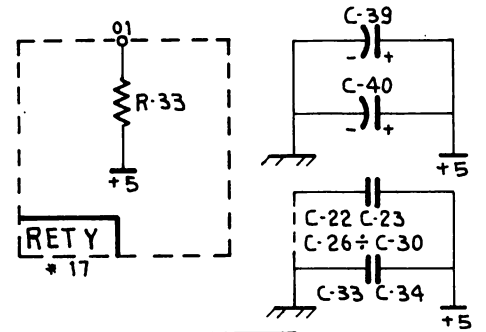
SEMICONDUCTORI SEMICONDUCTORS		
SIMBOLO SYMBOL	RIF.	
U-21 U-25 U-15	5	
U-18	4	
U-14	13	
U-20 U-19	3	
U-26	14	
U-22	15	
U-09	16	

CONDENSATORI CAPACITORS		
SIMBOLO SYMBOL	RIF.	
C-22 C-23 C-26÷C-30	9	
C-33 C-34	9	
C-14 C-20 C-21	8	
C-39 C-40	7	
C-41	19	

RESISTORI RESISTORS		
SIMBOLO SYMBOL	RIF.	
R-33	17	
R-45 R-35 R-36 R-37	18	

VARI OTHERS	
SIMBOLO SYMBOL	RIF.
J-20 ÷ J-22	10

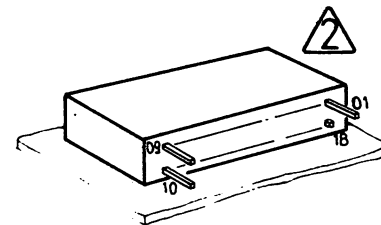
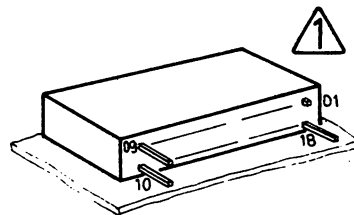
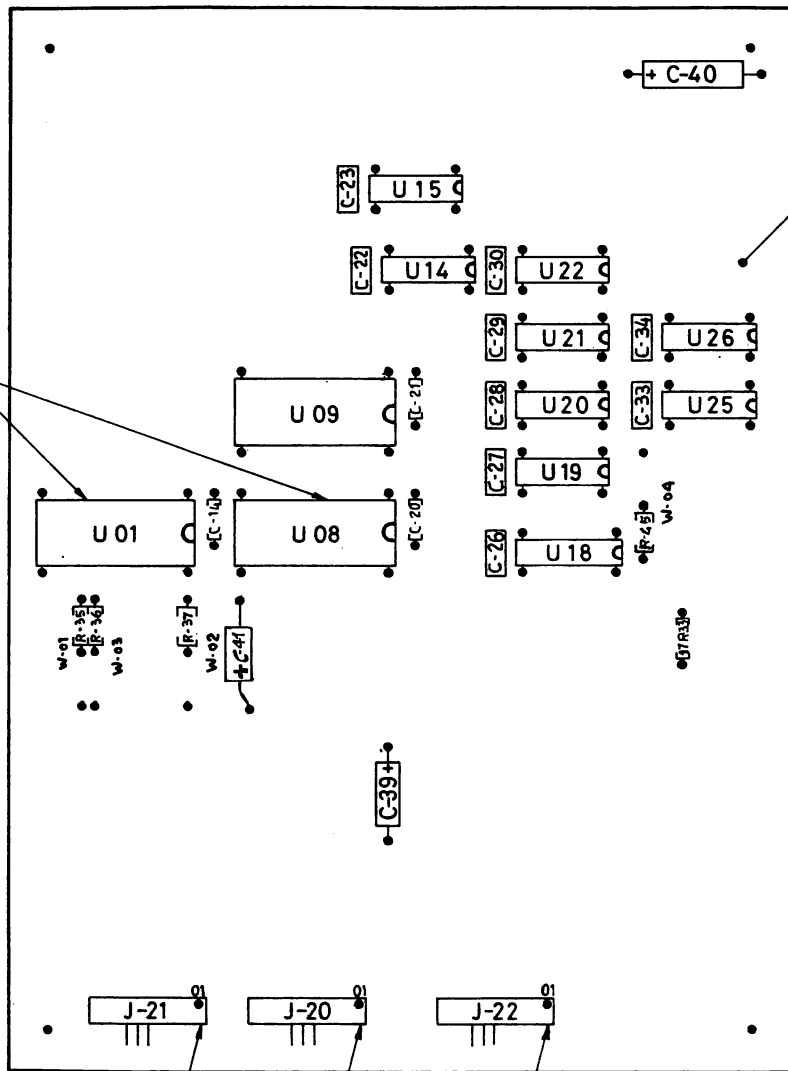
J-22 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2	MEMW *02	MEMORY WRITE
3	AB11 *1	ADDRESS BUS
4	AB8 *1	
5	AB7 *1	
6	AB5 *1	
7	AB2 *1	
8	AB0 *1	
9		
10	MEMR *02	MEMORY READ
11	AB12 *1	ADDRESS BUS
12	AB10 *1	
13	AB9 *1	
14	AB4 *1	
15	AB6 *1	
16	AB3 *1	
17	AB1 *1	
18	AB13 *1	



FOR DOCUMENT STATUS SEE REVISION STATUS SHEET		PER IL LIVELLO DI MODIFICA VEDI FOGLIO REVISIONE	
SALVO INDICAZIONE CONTRARIA DIMENSIONI: MILLIMETRI TOLLERANZE DIMENS. E DI FORMA VEDI: QUOTE IN PROIEZIONE		<p>Honeywell Honeywell Information Systems Italia LOC. PREGNANA MILANESE, ITALIA</p>	
DESCRIZIONE PWA AFF-ED		DISEGNATO 78-GIU-7 E. GUALANDI APPROVATO	
SCALA	COD.	F.TO DISEGNO B 78122656	PAG. REV. 1/3 AA

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

RIF. SPEC. N°



W01-W04-RESISTORS FOR BRIDGE SIMULATION.

W01-W04. RESISTENZE PER SIMULAZIONE PONTICELLI.

Δ2. CUT THE PIN # 18

Δ2. TAGLIARE IL PIN N° 18

Δ1. CUT THE PIN # 1

Δ1. TAGLIARE IL PIN N° 1

NOTES:

NOTE:

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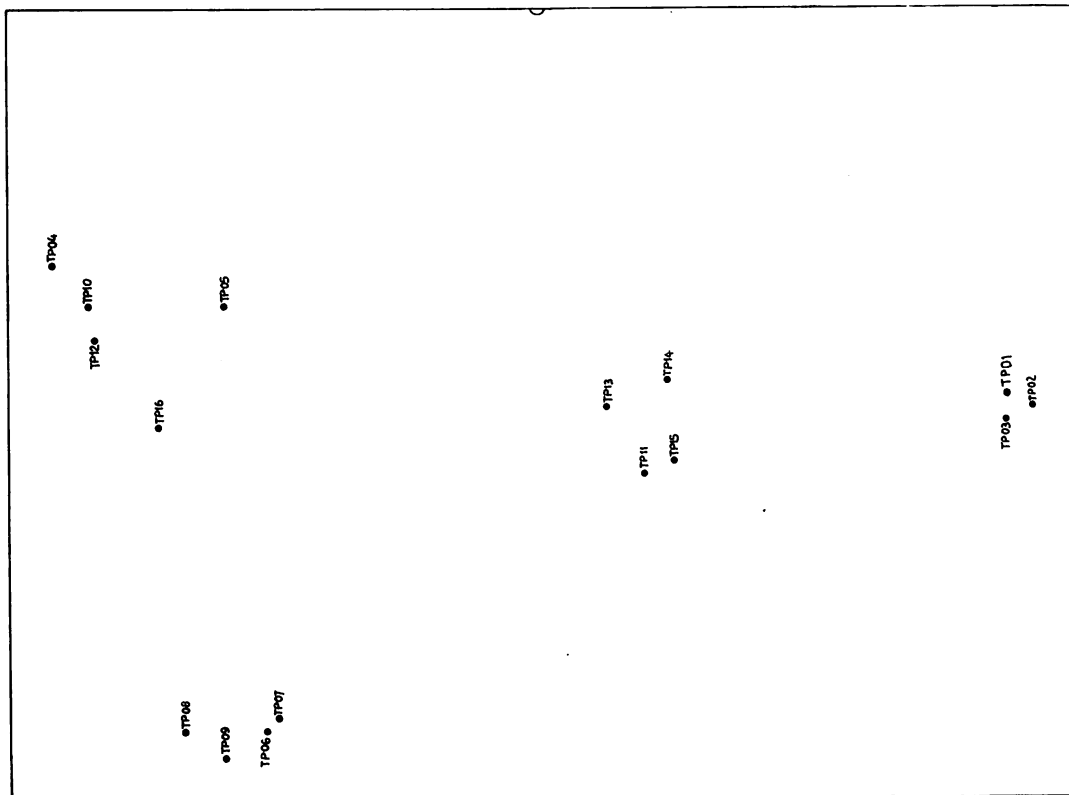
F.TO
B

DISEGNO
78122656

PAG.
2

REV
BA

B78122656



PWA AFF - ED

001

Honeywell

F.T.O. DISEGNO

B 78122656

PAG. REV

3/F AA

PWA AFF EDGILLO TP LATO SALDURA

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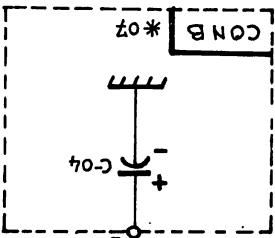
Honeywell

PWA VFU

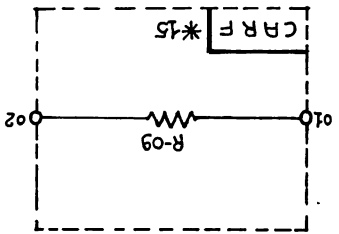
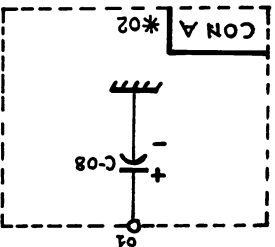
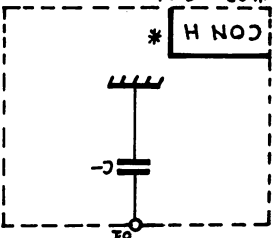
F.T.O. DISEGNO

B 78447219

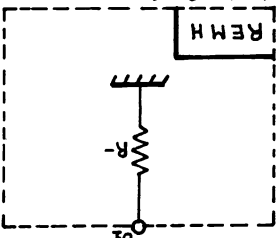
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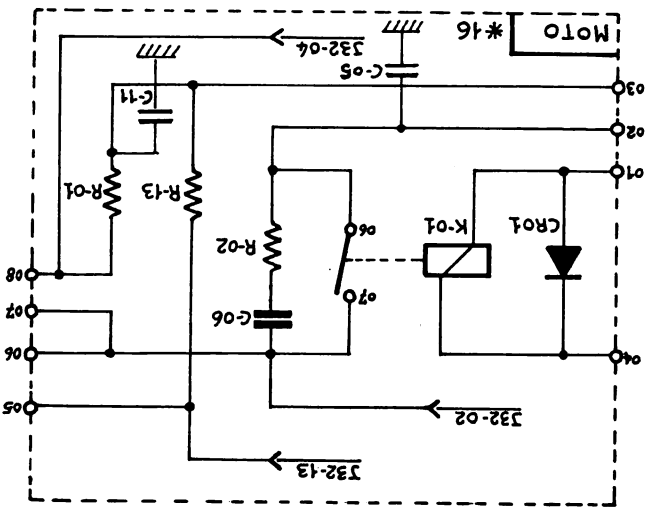
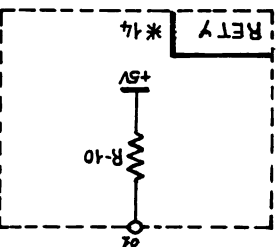
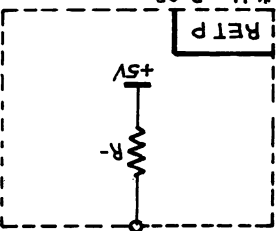
*03 = C-01
 *04 = C-02
 *05 = C-03
 *06 = C-07



*08 = R-06
 *09 = R-07
 *10 = R-08



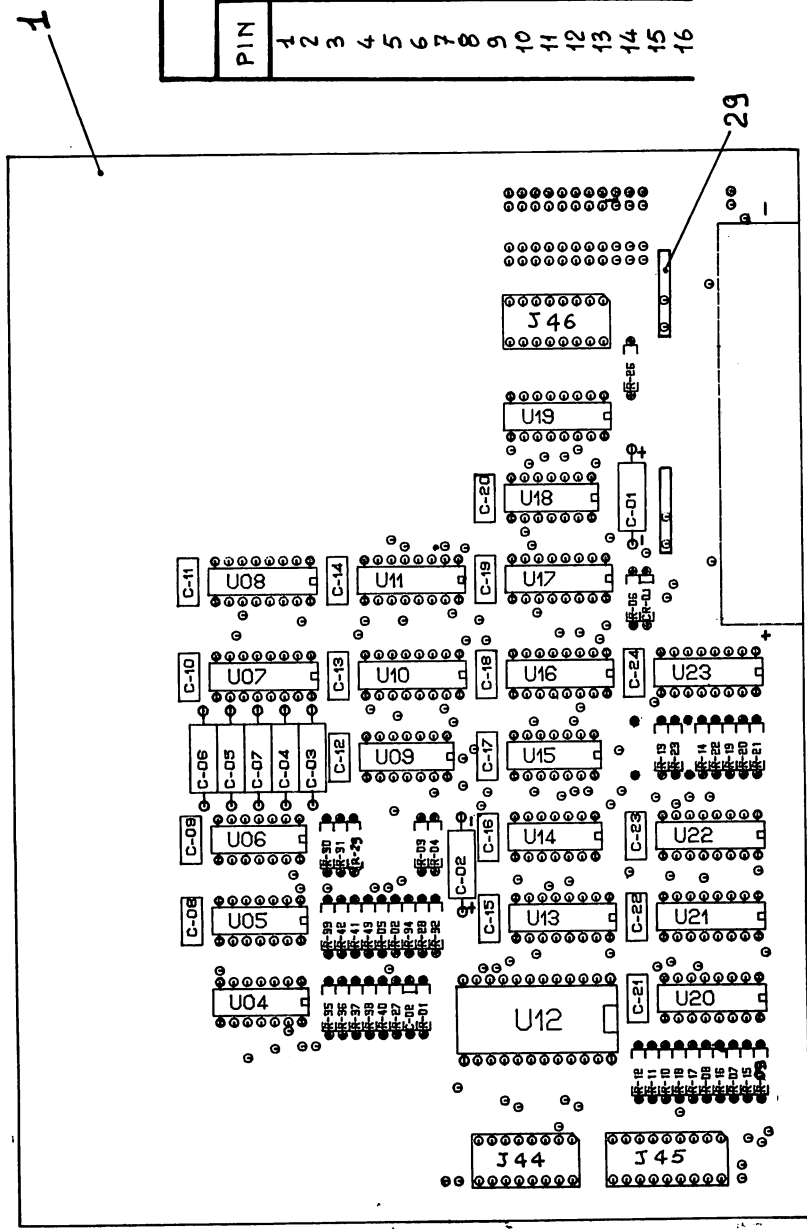
*11 = R-03
 *12 = R-04
 *13 = R-05



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CONNETTORI PWA SFU
CONNECTOR PWA SFU



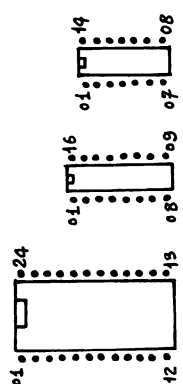
J44 (DRIVE)		J45 (SWITCH)	
PIN	SIGNAL	PIN	SIGNAL
1	DBR1 #1	1	FL10 *0
2	DBR3 #1	2	FL11 *0
3	PRO #00	3	FL12 *0
4	DBR0 #1	4	FL13 *0
5	DBR6 #1	5	FL20 *0
6	DBR2 #1	6	FL21 *0
7	DBR4 #1	7	FL22 *0
8	DBR5 #1	8	FL23 *0
9	+5V	9	GND
10	+19V	10	GND
11	DSD6I #0	11	VT23 *0
12	DBR7 #1	12	VT22 *0
13	IMPPA #01	13	VT21 *0
14	RESET #0	14	VT20 *0
15	ZERΦ #0	15	VT13 *0
16		16	VT12 *0
		17	VT11 *0
		18	VT10 *0

PWA-SFU



F.T.O DISEGNO B 78121028

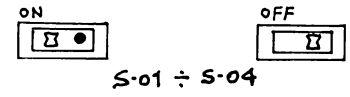
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B 78118448

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REV	NUMERO CO	DATA	FIRMA
		A M G	

STRAPPING

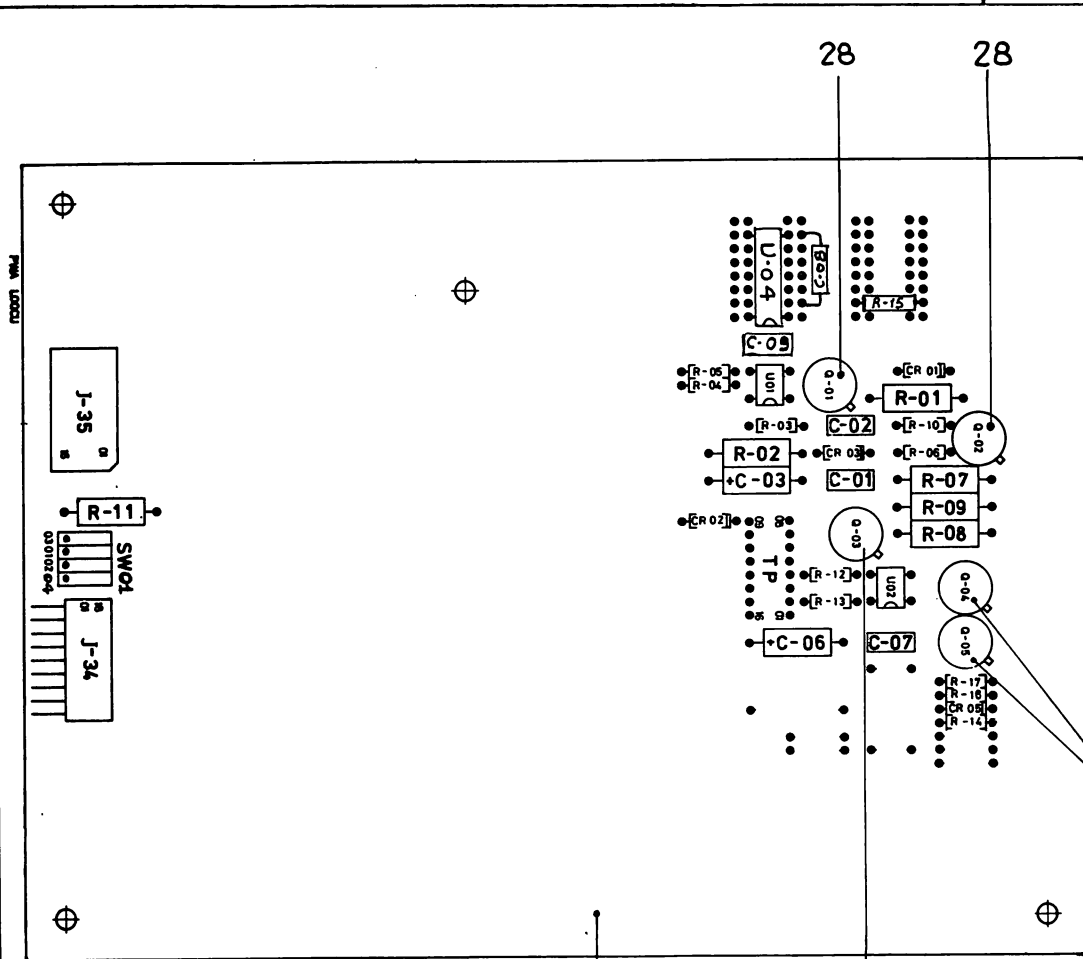


SEMICONDUTTORI SEMICONDUCTORS	
SIMBOLO SYMBOL	RIF.
Q-01 Q-02	19
Q-03 Q-04 Q-05	20
U01 U02	21
U04	31
CRO1 CRO2 CRO5	23
CRO3	24

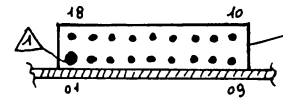
CONDENSATORI CAPACITORS	
SIMBOLO SYMBOL	RIF.
C-06 C-03	16
C-07 C-01 C-02	17
C-09	17
C-08	32

RESISTORI RESISTORS	
SIMBOLO SYMBOL	RIF.
R-01 R-11-R-02	2
R-03	3
R-04	33
R-05 R-13	34
R-12	35
R-08	7
R-14	8
R-06	9
R-10	36
R-07	37
R-09	12
R-16	13
R-17	14
R-15	30

VARI OTHER COMPONENTS	
SIMBOLO SYMBOL	RIF.
J-34	25
J-35	26
S-01 S-02	27
S-03 S-04	27



29



J-34

INATTIVO
DO NOT USE ON NEW BOARD

INATTIVO

Δ1 TAGLIARE IL PIN 01
NOTE:

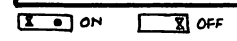
Δ1 CUT THE PIN 01
NOTES:

FOR DOCUMENT STATUS SEE
REVISION STATUS SHEET

PER IL LIVELLO DI MODIFICA
VEDI FOGLIO REVISIONI

LOOCU BOARD-SWITCH SETTING

THIS INSTRUCTIONS ARE USEFUL FOR ALL LCSP NOS MODELS (SARA-R05Y24-R05Y26)	S01 03	S01 01	S01 02
INTERFACE CURRENT PROVIDED BY EXTERNAL SOURCE	OFF	ON	OFF
INTERFACE CURRENT PROVIDED BY LOOCU BOARD	ON	OFF	ON



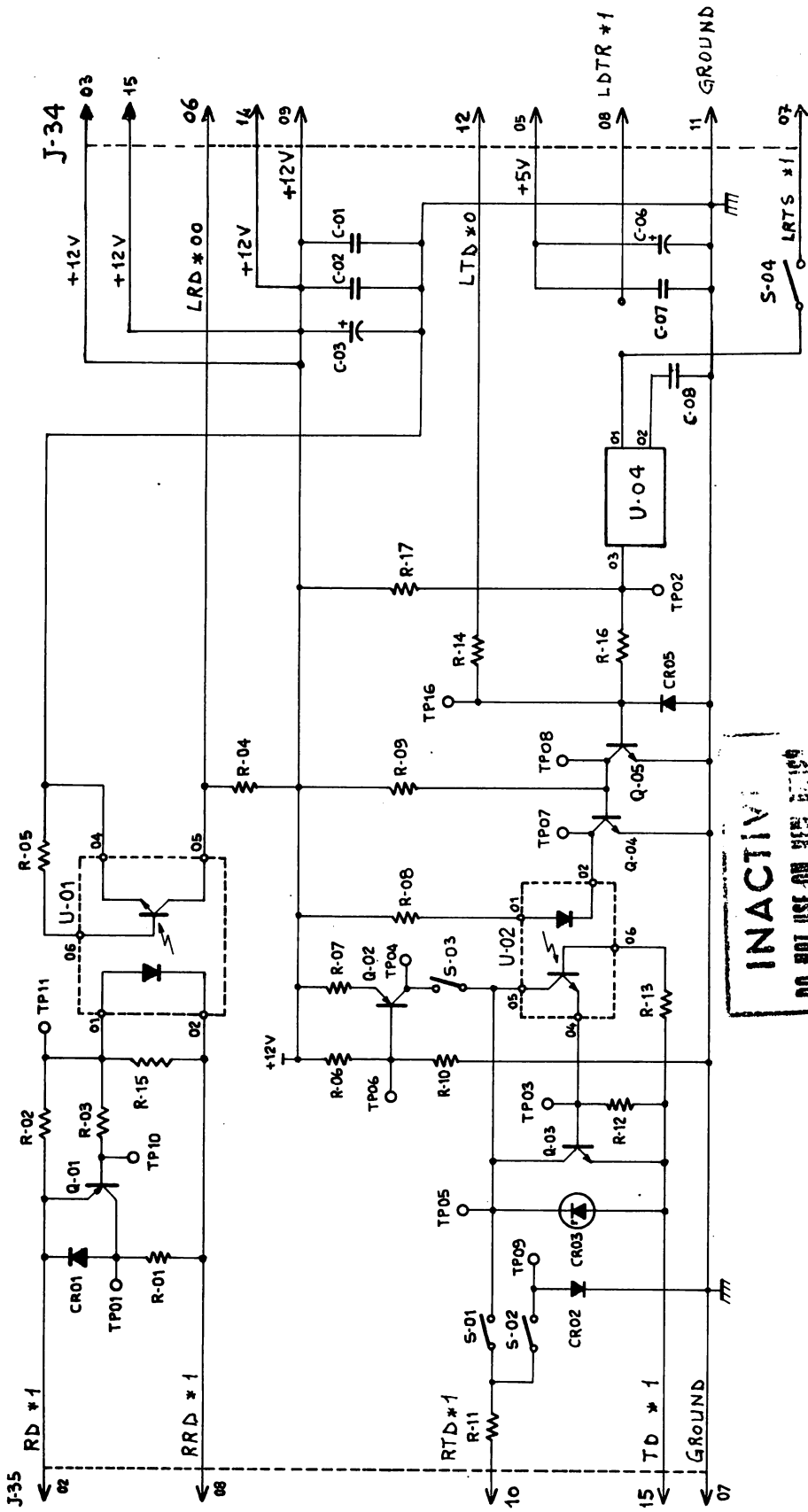
002

SALVO INDICAZIONE CONTRARIA		MAT.	
DIMENSIONI: MILLIMETRI INCH			
TOLLERANZE DIMENS. E DI FORMA VEDI:		TT.	
QUOTE IN		FIN.	
PROIEZIONE			
SCALA 1:1	COD.	DISEGNATO 76 OTT. 19 <i>Cecchini Sebastianus</i> APPROVATO	

Honeywell Honeywell Information Systems Italia LOC. PREGNANA MILANESE ITALIA			
DESCRIZIONE PWA LOOCU			
F.T.O	DISEGNO	PAG.	REV.
B	78118448	3	CB

RIF. SPEC. N°

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL
 INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO IN
 USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO
 APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS.



INACTIVE
DO NOT USE OR REPAIR

INATTIVO
NON UTILIZZARE O RIPARARE

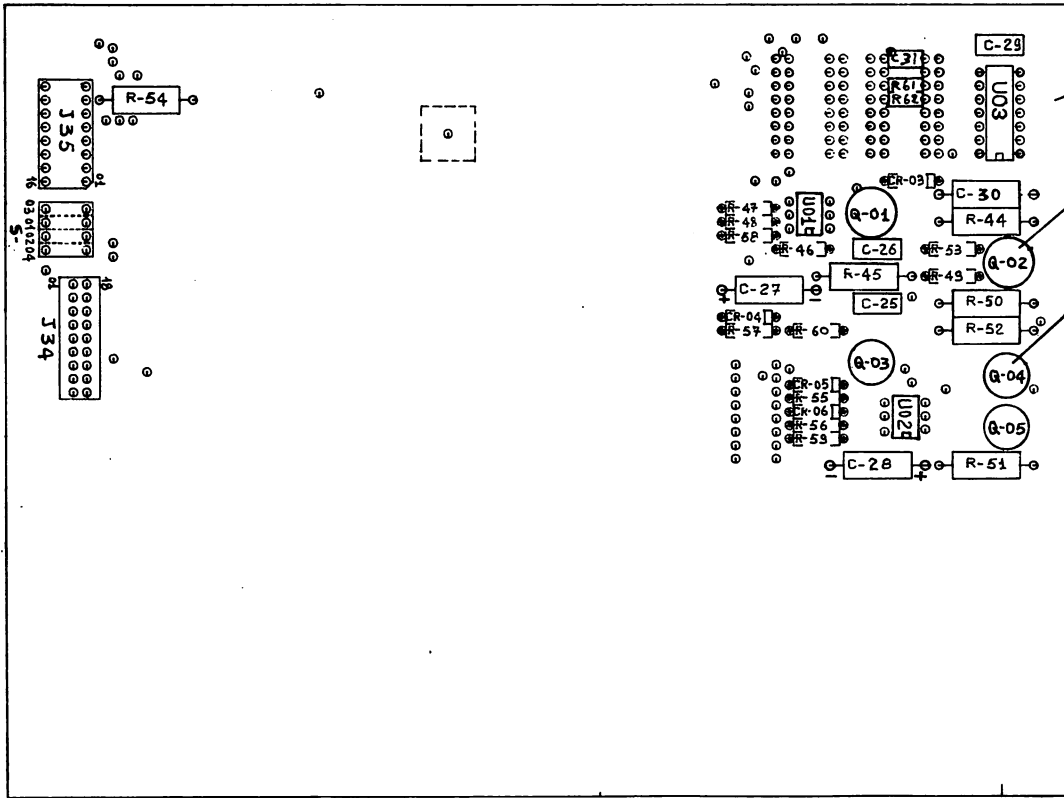
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Honeywell

STRAPPING S-01÷S-04

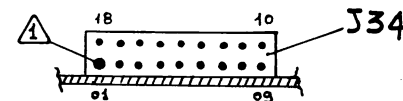


CONNETTORI PWA LØØCU
CONNECTOR PWA LØØCU



J34 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2		
3	+12V	
4		
5	+5V	
6	LRD *00	RECEIVED DATA
7	LRTS *1	REQUEST TO SEND
8	LDTR *1	DATA TERMINAL READY
9	+12V	
10		
11	ZERØ *1	
12	LTD *0	TRANSMITTED DATA
13		
14	+12V	
15	+12V	
16		
17		
18		

J35 (MOD. INTER)		
PIN	SIGNAL	FUNCTION
1		
2	RD *1	RECEIVED DATA
3		
4		
5		
6		
7	ZERØ *1	
8	RRD *1	RECEIVED DATA RETURN
9		
10	RTD *1	TRANSMITTED DATA RETURN
11		
12		
13		
14		
15	TD *1	TRANSMITTED DATA
16		



LØØCU BOARD-SWITCH SETTING

THIS INSTRUCTIONS ARE USE EUL FOR ALL LCSP MOS MODELS (SARA-ROSY24-ROSY26)	S 03	S 01	S 02	S 04
INTERFACE CURRENT PROVIDED BY EXTERNAL SOURCE	OFF	ON	OFF	ON
INTERFACE CURRENT PROVIDED BY LØØCU BOARD	ON	OFF	ON	ON

NOTES:
Δ1. CUT THE PIN 01

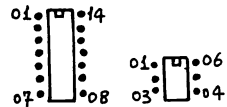
NOTE:
Δ1. TAGLIARE IL PIN 01

PWA-LØØCU

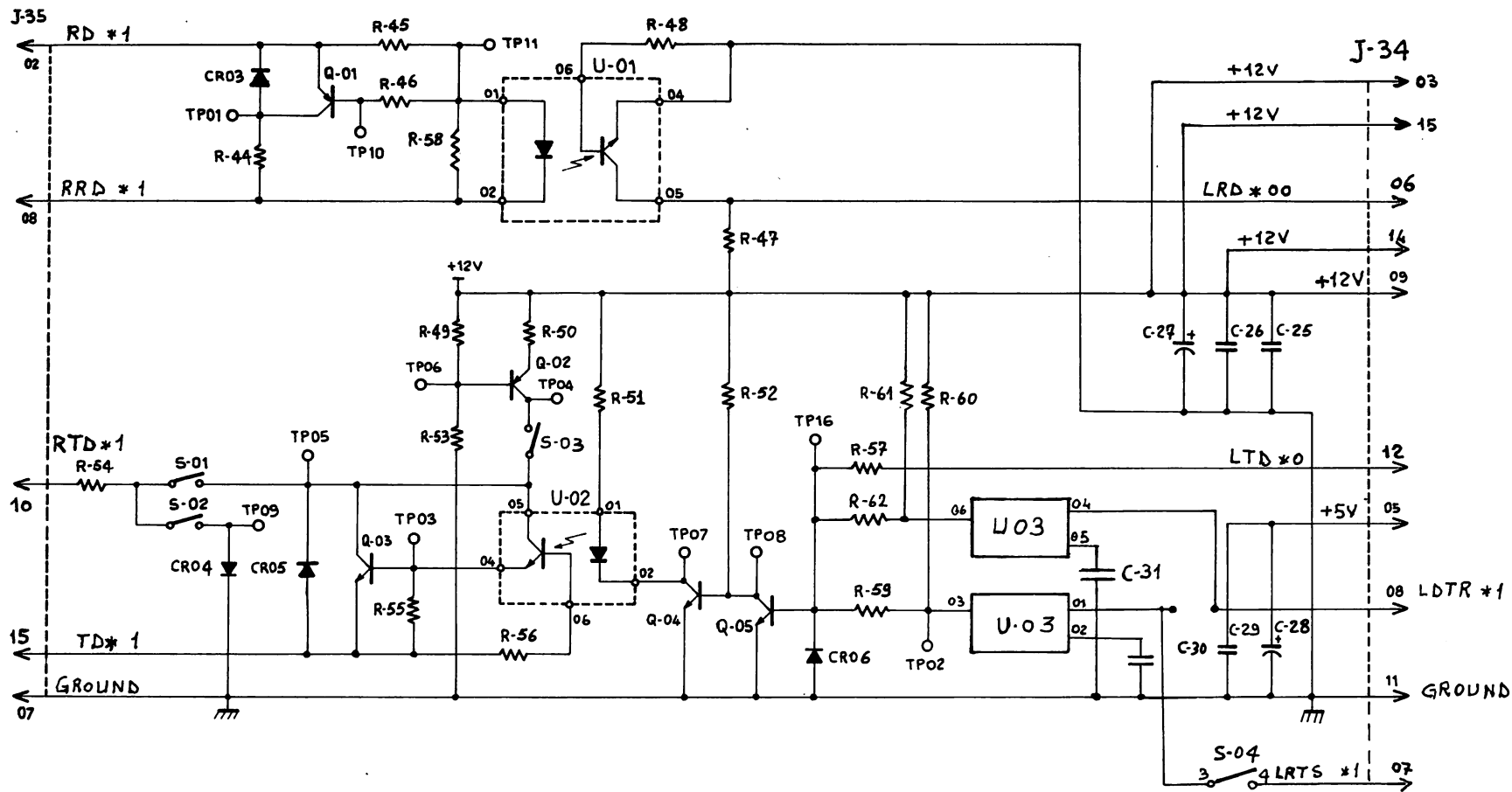
Honeywell

F.T.O. DISEGNO
B 78121029

PAG. REV
2 FA



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PWA-L00CU

Honeywell

F.TO	DISEGNO
B	78121029

PAG.	REV.
3/F	FA

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STRAPPING 501÷S-04



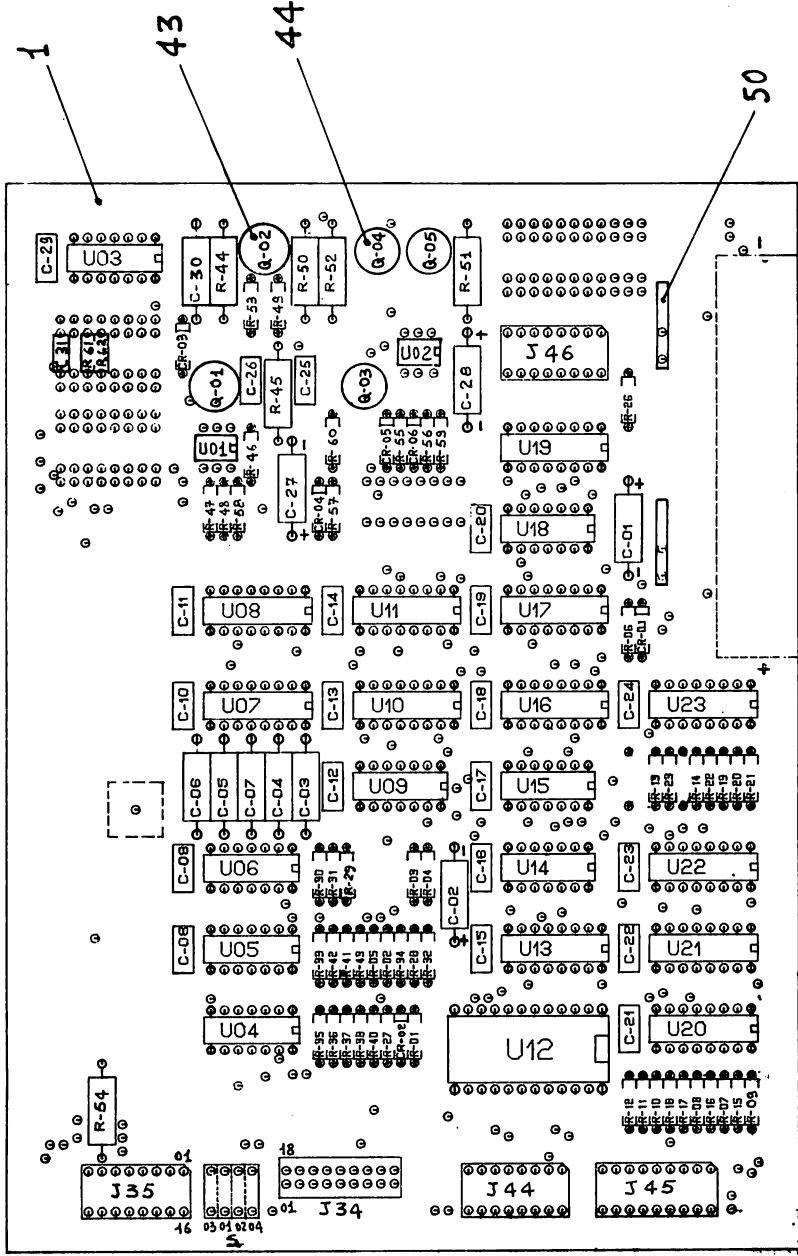
LODCU BOARD - SWITCH SETTING

THIS INSTRUCTIONS ARE USEFUL FOR ALL LCSP M05 MODELS (SARA-R05Y24-R05Y26)

INTERFACE CURRENT PROVIDED BY EXTERNAL SOURCE

INTERFACE CURRENT PROVIDED BY LODPCU BOARD

S	S	S	S
03	01	02	04
OFF	ON	OFF	ON
ON	OFF	ON	ON



Δ1. CUT THE PIN 01

NOTES:

Δ1-TAGLIARE IL PIN 01

NOTE:

PWA - L0SFU

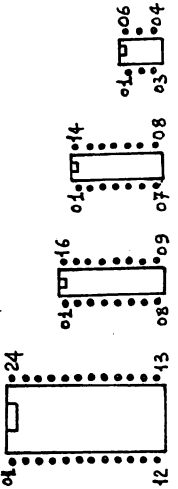
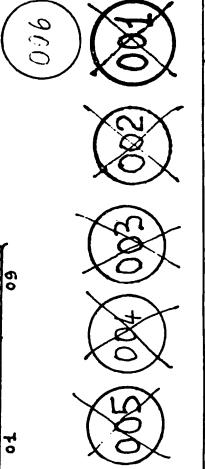
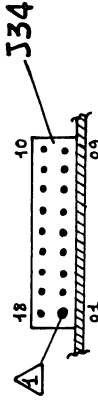
Honeywell

F.TO DISEGNO

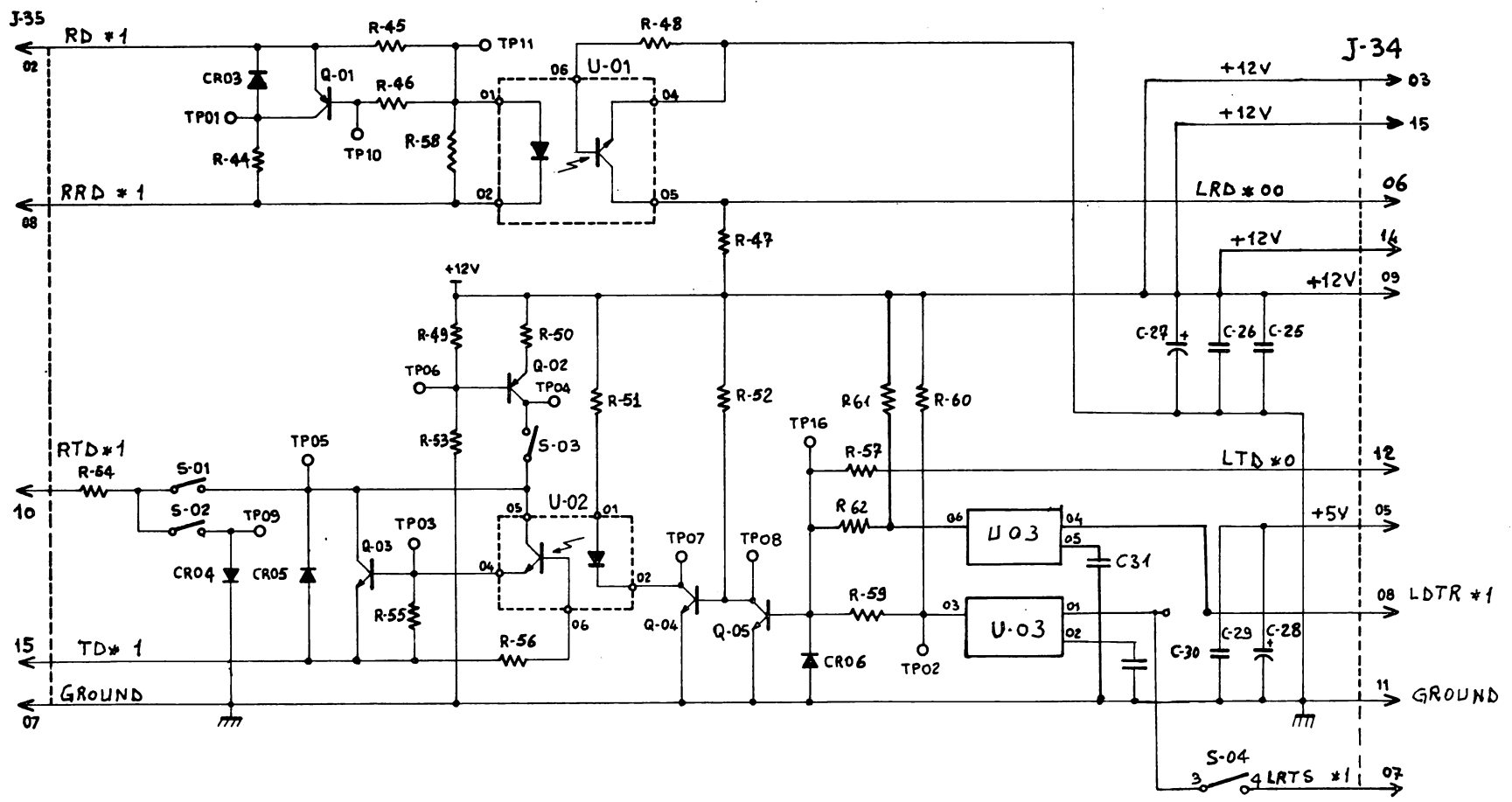
B 78120683

PAG. REV

2 GA



QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' INTELLETTUALE DI HONEYWELL. IL SUO USO INFRUONTO O IN QUALSIASI DISTRIBUZIONE, TRAMITE QUALSIASI MEZZO, E' PROIBITO SENZA LA PERMESSA SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE UN DOCUMENTO CONFIDENZIALE. SE VOI RICEVERE QUESTO DOCUMENTO SENZA AVERE LA PERMESSA SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA, IL SUO USO E' PROIBITO.



QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL
 INFORMATION SYSTEMS ITALIA; ESSO E' DA CONSIDERARE COME DOCUMENTO DI
 USO INTERNO. OGNI E QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO
 APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

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 005 003 002

PWA-L0SFU (PWA L00CU)

Honeywell

F.TO B DISEGNO 78120683

PAG. 3 REV GA

CONNETTORI PWA LØSFU
CONNECTOR PWA LØSFU

J34 (CPU)		
PIN	SIGNAL	FUNCTION
1		
2		
3	+12V	
4		
5	+5V	
6	LRD *00	RECEIVED DATA
7	LRTS *1	REQUEST TO SEND
8	LDTR *1	DATA TERMINAL READY
9	+12V	
10		
11	ZERφ *1	
12	LTD *0	TRANS
13		
14	+12V	
15	+12V	
16		
17		
18		

J35 (MOD. INTER.)		
PIN	SIGNAL	FUNCTION
1		
2	RD *1	RECEIVED DATA
3		
4		
5		
6		
7	ZERφ *1	
8	RRD *1	RECEIVED DATA RETURN
9		
10	RTD *1	TRANSMITTED DATA RETURN
11		
12		
13		
14		
15	TD *1	TRANSMITTED DATA
16		

J44 (DRIVE)		
PIN	SIGNAL	FUNCTION
1	DBR1 *1	DATA BUS
2	DBR3 *1	" "
3	PRO *00	VFU INT.
4	DBR0 *1	} DATA BUS
5	DBR6 *1	
6	DBR2 *1	
7	DBR4 *1	
8	DBR5 *1	
9	+5V	
10	+19V	
11	DS06I *0	SELECTION SIGN.
12	DBR7 *1	DATA BUS
13	IMPPA *01	MOTOR PAP. PULSE
14	RESET *0	RESET
15	ZERφ *0	
16	DSE8φ *0	SELECTION SIGNAL

J45 (SWITCH)		
PIN	SIGNAL	FUNCTION
1	FL10 *0	} FORMAT LENGTH
2	FL11 *0	
3	FL12 *0	
4	FL13 *0	
5	FL20 *0	
6	FL21 *0	
7	FL22 *0	
8	FL23 *0	
9	GND	} VERTICAL TABULATION
10	GND	
11	VT23 *0	
12	VT22 *0	
13	VT21 *0	
14	VT20 *0	
15	VT13 *0	
16	VT12 *0	
17	VT11 *0	
18	VT10 *0	

006 004 001
005 003 002

PWA -LØSFU

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REVISIONI			
REV.	NUMERO C.O.	DATA	FIRMA
		A M G	

J-50 ASYNC. (CPU)

PIN	SIGNAL	FUNCTION
1		
2	-12V	
3	LRD *0	RECEIVED DATA
4	LCALT *1	CALLING INDICATOR
5	LSTD *0	SECONDARY TX DATA
6	LSCTS *10	SECOND. CLEAR TO SEND
7	LSRTS *1	SECOND. REQVE. TO SEND
8	LDTR *1	DATA TERMIN. READY
9	LRTS *1	REQUEST TO SEND
10	LDSR *1	DATA SET READY
11	STD *1	TRANSM. DATA
12	GROUND	
13		
14	+12V	
15	DASTC *1	DATA STREAM CONTROL
16	LCTS *1	CLEAR TO SEND
17	LRELI *1	RECEIVED LINE
18	CHIAVE	

J-48 ASYNC. (MODEM INTERF.)

PIN	SIGNAL	FUNCTION
1	-12V	
2	LRD *00	RECEIVED DATA
3	LCALI *1	CALLING INDICAT.
4	LSTD *0	SECOND. TX DATA
5	LSCTS *10	" CLEAR TO SE.
6	LSRTS *1	" REQUEST "
7	LDTR *1	DATA TERM. READ
8	LRTS *1	REQUEST TO SEND.
9	LDSR *1	DATA SET READY
10	LTD *1	TRANSMITT. DATA
11	GROUND	
12	LSRD *00	SECOND. RX DATA
13	+12V	
14	DASTC *1	DATA STREAM. CONT.
15	LCTS *10	CLEAR TO SEND
16	LRELI *1	RECEIVED LINE SIGNAL DETECTOR

SEMICONDUITORI SEMICONDUCTORS

SIMBOLO SYMBOL	RIF.
U-08 U-04	2
U-07 U-06 U-05 U-03	3
U-02 U-01	3
Q-01	4
Q-02	5
Q-03 Q-08 Q-09	6
Q-04 Q-05 Q-06	7
Q-07 Q-10	8
CR04 CR05 CR07 CR08	9
CR09 CR10 CR11	9
VR01 VR03	10
VR02	11

CONDENSATORI CAPACITORS

SIMBOLO SYMBOL	RIF.
C-01 C-08	12
C-02 C-04 C-06 C-10	13
C-14 C-16 C-20	13
C-22 C-23 C-25	13
C-03 C-15 C-17	14
C-05 C-07 C-09 C-12	15
C-13	16

RESISTORI RESISTORS

SIMBOLO SYMBOL	RIF.
R-01 R-02	17
R-03	18
R-04 R-05	19
R-06 R-07 R-08 R-13	20
R-16 R-22	20
R-09	21
R-10	22
R-11	23
R-12	24
R-14 R-15	25
R-17	26
R-18	27
R-19 R-25	28
R-20 R-23	29
R-21 R-24	30

J-49 SYNC. (CPU)

PIN	SIGNAL	FUNCTION
1		
2	-12V	
3	LRELI *1	RECEIVED LINE
4	SSTBY *1	TRASM. CLOCK/STAND BY
5	LRD *0	RECEIVED DATA
6	LRTS *1	REQUEST TO SEND
7	LDTR *1	DATA TERMIN. READY
8	+12V	
9	LTRXC *1	RECEIVER CLOCK
10	GROUND	
11	LTD *0	TRANSMITTED DATA
12	LDARA *1	DATA RATE SELECTOR
13	LCTS *1	CLEAR TO SEND
14	LDSR *1	DATA SET READY
15	LTXC *1	TRASM. ELEM. TIMING
16	LRXC *1	RECEIV. ELEM. TIMING
17	CHIAVE	
18		

J-39 SYNC. (MODEM INTERF.)

PIN	SIGNAL	FUNCTION
1	-12V	RECEIVED LINE SIGNAL DETECTOR
2	LRELI *1	SIGNAL DETECTOR
3	SSTBY *110	SELECT STAND BY
4		
5	LRD *00	RECEIVED DATA
6	LRTS *1	REQUEST TO SEND
7	LDTR *1	DATA TERMINAL READY
8	+12V	
9	LTRXC *1	TX CLOCK
10	GROUND	
11	LTD *0	TRANSMITT. DATA
12	LDARA *1	DATA RATE SELE.
13	LCTS *10	CLEAR TO SEND.
14	LDSR *1	DATA SET READY
15	LTXC *10	TX CLOCK
16	LRXC *10	RX CLOCK

J-38 (POWER SUPPLY)

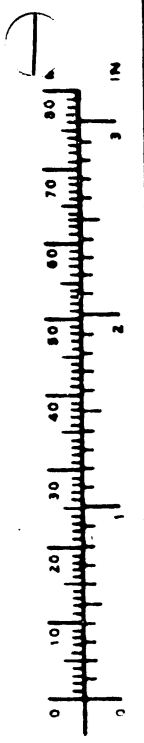
PIN	SIGNAL	FUNCTION
1	-19V	
2	+8.5V	

VARI OTHERS

SIMBOLO SYMBOL	RIF.
S-01 ÷ S-04	31
J-49 J-50	32
J-39 J-48	33
J-38	34

FOR DOCUMENT STATUS SEE REVISION STATUS SHEET		PER IL LIVELLO DI MODIFICA VEDI FOGLIO REVISIONI	
SALVO INDICAZIONE CONTRARIA		MAT.	
DIMENSION: MILLIMETRI INCH		TT.	
TOLLERANZE DIMENS. E DI FORMA VEDI:		FIN.	
QUOTE IN:		DISEGNATO 78-6-1 E. GUALANO APPROVATO	
PROIEZIONE:		DESCRIZIONE PWA MIPI	
SCALA	COD.	F.T.O DISEGNO B 78121582	PAG. REV. 1/3 AA

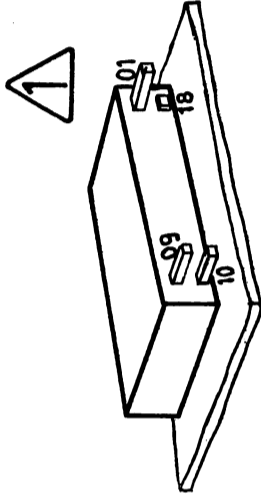
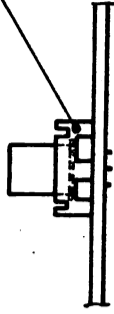
001



QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

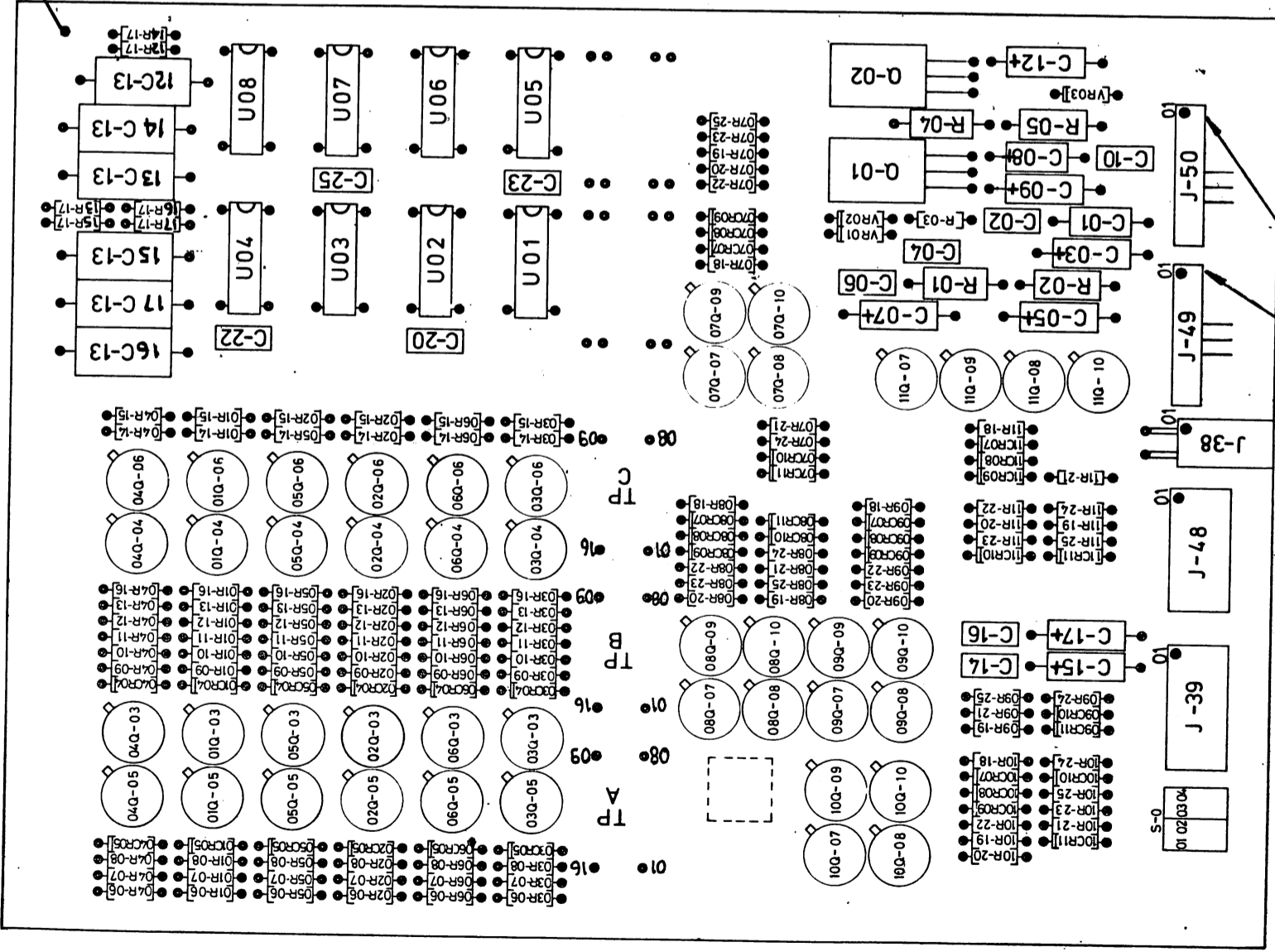
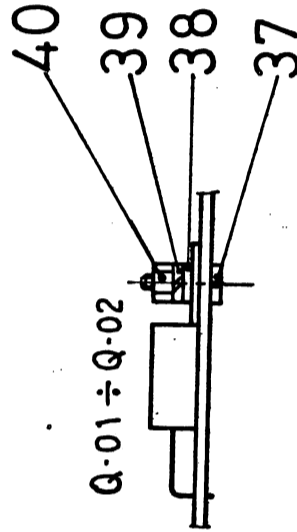
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Q-03 ÷ Q-10



PWA MIPI SWITCH SETTING

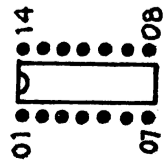
S-01	S-02	S-03	S-04
LOCAL CLOCK CONN.	ON	ON	OFF
REMOTE CLOCK CONN.	OFF	OFF	ON



PWA MIPI

PWA MIPI

001



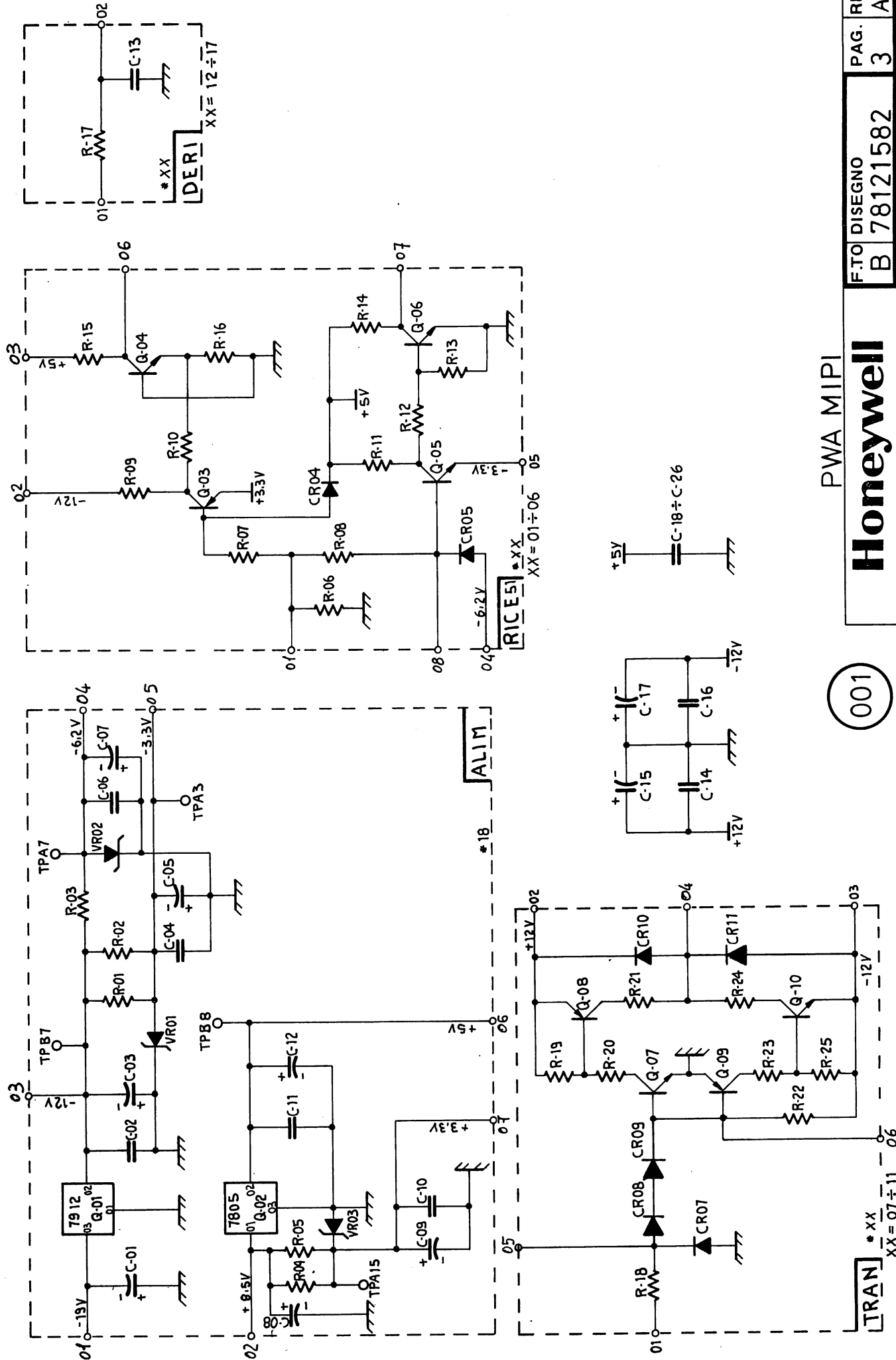
Δ1. TAGLIARE PIH N° 18

Δ1. CUT PIH # 18

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PWA MIPI
Honeywell

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F.T.O. DISEGNO B 78121582

PAG. REV 3 AA

[TRAN] * XX
 XX = 07 ÷ 11

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. L'USO INTERNO, OGNI E QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

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REVISIONI			
REV.	NUMERO C.O.	DATA	FIRMA
		A M G	

J-40 DRIVE		
PIN	SIGNAL	FUNCTION
1	KEY	
2	DBR1 * 1	
3	DBR3 * 1	
4	PRO * 00	
5	DBR0 * 1	
6	DBR6 * 1	
7	DBR2 * 1	
8	DBR4 * 1	
9	DBR5 * 1	
10	+5V	
11	+19V	
12	DS06 I * 0	
13	DBR7 * 1	
14	IMPPA * 01	
15	RESET * 0	
16	ZERØ * 1	
17	DSEBØ * 0	
18	COHC * 1	TEST POINT

J-41 (CPU)		
PIN	SIGNAL	FUNCTION
1	KEY	
2	-12V	
3	LRD * 00	
4	LCALI * 1	
5	LSTD * 0	
6	LSCTS * 10	
7	LSRTS * 1	
8	LDTR * 1	
9	LRTS * 1	
10	LDSR * 1	
11	LTD * 0	
12	GROUND	
13	LSRD * 00	
14	+12V	
15	DASTC * 1	
16	LCTS * 10	CONT.
17	LRELI * 1	
18	RD02 * 1	TEST POINT

SEMICONDUITORI SEMICONDUCTORS	
SIMBOLO SYMBOL	RIF.
U-09	2
U-08 U-05 U-03 U-10	3
U-11 U-12 U-19	4
U-04 U-14	5
U-13	6
U-15	7
U-18	8
U-16	9
U-20	10
U-17	11
U-01	12
U-07	13
U-06	14
U-02	15
VR01 ÷ VR04	16

CONDENSATORI CAPACITORS	
SIMBOLO SYMBOL	RIF.
C-01 ÷ C-04 C-37	17
C-05 C-10 C-11	18
C-06 C-38 C-39 C-40	19
C-07 C-41 C-42	20
C-08 C-09	21
C-12 ÷ C-15 C-35	22
C-16 ÷ C-27	23
C-29 ÷ C-34 C-36	23
C-28	24

RESISTORI RESISTORS	
SIMBOLO SYMBOL	RIF.
R-02	25
	26
R-03	27
R-04	28
R-05 R-09 R-07	29
R-06 R-08	30
R-10	31
R-01	37

VARI OTHERS	
SIMBOLO SYMBOL	RIF.
J-40 ÷ J-43	32
Y-01	33
So1-So2	35

J-42 CAVO (2ND) INT. DCL		
PIN	SIGNAL	FUNCTION
1	RDC11 * 1	
2	RDC12 * 1	
3	RDC22 * 1	
4	RDC21 * 1	
5	DCL11 * 1	
6	CØLL2 * 000	
7	RD06 * 1	
8	ZERØ * 1	
9	DRESE * 01	TEST POINT
10	DREQ * 0	
11	MR2 * 1	
12	RD07 * 1	
13	RD03 * 1	
14	DCL12 * 1	
15	DCL21 * 1	
16	DCL22 * 1	
17	ZERØ * 1	
18	KEY	

J-43 CAVO BASE INT. DCL		
PIN	SIGNAL	FUNCTION
1	RDC11 * 1	
2	RDC12 * 1	
3	RDC22 * 1	
4	RDC21 * 1	
5	DCL11 * 1	
6	RD05 * 1	
7	RD01 * 1	
8	ZERØ * 1	
9	PAER * 1	TEST POINT
10	DATAY * 1	
11	CØLL1 * 100	
12	RD04 * 1	
13	RD08 * 1	
14	DCL12 * 1	
15	DCL21 * 1	
16	DCL22 * 1	
17	ZERØ * 1	
18	KEY	

- △ 3. CUT PIN # 1
- △ 2. CUT PIN # 18
- △ 1. CUT PIN # 1

- △ 3. TAGLIARE PIN 1
- △ 2. TAGLIARE PIN 18
- △ 1. TAGLIARE PIN 1

FOR DOCUMENT STATUS SEE REVISION STATUS SHEET

PER IL LIVELLO DI MODIFICA VEDI FOGLIO REVISIONE

SALVO INDICAZIONE CONTRARIA MAT.	
DIMENSIONI: \leftarrow MILLIMETRI \rightarrow MCR	TT.
TOLLERANZE DIMENS. E DI	FM.
FORMA VEDI:	
QUOTE IN	
PROIEZIONE	
SCALA	COD.
	DISEGNATO 78-APR-26 E. GUALANDI
	APPROVATO

Honeywell Honeywell Information Systems Italia LOC. PREGNANA MILANESE, ITALIA			
DESCRIZIONE PWA DCL			
F.TO	DISEGNO	PAG.	REV.
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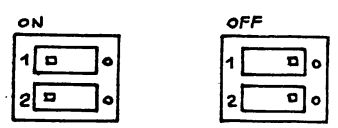
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0 10 20 30 40 50 60 70 80 90 100 110 120 130 140 150 160 170 180 190 200 210 220 230 240 250 260 270 280 290 300 310 320 330 340 350 360 370 380 390 400 410 420 430 440 450 460 470 480 490 500 510 520 530 540 550 560 570 580 590 600 610 620 630 640 650 660 670 680 690 700 710 720 730 740 750 760 770 780 790 800 810 820 830 840 850 860 870 880 890 900 910 920 930 940 950 960 970 980 990 1000

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

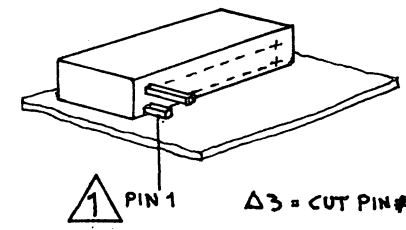
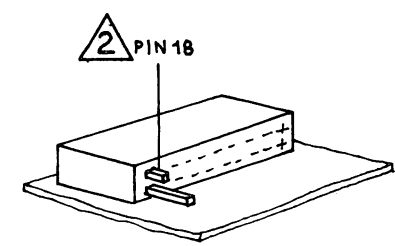
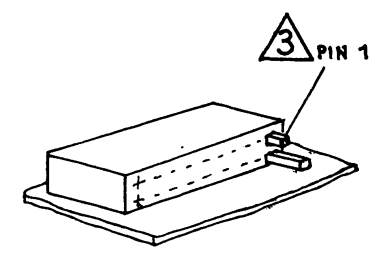
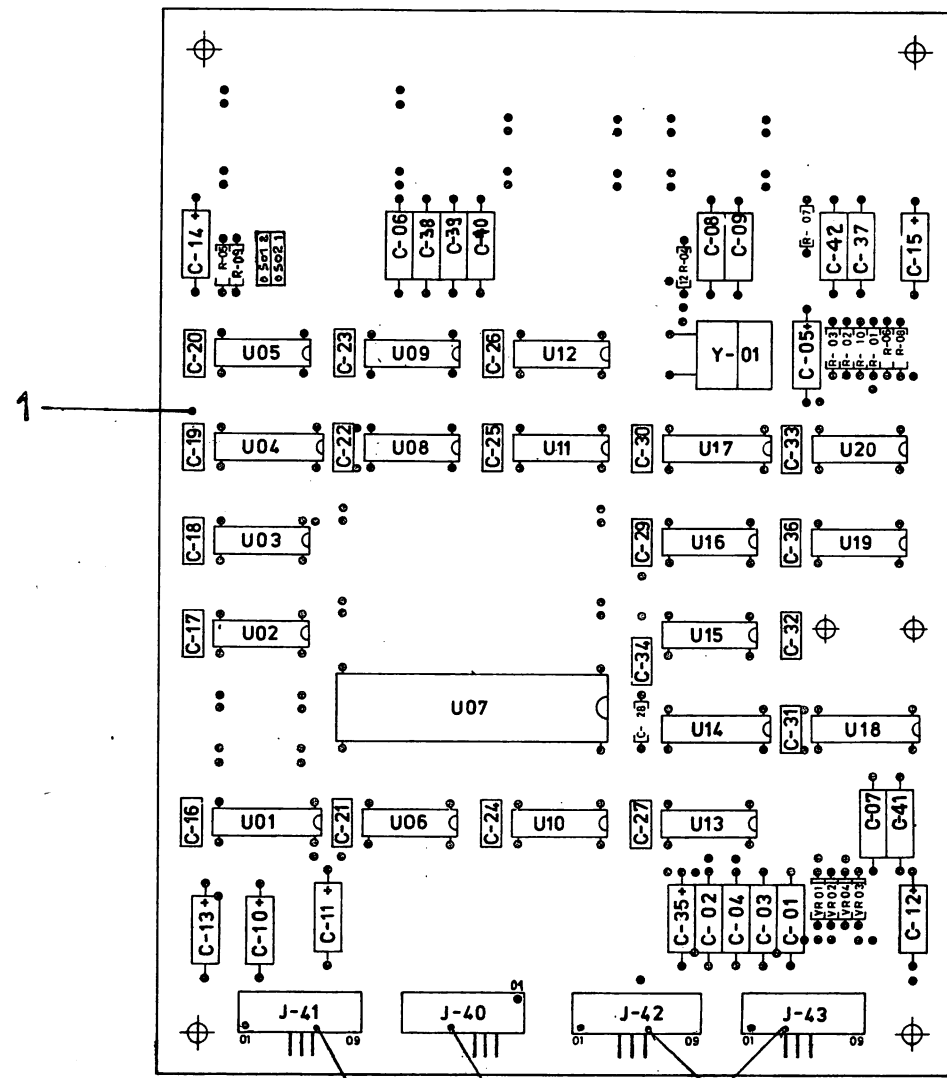
RIF. SPEC. N°

STRAPPING OPTIONS

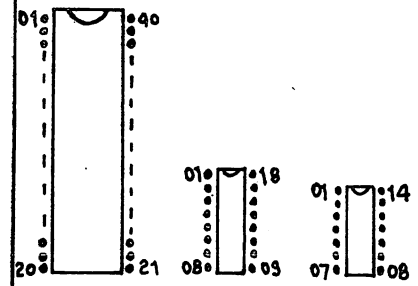


S01 - S02

SWITCHETS SET		
MODELS	S01	S02
ROSY21	ON	OFF
ROSY B2	OFF	ON



Δ3 = CUT PIN# 1
 Δ2 = CUT PIN# 18
 Δ1 = CUT PIN# 1



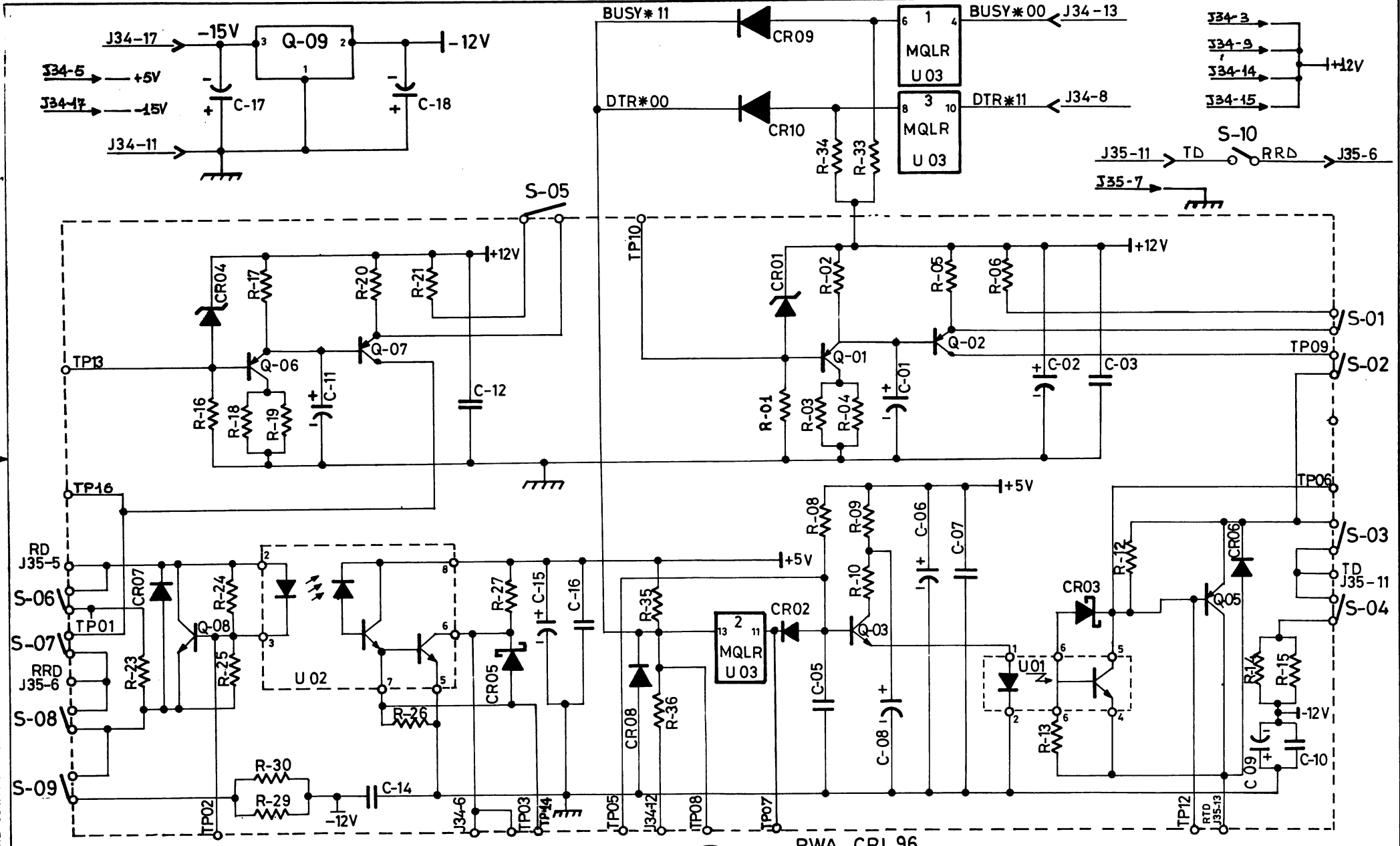
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2 PWA DCL

Honeywell

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B 78 123 85 1



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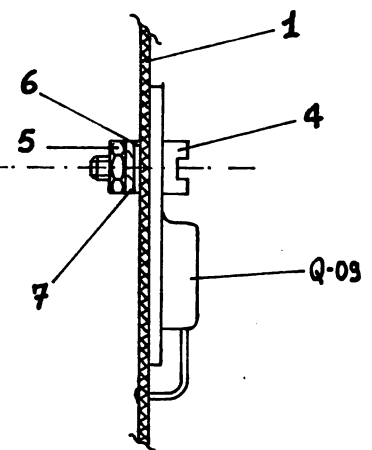
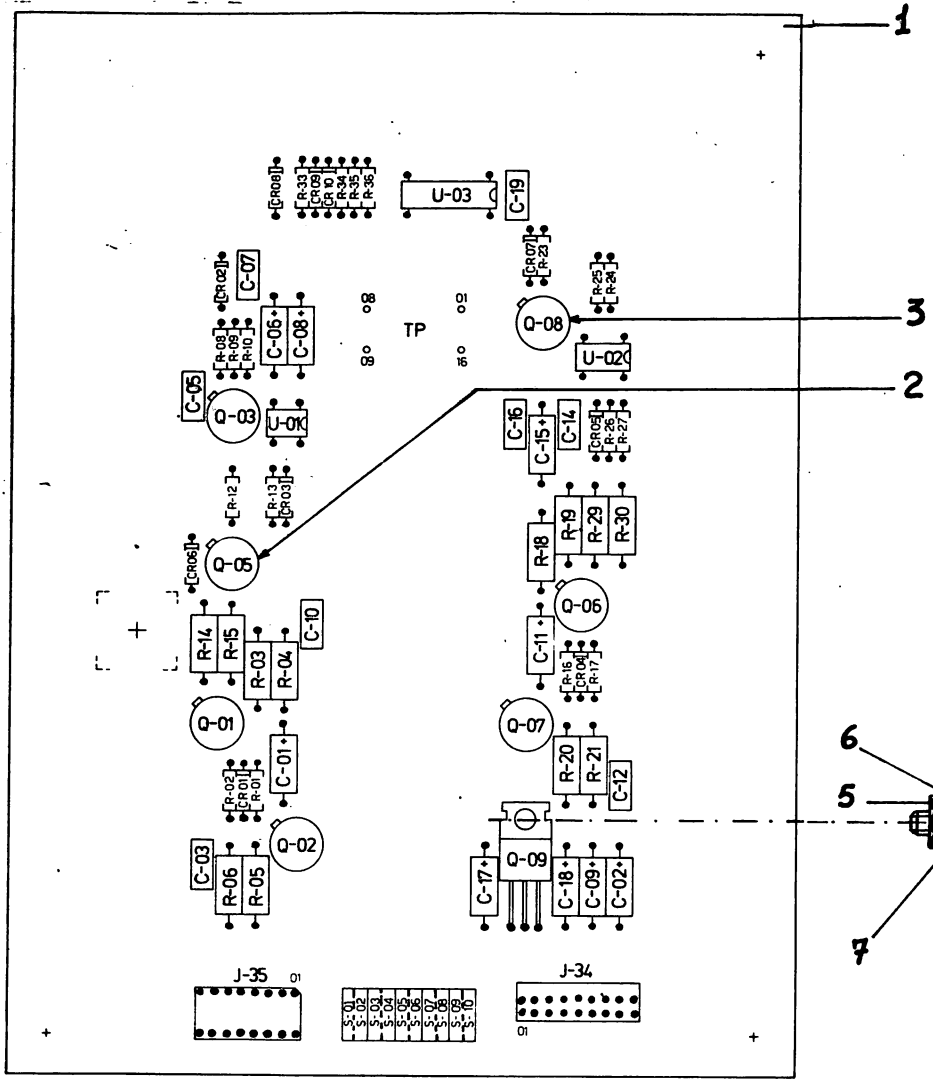
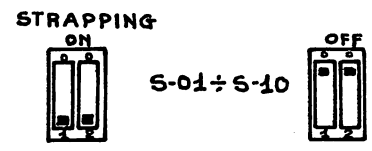
PWA CRL 96

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F.T.O	DISEGNO	PAG.	REV
B	78 123 85 1	7	FA

QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETA' DELLA HONEYWELL
 INFORMATION SYSTEMS ITALIA. ESSO E' DA CONSIDERARE COME DOCUMENTO DI
 USO INTERNO. OGNI E' QUALSIASI DISTRIBUZIONE A TERZI E' VIETATA SALVO
 APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

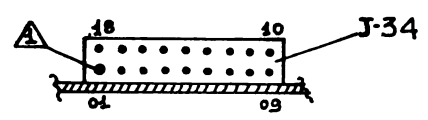
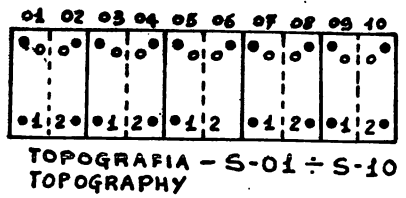
CRL 96 BOARD - SWITCH SETTING		
S-01	ON OFF	MUST BE OFF
S-02	ON OFF	ACTIVE PASSIVE
S-03	ON OFF	PASSIVE ACTIVE
S-04	ON OFF	ACTIVE PASSIVE
S-05	ON OFF	MUST BE OFF
S-06	ON OFF	MUST BE OFF
S-07	ON OFF	ACTIVE PASSIVE
S-08	ON OFF	PASSIVE ACTIVE
S-09	ON OFF	ACTIVE PASSIVE
S-10	ON OFF	OPT INCOTERM (GTWF8413) " 4 FILI (GTWF8446)



J 34 (CP28B)		
PIN	SIGNAL	FUNCTION
1		
2		
3	+12V	
4	+5V	
5	OUT PUT	RECEIVED DATA
6		RECEIVED DATA
7		DATA TERM. READY.
8	DTR # 11	
9	+12V	
10		
11	ZERφ0 #1	
12	INPUT	TRASH. DATA
13	BUSY #00	BUSY SYNCR.
14	+12V	
15	+12V	
16		
17	-15V	
18		

J 35 (MOD. INTERF)		
PIN	SIGNAL	FUNCTION
1		
2		
3		
4		
5	RD	RECEIVED DATA
6	RRD	RECEIVED DATA RETURN
7	ZERφ0 #1	
8		
9		
10		
11	TD	TRANSMIT. DATA
12		
13	RTD	TRANSMITTED DATA RETURN
14		
15		
16		

PWA CRL 96



Δ1-CUT THE PIN 01
NOTES:

Δ1-TAGLIARE IL PIN 0
NOTE:

PWA-CRL 96

004

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CONNETTORI PWA CPU 28 B-Ø
CONNECTORS PWA CPU 28 B-Ø

J01 (TEST CONNECTOR)		
PIN	SIGNAL	
1	MEMR	* 0
2	HOLD	* 1
3	RESET	* 0
4	HLDA	* 1
5	AB14	* 12
6	INTA	* 0
7	AB13	* 12
8	EXREA	* 1
9	AB12	* 12
10	ENBUS	* 1
11	AB15	* 12
12	EXCLØ	* 1
13	EINT	* 1
14	MEMW	* 0
15	STRØ	* 0
16	GROUND	
17	BUSEN	* 0
18	IØW	* 0
19	AB10	* 12
20	AB11	* 12
21	AB9	* 12
22	AB8	* 12
23	IØR	* 0
24	DS00Ø	* 0
25	DB6	* 1
26	DB7	* 1
27	DB5	* 1
28	DB4	* 1
29	DB3	* 1
30	DB2	* 1
31	DB1	* 1
32	DB0	* 1
33	AB4	* 12
34	AB7	* 12
35	AB6	* 12
36	AB5	* 12
37	AB3	* 12
38	AB2	* 12
39	AB1	* 12
40	AB0	* 12

J02 (KEYBOARD)		
PIN	SIGNAL	
1	-12V	
2	KB7	* 0
3	KB6	* 0
4	GROUND	
5	GROUND	
6	REPEA	* 0
7	CTR	* 1
8	STRØB	* 0
9	KB3	* 0
10	KB4	* 0
11	PR7	* 1
12	+ 5V	
13	KB5	* 0
14	KB1	* 0
15	KB2	* 0
16	KB0	* 0

J03 (OPERATOR PANEL)		
PIN	SIGNAL	
1	+ 5V	
2	DS05Ø	* 0
3	GROUND	
4	F12TT	* 1
5	PR5L	* 1
6	BREAK	* 10
7	F.F.	* 10
8	SWLOL	* 0
9	DS06Ø	* 0
10	START/STOP	* 10
11	L.F.	* 10
12	PAPERØ	* 1
13	STABY	* 1
14	READY	* 1
15	LØCAL	* 1
16	WAIT	* 1
17	KEYBO	* 1
18	GROUND	

J05 (ASYNCHRONOUS MODEM INTERFACE)		
PIN	SIGNAL	
1	-12V	
2	LDCD	* 10
3	LCALI	* 10
4	+ 5V	
5	LRD	* 00
6	LRTS	* 1
7	LDTR	* 1
8	+12V	
9	LBREK	* 02
10	GROUND	
11	LTD	* 0
12	BUSY	* 0
13	LCTS	* 10
14	LDSR	* 10
15	LSDCD	* 10
16	-15V	

J06 (DRIVE)		
PIN	SIGNAL	
1	SØNAL	* 1
2		
3	AB11	* 03
4	FIVIS	* 1
5	IØWR	* 0
6	PICAL1	* 1
7	PR3	* 0
8	AB13	* 12
9	AB12	* 12
10	AB14	* 12
11	PR6	* 1
12	PAPEN	* 1
13	SØL	* 00
14	PICAL2	* 1
15	AB15	* 12
16	PRO	* 1
17	100 KH	* 0
18	PR5	* 1

J07 (DRIVE)		
PIN	SIGNAL	
1	-12V	
2	RESET	* 0
3	-15V	
4	DBR7	* 1
5	DBR3	* 1
6	DBR1	* 1
7	DBR0	* 1
8	DBR2	* 1
9	+12V	
10	+12V	
11	DBR6	* 1
12	DBR5	* 1
13	DS06I	* 0
14	ØRDS	* 0
15	DBR4	* 1
16	DS05I	* 0
17	MR1	* 1
18	-5V	

J18 (O.P. AUSILIARY)		
PIN	SIGNAL	
1	+5V	
2	SET PAR	* 10
3	BRG2	* 0
4	BRG1	* 0
5	BRG0	* 0
6	GROUND	
7	WSPØ	* 0
8	SWA01	* 0
9	SWA02	* 0
10	SWA03	* 0
11	SWA04	* 0
12	SWA05	* 0
13	SWA06	* 0
14	SWA07	* 0
15	SWA08	* 0
16	ABCLE	* 0

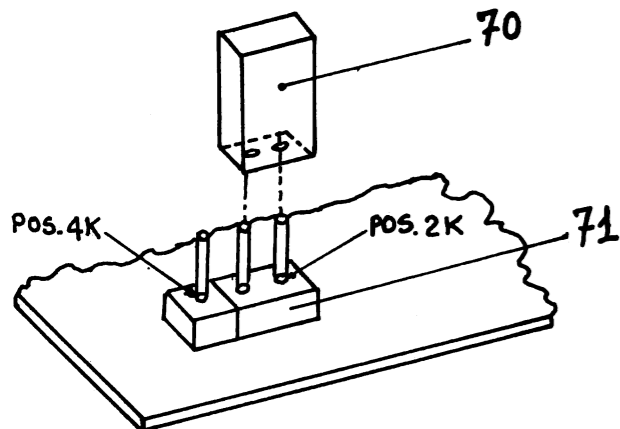
J19 (POWER SUPPLY)		
PIN	SIGNAL	
1	+5V	
2	GROUND	

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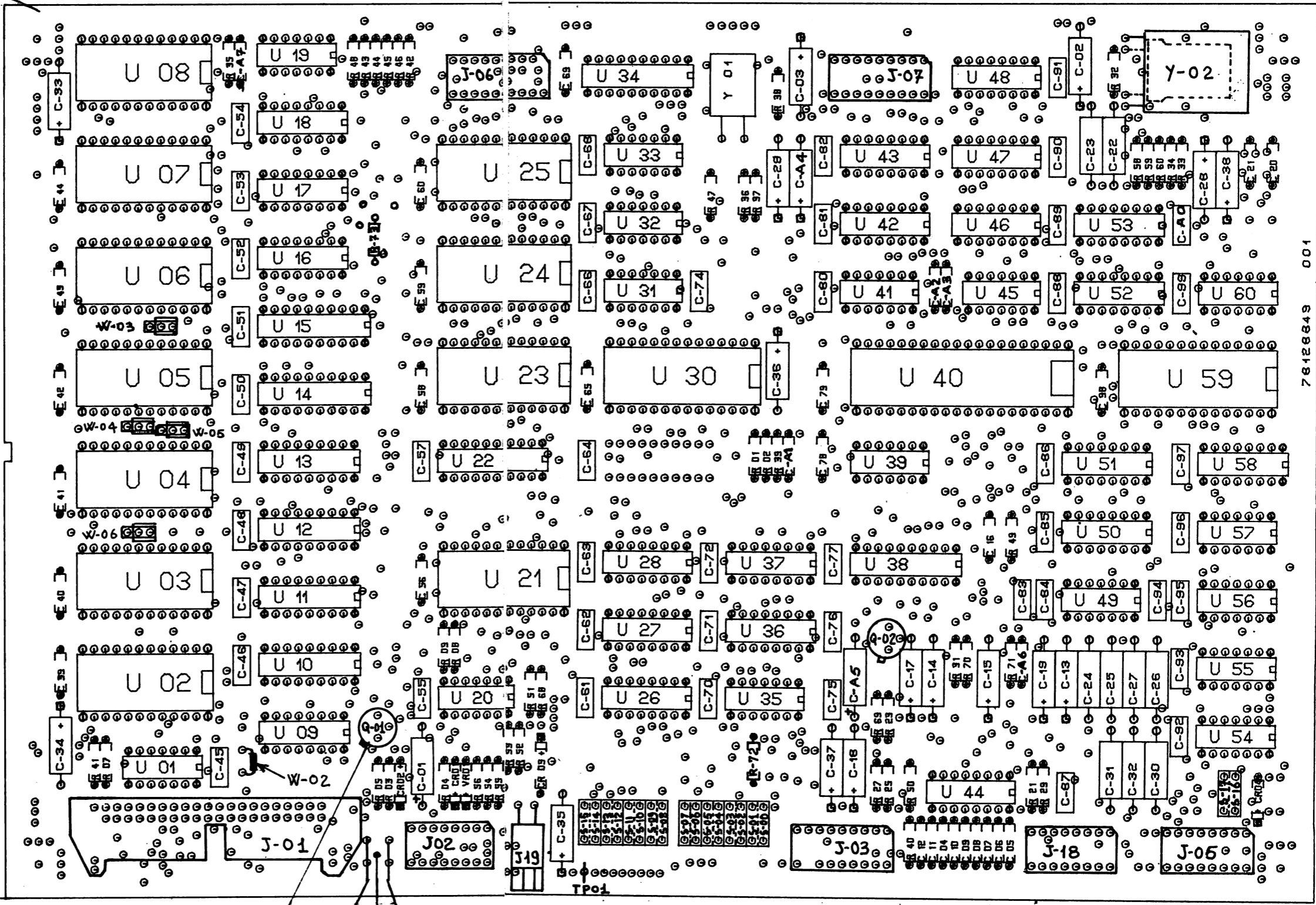
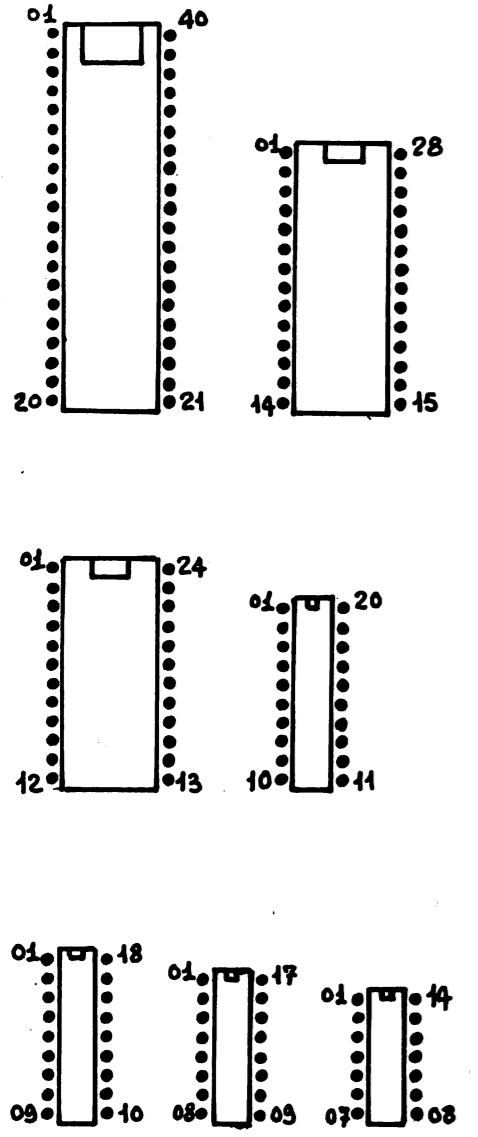
QUESTO DOCUMENTO CONTIENE INFORMAZIONI DI PROPRIETÀ DELLA HONEYWELL INFORMATION SYSTEMS ITALIA. ESSO È DA CONSIDERARE COME DOCUMENTO DI USO INTERNO. OGNI E QUALSIASI DISTRIBUZIONE A TERZI È VIETATA SALVO APPROVAZIONE SCRITTA DELLA HONEYWELL INFORMATION SYSTEMS ITALIA.

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PARTICOLARE INSERIZIONE W-03÷W-06



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PWA-CP280

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CONNETTORI PWA CPU 28 B-Ø
CONNECTORS PWA CPU 28 B-Ø

J01 (TEST CONNECTOR)		
PIN	SIGNAL	
1	MEMR	* 0
2	HØLD	* 1
3	RESET	* 0
4	HLDA	* 1
5	AB14	* 12
6	INTA	* 0
7	AB13	* 12
8	EXREA	* 1
9	AB12	* 12
10	ENBUS	* 1
11	AB15	* 12
12	EXCLØ	* 1
13	EINT	* 1
14	MEMW	* 0
15	STRØ	* 0
16	GROUND	
17	BUSEN	* 0
18	IØW	* 0
19	AB10	* 12
20	AB11	* 12
21	AB9	* 12
22	AB8	* 12
23	IØR	* 0
24	DS00Ø	* 0
25	DB6	* 1
26	DB7	* 1
27	DB5	* 1
28	DB4	* 1
29	DB3	* 1
30	DB2	* 1
31	DB1	* 1
32	DB0	* 1
33	AB4	* 12
34	AB7	* 12
35	AB6	* 12
36	AB5	* 12
37	AB3	* 12
38	AB2	* 12
39	AB1	* 12
40	AB0	* 12

J02 (KEYBOARD)		
PIN	SIGNAL	
1	-12V	
2	KB7	* 0
3	KB6	* 0
4	GROUND	
5	GROUND	
6	REPEA	* 0
7	CTR	* 1
8	STRØB	* 0
9	KB3	* 0
10	KB4	* 0
11	PR7	* 1
12	+5V	
13	KB5	* 0
14	KB1	* 0
15	KB2	* 0
16	KB0	* 0

J03 (OPERATOR PANEL)		
PIN	SIGNAL	
1	+5V	
2	DS05Ø	* 0
3	GROUND	
4	F12TT	* 1
5	PR5L	* 1
6	BREAK	* 10
7	F.F.	* 10
8	SWLOL	* 0
9	DS06Ø	* 0
10	START/STOP	* 10
11	L.F.	* 10
12	PAPERØ	* 1
13	STABY	* 1
14	READY	* 1
15	LØCAL	* 1
16	WAIT	* 1
17	KEYBO	* 1
18	GROUND	

J05 (ASYNCHRONOUS MODEM INTERFACE)		
PIN	SIGNAL	
1	-12V	
2	LDCD	* 10
3	LCALI	* 10
4	+5V	
5	LRD	* 00
6	LRTS	* 1
7	LDTR	* 1
8	+12V	
9	LBREK	* 02
10	GROUND	
11	LTD	* 0
12	BUSY	* 0
13	LCTS	* 10
14	LDSR	* 10
15	LSDCD	* 10
16	-15V	

J06 (DRIVE)		
PIN	SIGNAL	
1	SØNAL	* 1
2		
3	AB11	* 03
4	FIVIS	* 1
5	IØWR	* 0
6	PICAL1	* 1
7	PR3	* 0
8	AB13	* 12
9	AB12	* 12
10	AB14	* 12
11	PR6	* 1
12	PAPEN	* 1
13	SØL	* 00
14	PICAL2	* 1
15	AB15	* 12
16	PRO	* 1
17	100 KH	* 0
18	PR5	* 1

J07 (DRIVE)		
PIN	SIGNAL	
1	-12V	
2	RESET	* 0
3	-15V	
4	DBR7	* 1
5	DBR3	* 1
6	DBR1	* 1
7	DBR0	* 1
8	DBR2	* 1
9	+12V	
10	+12V	
11	DBR6	* 1
12	DBR5	* 1
13	DS06I	* 0
14	ØRDS	* 0
15	DBR4	* 1
16	DS05I	* 0
17	MR1	* 1
18	-5V	

J18 (O.P. AUSILIARY)		
PIN	SIGNAL	
1	+5V	
2	SET PAR	* 10
3	BRG2	* 0
4	BRG1	* 0
5	BRG0	* 0
6	GROUND	
7	WSPØ	* 0
8	SWA01	* 0
9	SWA02	* 0
10	SWA03	* 0
11	SWA04	* 0
12	SWA05	* 0
13	SWA06	* 0
14	SWA07	* 0
15	SWA08	* 0
16	ABCLE	* 0

J19 (POWER SUPPLY)		
PIN	SIGNAL	
1	+5V	
2	GROUND	

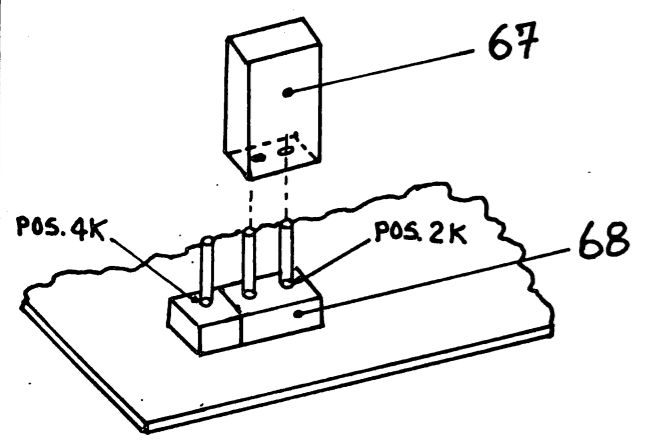
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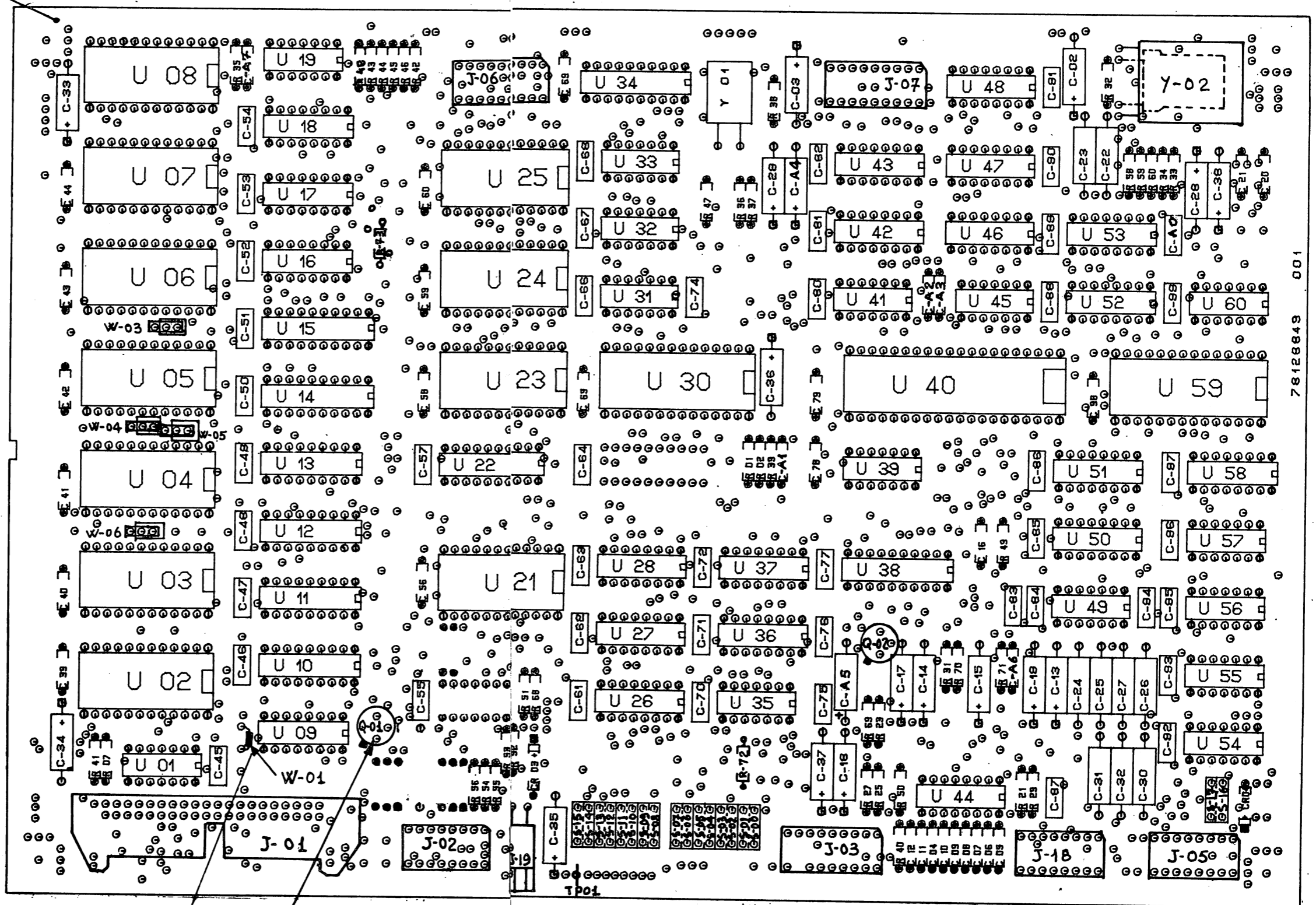
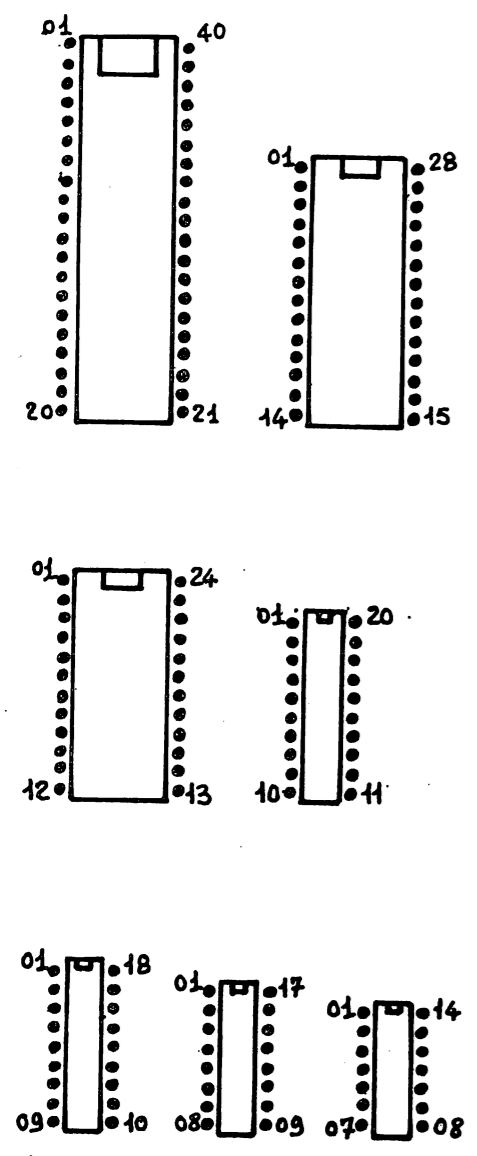
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PARTICOLARE INSERIZIONE W-03÷W-06



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